Contents

1	Electrical ratings	3
2	Electrical characteristics	4
	2.1 Electrical characteristics (curves)	6
3	Test circuits	8
4	Package information	9
	4.1 TO-220FP package information	0
	4.2 I ² PAK (TO-281) package information	2
5	Revision history	4



1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{GS}	Gate-source voltage	± 25	V
I _D	Drain current (continuous) at T _C = 25 °C	22 ⁽¹⁾	Α
I _D	Drain current (continuous) at T _C = 100 °C	14 ⁽¹⁾	Α
I _{DM} ⁽²⁾	Drain current (pulsed)	88 ⁽¹⁾	Α
P _{TOT}	Total dissipation at T _C = 25 °C	30	W
dv/dt (3)	Peak diode recovery voltage slope	15	V/ns
dv/dt ⁽⁴⁾	MOSFET dv/dt ruggedness	50	V/ns
V _{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink (t = 1 s; $T_C = 25$ °C)	2500	V
T _{stg}	Storage temperature - 55 to 150		°C
T _j	Operating junction temperature	- 55 10 150	

- 1. Limited by maximum junction temperature.
- 2. Pulse width limited by safe operating area.
- 3. $I_{SD} \le 22$ A, di/dt ≤ 400 A/ μ s; $V_{DS\ peak} < V_{(BR)DSS}$, $V_{DD} = 400$ V.
- $4. \quad V_{DS} \leq 480 \ V$

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case max	4.17	°C/W
R _{thj-amb}	Thermal resistance junction-ambient max	62.5	°C/W

Table 4. Avalanche characteristics

Symbol	Parameter	Value	Unit
I _{AR}	Avalanche current, repetitive or not repetitive (pulse width limited by T_{jmax})	3.6	Α
E _{AS}	Single pulse avalanche energy (starting T_j =25°C, I_D = I_{AR} ; V_{DD} =50V)	350	mJ

2 Electrical characteristics

(T_C = 25 °C unless otherwise specified)

Table 5. On /off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	$I_D = 1 \text{ mA}, V_{GS} = 0$	600			V
1	Zero gate voltage	V _{DS} = 600 V			1	μΑ
l _{DSS} c	drain current (V _{GS} = 0)	V _{DS} = 600 V, T _C =125 °C			100	μΑ
I _{GSS}	Gate-body leakage current (V _{DS} = 0)	V _{GS} = ± 25 V			±10	μΑ
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	3	4	V
R _{DS(on)}	Static drain-source on-resistance	V _{GS} = 10 V, I _D = 11 A		0.135	0.150	Ω

Table 6. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{iss}	Input capacitance		-	1440	-	pF
C _{oss}	Output capacitance	V _{DS} = 100 V, f = 1 MHz,	1	70	-	pF
C _{rss}	Reverse transfer capacitance	$V_{GS} = 0$	-	2	-	pF
Coss eq. (1)	Equivalent output capacitance	$V_{DS} = 0$ to 480 V, $V_{GS} = 0$	-	104	-	pF
R _G	Intrinsic gate resistance	f = 1 MHz open drain	-	5.5	-	Ω
Qg	Total gate charge	V _{DD} = 480 V, I _D = 22 A,	-	36	-	nC
Q _{gs}	Gate-source charge	V _{GS} = 10 V	-	7.2	-	nC
Q _{gd}	Gate-drain charge	(see Figure 15)	-	16	-	nC

^{1.} $C_{oss\ eq.}$ is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS}

Table 7. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time		-	14.5	-	ns
t _r	Rise time	$V_{DD} = 300 \text{ V}, I_D = 11 \text{ A},$ $R_G = 4.7 \Omega, V_{GS} = 10 \text{ V}$	-	7.2	-	ns
t _{d(off)}	Turn-off delay time	(see <i>Figure 14</i> and <i>Figure 19</i>)	-	100	-	ns
t _f	Fall time		-	8	-	ns

4/15 DocID025255 Rev 3



Table 8. Source drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{SD}	Source-drain current		-		22	Α
I _{SDM} ⁽¹⁾	Source-drain current (pulsed)		-		88	Α
V _{SD} (2)	Forward on voltage	I _{SD} = 22 A, V _{GS} = 0	-		1.6	V
t _{rr}	Reverse recovery time		-	350		ns
Q _{rr}	Reverse recovery charge	I _{SD} = 22 A, di/dt = 100 A/μs V _{DD} = 60 V (see <i>Figure 19</i>)	-	4.7		μC
I _{RRM}	Reverse recovery current	T VDD = 00 V (000 Figure 70)	-	27		Α
t _{rr}	Reverse recovery time	I _{SD} = 22 A, di/dt = 100 A/μs	-	451		ns
Q _{rr}	Reverse recovery charge	$V_{DD} = 60 \text{ V}, T_j = 150 ^{\circ}\text{C}$	-	6.5		μC
I _{RRM}	Reverse recovery current	(see Figure 19)	-	29		Α

^{1.} Pulse width limited by safe operating area.



^{2.} Pulsed: pulse duration = $300 \mu s$, duty cycle 1.5%

Electrical characteristics (curves) 2.1

Figure 2. Safe operating area

AM17988v1 (A) 10 10µs 100µs 1ms 10ms 0.1 Tj=150°C Tc=25°C Single pulse 100 10 VDS(V)

Figure 3. Thermal impedance

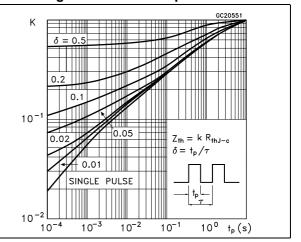


Figure 4. Output characteristics

AM17989v1 ID(A) Vgs=7, 8, 9, 10V 50 6V 40 30 5V 20

15

4V

V_{DS}(V)

Figure 5. Transfer characteristics

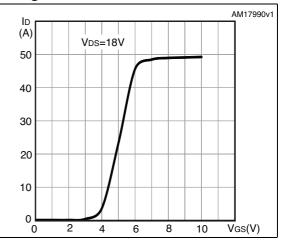


Figure 6. Gate charge vs gate-source voltage

10

10

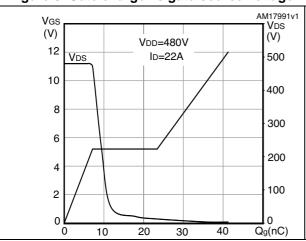
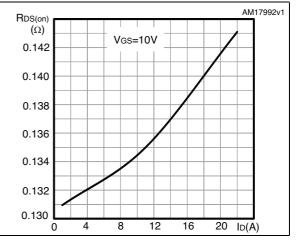


Figure 7. Static drain-source on-resistance



DocID025255 Rev 3 6/15

0.1

Figure 8. Capacitance variations

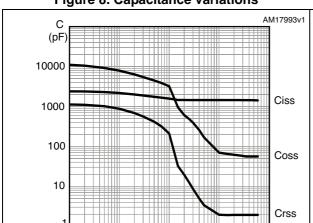


Figure 9. Output capacitance stored energy

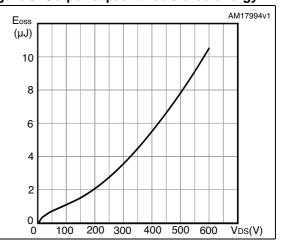


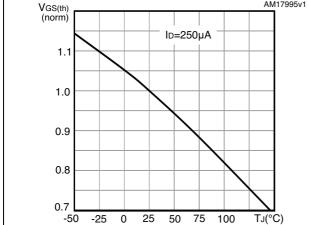
Figure 10. Normalized gate threshold voltage vs temperature

10

100

V_{DS}(V)

Figure 11. Normalized on-resistance vs temperature AM17995v1 RDS(on) (norm) ID=11A ID=250µA



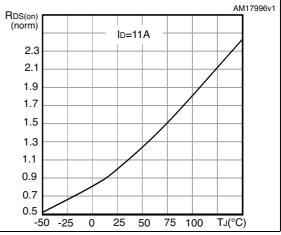
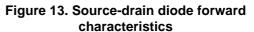
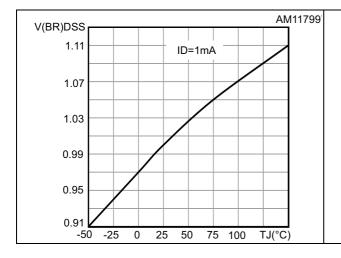
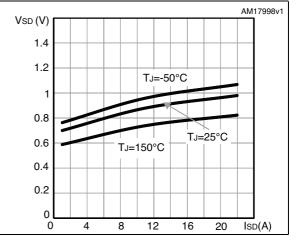


Figure 12. Normalized V_{(BR)DSS} vs temperature







3 Test circuits

Figure 14. Switching times test circuit for resistive load

Figure 15. Gate charge test circuit

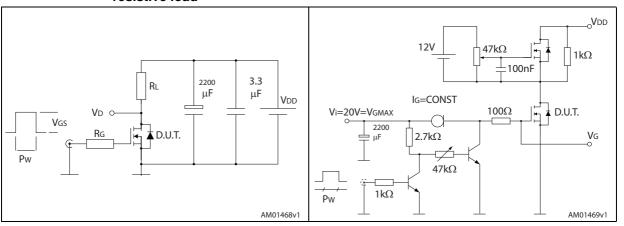


Figure 16. Test circuit for inductive load switching and diode recovery times

Figure 17. Unclamped inductive load test circuit

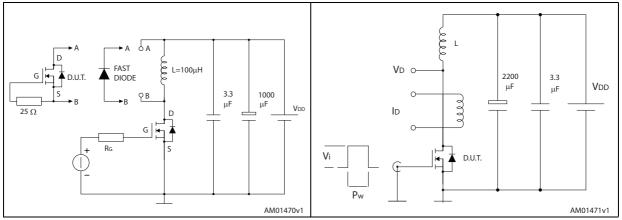
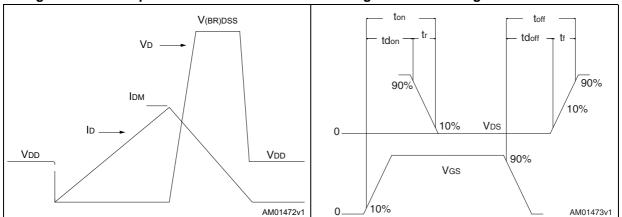


Figure 18. Unclamped inductive waveform

Figure 19. Switching time waveform



57/

8/15

4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.



4.1 TO-220FP package information

-В., Dia L6 *L2 L7* L3 L4 F2 Ε

Figure 20. TO-220FP outline

57

7012510_Rev_K_B

Table 9. TO-220FP mechanical data

Table 5. 10-2201 F mechanical data			
Dim.		mm	
	Min.	Тур.	Max.
А	4.4		4.6
В	2.5		2.7
D	2.5		2.75
Е	0.45		0.7
F	0.75		1
F1	1.15		1.70
F2	1.15		1.70
G	4.95		5.2
G1	2.4		2.7
Н	10		10.4
L2		16	
L3	28.6		30.6
L4	9.8		10.6
L5	2.9		3.6
L6	15.9		16.4
L7	9		9.3
Dia	3		3.2

4.2 I²PAK (TO-281) package information

Η В D1 -97 D -F1 (x3) F (x3) Ε G 8291506 Rev. C

Figure 21. I²PAKFP (TO-281) outline

577

Table 10. I²PAKFP (TO-281) mechanical data

D *		mm	
Dim.	Min.	Тур.	Max.
А	4.40	-	4.60
В	2.50		2.70
D	2.50		2.75
D1	0.65		0.85
Е	0.45		0.70
F	0.75		1.00
F1			1.20
G	4.95		5.20
Н	10.00		10.40
L1	21.00		23.00
L2	13.20		14.10
L3	10.55		10.85
L4	2.70		3.20
L5	0.85		1.25
L6	7.50	7.60	7.70



5 Revision history

Table 11. Document revision history

Date	Revision	Changes
13-Sep-2013	1	First release.
29-Jan-2014	2	 Added: I²PAKFP package Modified: title, I_D value and features in cover page Modified: I_D, I_{DM} and P_{TOT} values in <i>Table 2</i> Modified: note 3 Modified: R_{thj-case} value in <i>Table 3</i> Modified: the entire typical values in <i>Table 4</i>, 6, 7 and 8 Modified: R_{DS(on)} typical value Modified: <i>Figure 7</i> and 8 Updated: <i>Table 9</i> and <i>Figure 14</i> Added: <i>Section 4: Package information</i> Minor text changes
13-Feb-2015	3	 Updated title, description and features in cover page. Updated Table 2.: Absolute maximum ratings and Table 4.: Avalanche characteristics. Updated Figure 12.: Normalized V_{(BR)DSS} vs temperature. Updated 4: Package information. Minor text changes.



14/15 DocID025255 Rev 3

IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2015 STMicroelectronics - All rights reserved



DocID025255 Rev 3

15/15