PAD/JPAD/SSTPAD Series

Vishay Siliconix



ABSOLUTE MAXIMUM RATINGS^a

Forward Current:	(PAD	50 mA	
	(JPAD/SSTPAD)	10 mA	
Total Device Dissipation:	(PAD) ^b	300 mW	
	(JPAD/SSTPAD)b	350 mW	
Operation Junction Temp:	(PAD)	–55 to 175°C	
	(JPAD/SSTPAD) ^c	–55 to 150°C	
Lead Temperature (1/16" from case for 10 sec.)			

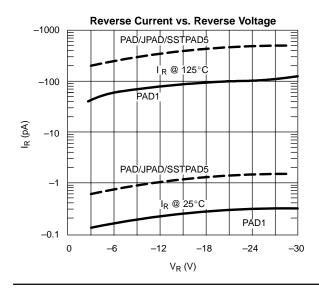
Notes:

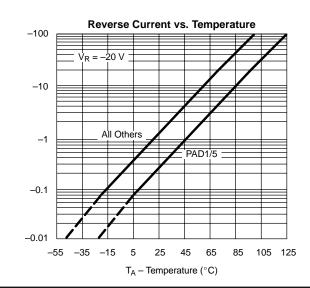
- $T_A = 25$ °C unless otherwise noted. Derate 2 mW/°C above 25 °C. Derate 2.8 mW/°C above 25 °C. a.

SPECIFICATIONS SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)									
		Test Conditions		Limits					
Parameter	Symbol			Min	Typ ^a	Max	Unit		
Static									
Reverse Current I _R		V _R = -20 V	PAD1		-0.3	-1	pA		
			PAD5/JPAD5/SSTPAD5		-1	-5			
	I _R		PAD50/JPAD50		-5	-50			
			SSTPAD100		-10	-100			
Reverse Breakdown Voltage BV _R			PAD1/PAD5	-45	-60				
	$I_R = -1 \mu A$	SSTPAD5/100	-30	-55		V			
		All Others	-35	-55					
Forward Voltage Drop	V _F	I _F = 1 mA			0.8	1.5			
Dynamic				-		-	-		
Reverse Capacitance C _R	C-	V _R = −5V, f = 1 MHz	PAD1/PAD5		0.5	0.8	pF		
	c_R		All Others		1.5	2			

Notes:

TYPICAL CHARACTERISTICS (TA = 25°C UNLESS OTHERWISE NOTED)





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a. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.



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