PACKAGE/ORDERING INFORMATION

MODEL	V _{OUT} (V)	PIN- PACKAGE	ORDERING NUMBER	PACKAGE MARKING	PACKAGE OPTION
SGM2035C-3.3	3.3V	UTDFN-1.6×1.6-6L	SGM2035C-3.3YUDN6G/TR	нвх	Tape and Reel, 3000
SGM2035C-3.3	3.3V	TDFN-2×2-6L	SGM2035C-3.3YTDI6G/TR	SHA XXXX	Tape and Reel, 3000
SGM2035C-3.0	3.0V	UTDFN-1.6×1.6-6L	SGM2035C-3.0YUDN6G/TR	TEX	Tape and Reel, 3000
SGM2035C-2.8	2.8V	UTDFN-1.6×1.6-6L	SGM2035C-2.8YUDN6G/TR	S0X	Tape and Reel, 3000
SGM2035C-ADJ	ADJ	UTDFN-1.6×1.6-6L	SGM2035C-ADJYUDN6G/TR	M7X	Tape and Reel, 3000

NOTE: X = Date Code, XXXX = Date Code.

ABSOLUTE MAXIMUM RATINGS

IN to GND Output Short-Circuit Duration	
EN to GND	0.3V to (V _{IN} + 0.3V)
OUT, BP/FB to GND	$-0.3V$ to $(V_{IN} + 0.3V)$
Power Dissipation, $P_D @ T_A = 25^{\circ}C$	
TDFN-2×2-6L	
UTDFN-1.6×1.6-6L	0.88W
Package Thermal Resistance	
TDFN-2×2-6L,θ _{JA}	138°C/W
UTDFN-1.6×1.6-6L, θ_{JA}	142°C/W
Operating Temperature Range	40°C to +85°C
Junction Temperature	150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	260°C
ESD Susceptibility	
HBM	4000V
MM	400V

NOTE:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

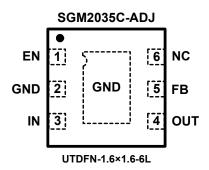
CAUTION

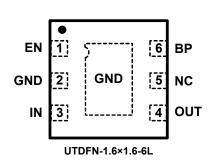
This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

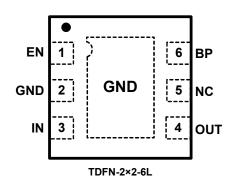
SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.



PIN CONFIGURATIONS (TOP VIEW)



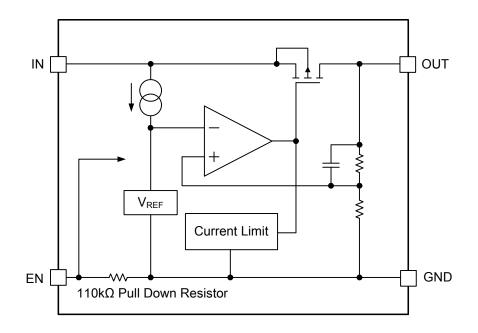




PIN DESCRIPTION

PIN	NAME	FUNCTION
1	EN	Shutdown Input. A logic low reduces the supply current to 10nA. 110k Ω pull down resistor at EN pin.
2	GND	Ground.
3	IN	Regulator Input. Supply voltage can range from 2.5V to 5.5V. Bypass with a $1\mu F$ capacitor to GND.
4	OUT	Regulator Output.
5	NC	No Connection (fixed voltage version only).
	FB	Feedback Pin (adjustable voltage version only). This is used to set the output voltage of the device.
6	BP	Reference-Noise Bypass (fixed voltage version only). Bypass with a low-leakage 0.01µF ceramic capacitor for reduced noise at the output.
	NC	No Connection (adjustable voltage version only).
Exposed Pad	GND	Exposed pad should be soldered to PCB board and connected to GND.

FUNCTION BLOCK DIAGRAM



ELECTRICAL CHARACTERISTICS

 $(V_{IN} = V_{OUT (NOMINAL)} + 0.5V \text{ or } 2.5V, \text{ whichever is greater, Full} = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted.}$ For SGM2035C-ADJ, $V_{OUT} = 3.3V.$)

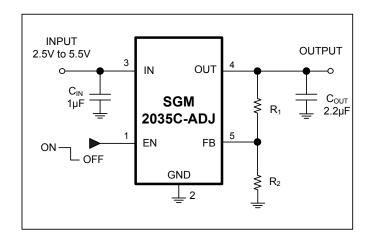
PARAMETER	SYMBOL	CONDITIONS		TEMP	MIN	TYP	MAX	UNITS
Input Voltage	V _{IN}		+25°C	2.5		5.5	V	
Output Voltage Accuracy		I _{OUT} = 0.1mA	+25°C	-3		+3	%	
Maximum Output Current (1)				+25°C	500			mA
Current Limit	I _{LIM}			+25°C	510			mA
Ground Pin Current	ΙQ	No Load, V _{EN} = 2V		+25°C		115	220	μA
		I _{OUT} = 100mA				50	90	
Dropout Voltage (2)		I _{OUT} = 300mA		+25°C		150	250	mV
		I _{OUT} = 500mA				250	400	
Line Regulation	$\frac{\Delta V_{\text{OUT}}}{\Delta V_{\text{IN}} \times V_{\text{OUT}}}$	V _{IN} = 2.5V to 5.5V, I _{OUT} = 1mA	+25°C		0.02	0.095	%/V	
Load Regulation	$\frac{\Delta V_{\text{OUT}}}{\Delta I_{\text{LOAD}} \times V_{\text{OUT}}}$	I_{OUT} = 0.1mA to 500mA, C_{OUT}	+25°C		0.0025	0.0075	%/mA	
Output Voltage Noise	e _n	$f = 10Hz$ to 100kHz, $C_{BP} = 0.0$ $C_{OUT} = 10\mu F$	+25°C		30		μV_{RMS}	
Power Supply Rejection Ratio	PSRR	$C_{BP} = 0.1 \mu F$, $I_{LOAD} = 50 mA$, $C_{OUT} = 1 \mu F$, $V_{IN} = V_{OUT} + 1 V$	f = 217Hz	+25°C		77		dB
Tower ouppry Rejection Ratio	TORK		f = 1kHz	+25°C		73		dB
SHUTDOWN								
EN Input Threshold	V_{IH}	V _{IN} = 2.5V to 5.5V		Full	1.5			V
LN IIIput Tillesiloid	V_{IL}			Full			0.3	V
Pull Down Resistor	R _{PULL DOWN}			+25°C		110		kΩ
Shutdown Supply Current	$I_{Q(SHDN)}$	V _{EN} = 0.3V	Full		0.01		μA	
Shutdown Exit Delay (3)		$C_{BP} = 0.01 \mu F$, $C_{OUT} = 1 \mu F$, No Load		+25°C		30		μs
THERMAL PROTECTION								
Thermal Shutdown Temperature	T _{SHDN}					150		°C
Thermal Shutdown Hysteresis	ΔT_{SHDN}			_		15		°C

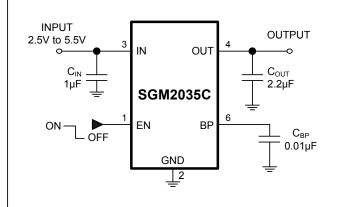
NOTES:

- 1. Maximum output current is affected by PCB layout, size of metal trace, the thermal conduction path between metal layers and the environment of the system.
- 2. The dropout voltage is defined as V_{IN} V_{OUT} , when V_{OUT} is 100mV below the value of V_{OUT} for V_{IN} = V_{OUT} + 0.5V. (Only applicable for V_{OUT} = +2.5V to +5.0V.)
- 3. Time needed for V_{OUT} to reach 90% of final value.



TYPICAL APPLICATION CIRCUITS



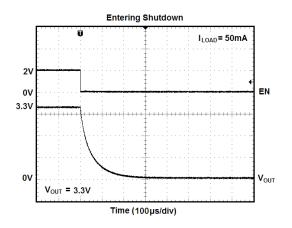


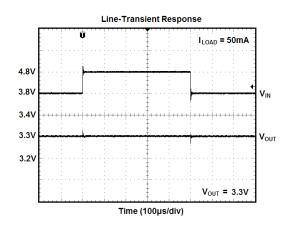
NOTE: Choose R_2 = 47k Ω to maintain a 26µA minimum load. Calculate the value for R_1 using the following equation:

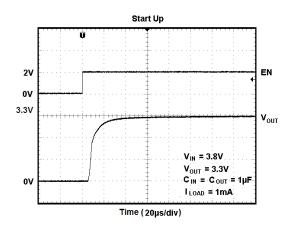
$$R_1 = R_2 \times \left(\frac{V_{OUT}}{1.206V} - 1 \right)$$

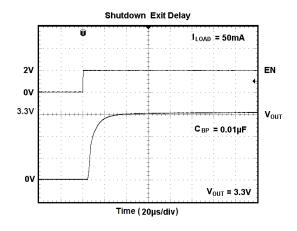
TYPICAL PERFORMANCE CHARACTERISTICS

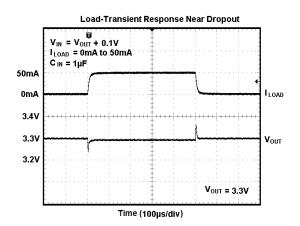
 $V_{IN} = V_{OUT\,(NOMINAL)} + 0.5V \text{ or } 2.5V \text{ (whichever is greater)}, \ C_{IN} = 1\mu\text{F}, \ C_{OUT} = 1\mu\text{F}, \ C_{BP} = 0.01\mu\text{F}, \ T_A = +25^{\circ}\text{C}, \ unless otherwise noted.}$

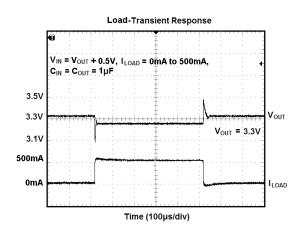






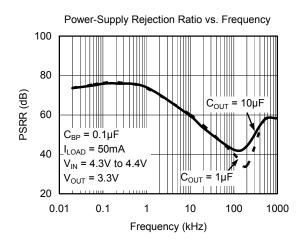


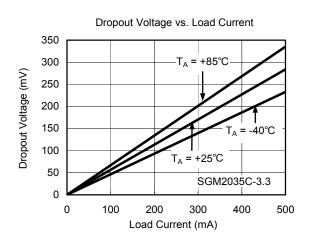


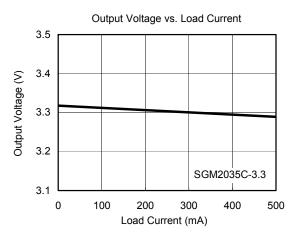


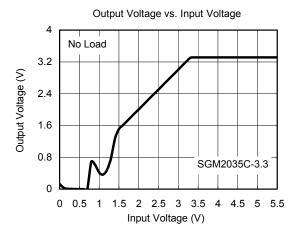
TYPICAL PERFORMANCE CHARACTERISTICS

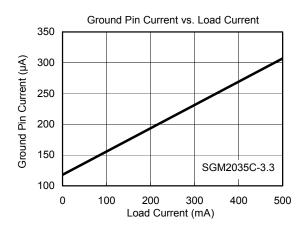
 $V_{IN} = V_{OUT\,(NOMINAL)} + 0.5V \text{ or } 2.5V \text{ (whichever is greater)}, \ C_{IN} = 1\mu\text{F}, \ C_{OUT} = 1\mu\text{F}, \ C_{BP} = 0.01\mu\text{F}, \ T_A = +25^{\circ}\text{C}, \ unless otherwise noted.}$

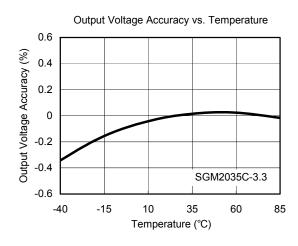






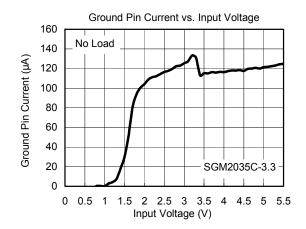


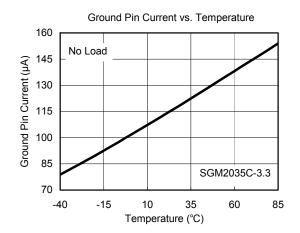




TYPICAL PERFORMANCE CHARACTERISTICS

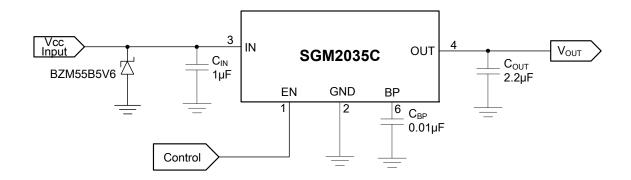
 $V_{IN} = V_{OUT\,(NOMINAL)} + 0.5V \text{ or } 2.5V \text{ (whichever is greater)}, \ C_{IN} = 1\mu\text{F}, \ C_{OUT} = 1\mu\text{F}, \ C_{BP} = 0.01\mu\text{F}, \ T_A = +25^{\circ}\text{C}, \ unless otherwise noted.}$





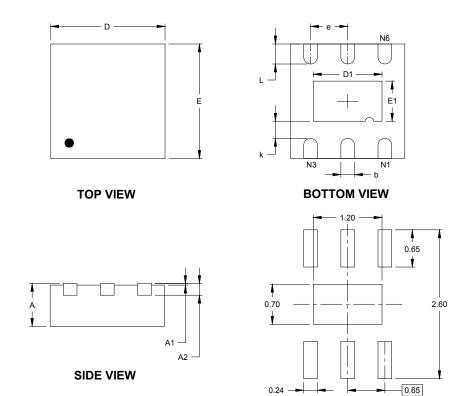
APPLICATION NOTE

When LDO is used in handheld products, attention must be paid to voltage spikes which could damage SGM2035C. In such applications, voltage spikes will be generated at charger interface and V_{BUS} pin of USB interface when charger adapters and USB equipments are hot-plugged. Besides this, handheld products will be tested on the production line without battery. Test engineer will apply power from the connector pin which connects with positive pole of the battery. When external power supply is turned on suddenly, the voltage spikes will be generated at the battery connector. The voltage spikes will be very high, and it always exceeds the absolute maximum input voltage (6.0V) of LDO. In order to get robust design, design engineer needs to clear up this voltage spike. Zener diode is a cheap and effective solution to eliminate such voltage spike. For example, BZM55B5V6 is a 5.6V small package Zener diode which can be used to remove voltage spikes in cell phone designs. The schematic is shown below.



PACKAGE OUTLINE DIMENSIONS

TDFN-2×2-6L

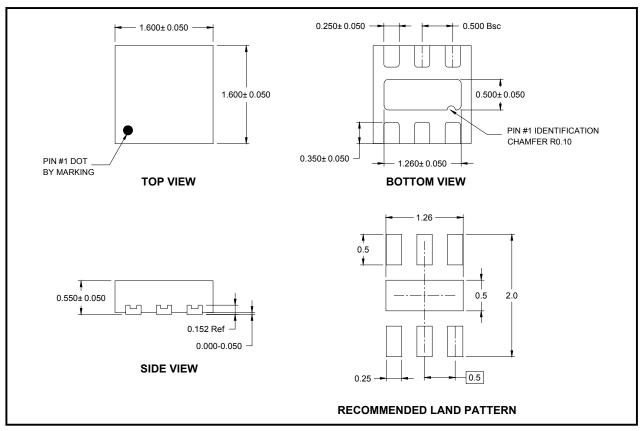


RECOMMENDED LAND PATTERN (Unit: mm)

Symbol		nsions meters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
Α	0.700	0.800	0.028	0.031	
A1	0.000	0.050	0.000	0.002	
A2	0.203	REF	0.008 REF		
D	1.900	2.100	0.075	0.083	
D1	1.100	1.300	0.043	0.051	
E	1.900	2.100	0.075	0.083	
E1	0.600	0.800	0.024	0.031	
k	0.200) MIN	0.008	3 MIN	
b	0.180	0.300	0.007	0.012	
е	0.650) TYP	0.026	TYP	
L	0.250	0.450	0.010	0.018	

PACKAGE OUTLINE DIMENSIONS

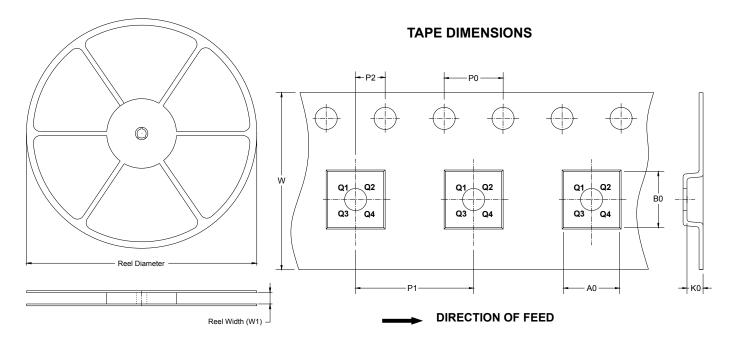
UTDFN-1.6×1.6-6L



NOTE: All linear dimensions are in millimeters.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

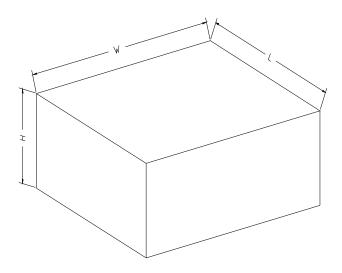


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
UTDFN-1.6×1.6-6L	7"	9.0	1.78	1.78	0.69	4.00	4.00	2.00	8.00	Q1
TDFN-2×2-6L	7"	9.5	2.30	2.30	1.10	4.00	4.00	2.00	8.00	Q1

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18