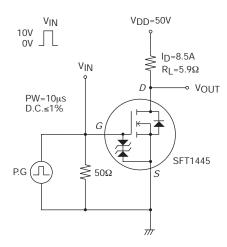
Electrical Characteristics at Ta=25°C

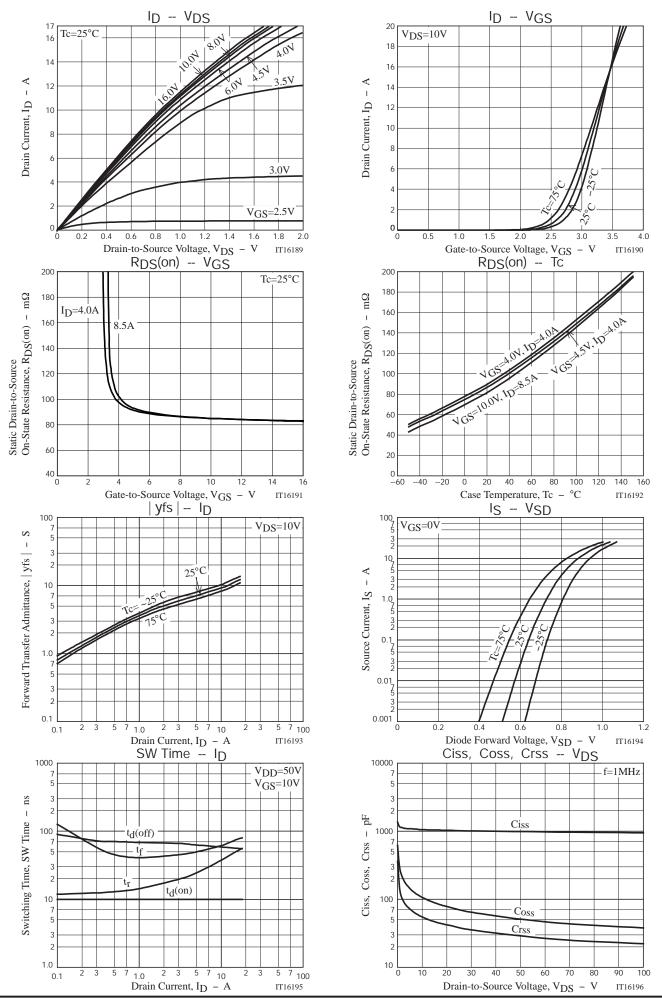
Parameter	Symbol	Conditions	Ratings			Unit	
Parameter	Symbol	Conditions	min	typ	max	UIIII	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	100			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =100V, V _{GS} =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ	
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.2		2.6	V	
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =8.5A		8.9		S	
	R _{DS} (on)1	I _D =8.5A, V _{GS} =10V		85	111	mΩ	
Static Drain-to-Source On-State Resistance	R _{DS} (on)2	I _D =4A, V _G S=4.5V		90	126	mΩ	
	R _{DS} (on)3	I _D =4A, V _{GS} =4V		93	130	mΩ	
Input Capacitance	Ciss			1030		pF	
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		78		pF	
Reverse Transfer Capacitance	Crss			42		pF	
Turn-ON Delay Time	t _d (on)			10		ns	
Rise Time	t _r	Can appointed Toot Circuit		35		ns	
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		60		ns	
Fall Time	tf			60		ns	
Total Gate Charge	Qg			19		nC	
Gate-to-Source Charge	Qgs	V _{DS} =50V, V _{GS} =10V, I _D =17A		3.6		nC	
Gate-to-Drain "Miller" Charge	Qgd			3.8		nC	
Diode Forward Voltage	V _{SD}	I _S =17A, V _{GS} =0V		0.96	1.2	V	

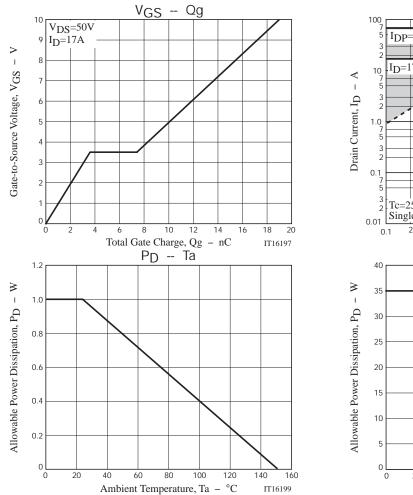
Switching Time Test Circuit

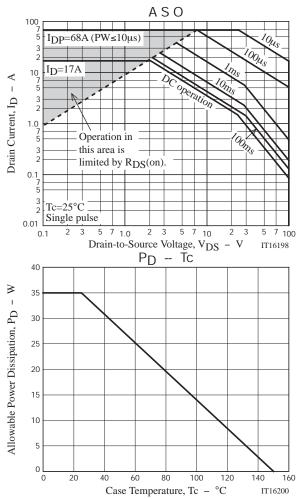


Ordering Information

Device	Package	Shipping	memo
SFT1445-H	TP	500pcs./bag	Db Free and Helegen Free
SFT1445-TL-H	TP-FA	700pcs./reel	Pb Free and Halogen Free





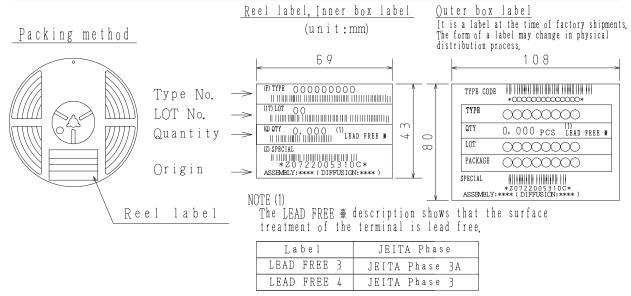


Taping Specification

SFT1445-TL-H

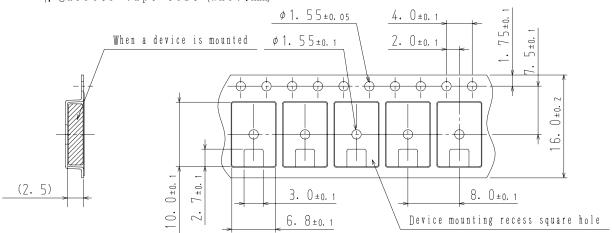
Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format		
	Туре	Reel	Inner box	Outer box	Inner $BOX(C-1)$	Outer BOX (A-7)	
TP-FA	TP	700	2, 100	12, 600	3 reels contained	6 inner boxes contained	
					Dimensions:mm (external)	Dimensions:mm (external)	
					183×72×185	440×195×210	

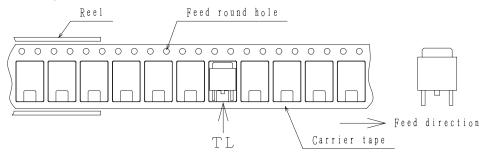


Taping configuration

1. Carrier tape size (unit:mm)



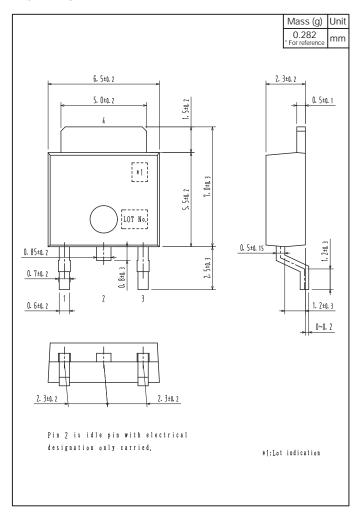
7. Device placement direction

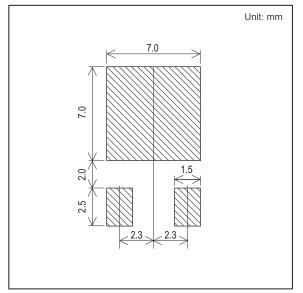


Those with one electrode terminal on the feed hole side·····TL

Outline Drawing SFT1445-TL-H

Land Pattern Example



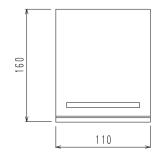


Bag Packing Specification SFT1445-H

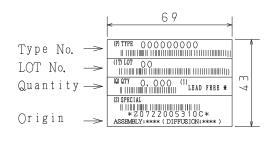
1. Packing Format

Package Name		Maximum Number of devices contained (pcs)					
I wonago Mamo	Bag	Inner box	Outer box				
ТР		B-1	A-1	A-2			
1 1 1	500	10,000	50,000	30,000			
	•	Packing format (Dimensions:mm (external))					
		Inner box	Outer box				
		B-1	A-1	A-2			
		445×225×55	470×250×300	470×250×190			

2. Bag dimensions (unit:mm)





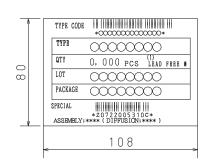


4. Outer box label (unit:mm)

It is a label at the time of factory shipments, The form of a label may change in physical distribution process,

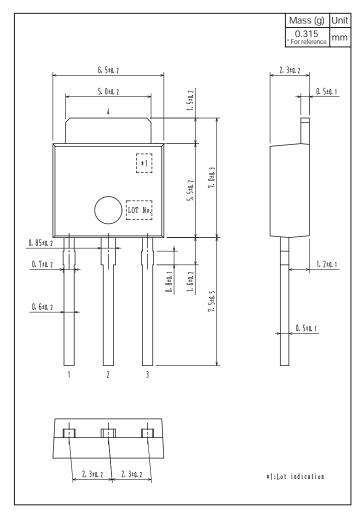


Label		JEITA Phase
LEAD FREE	3	JEITA Phase 3A
LEAD FREE	4	JEITA Phase 3



Outline Drawing

SFT1445-H



Note on usage: Since the SFT1445 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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