## RHRD660, RHRD660S

**Electrical Specifications**  $T_C = 25^{\circ}C$ , Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
V <sub>F</sub>	I <sub>F</sub> = 6A	-	-	2.1	V
	I <sub>F</sub> = 6A, T <sub>C</sub> = 150 <sup>o</sup> C	-	-	1.7	V
I <sub>R</sub>	V <sub>R</sub> = 600V	-	-	100	μΑ
	$V_R = 600V, T_C = 150^{\circ}C$	-	-	500	μΑ
t <sub>rr</sub>	I <sub>F</sub> = 1A, dI <sub>F</sub> /dt = 200A/μs	-	-	30	ns
	$I_F = 6A$ , $dI_F/dt = 200A/\mu s$	-	-	35	ns
t <sub>a</sub>	$I_F = 6A$ , $dI_F/dt = 200A/\mu s$	-	16	-	ns
t <sub>b</sub>	$I_F = 6A$ , $dI_F/dt = 200A/\mu s$	-	8.5	-	ns
Q <sub>RR</sub>	$I_F = 6A$ , $dI_F/dt = 200A/\mu s$	-	45	-	nC
CJ	V <sub>R</sub> = 10V, I <sub>F</sub> = 0A	-	20	-	pF
$R_{ heta JC}$		-	-	3	°C/W

#### **DEFINITIONS**

 $V_F$  = Instantaneous forward voltage (pw = 300 $\mu$ s, D = 2%).

I<sub>R</sub> = Instantaneous reverse current.

 $t_{rr}$  = Reverse recovery time (See Figure 9), summation of  $t_a$  +  $t_b$ .

 $t_a$  = Time to reach peak reverse current (See Figure 9).

 $t_b$  = Time from peak  $I_{RM}$  to projected zero crossing of  $I_{RM}$  based on a straight line from peak  $I_{RM}$  through 25% of  $I_{RM}$  (See Figure 9).

Q<sub>RR</sub> = Reverse recovery charge.

 $C_J$  = Junction capacitance.

 $R_{\theta,JC}$  = Thermal resistance junction to case.

pw = Pulse width.

D = Duty cycle.

# **Typical Performance Curves**

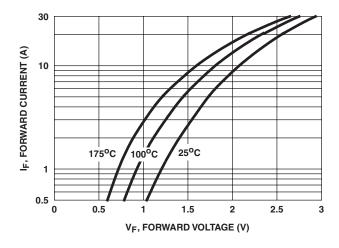


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

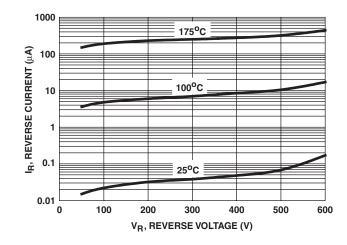


FIGURE 2. REVERSE CURRENT vs REVERSE

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# Typical Performance Curves (Continued)

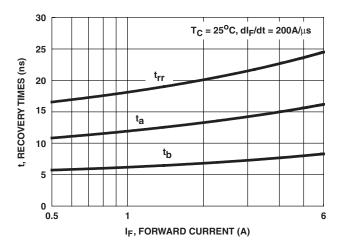


FIGURE 3.  $t_{\rm rr}, t_{\rm a}$  AND  $t_{\rm b}$  CURVES vs FORWARD CURRENT

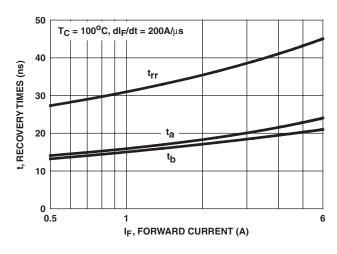


FIGURE 4. t<sub>rr</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

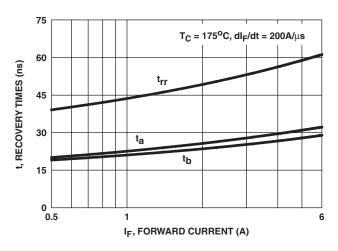


FIGURE 5.  $t_{rr}$ ,  $t_a$  AND  $t_b$  CURVES vs FORWARD CURRENT

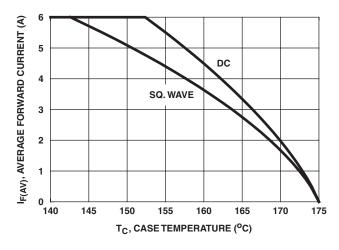


FIGURE 6. CURRENT DERATING CURVE

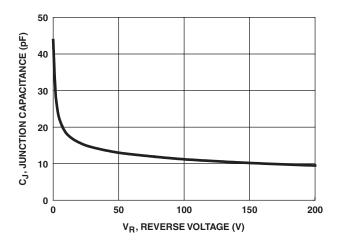


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

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## Test Circuits and Waveforms

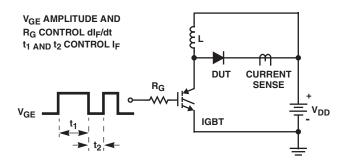


FIGURE 8.  $t_{rr}$  TEST CIRCUIT

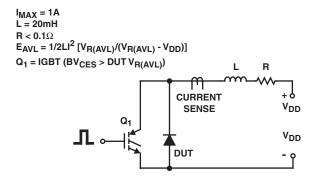


FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

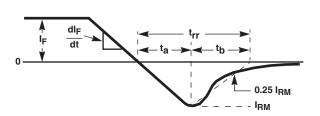


FIGURE 9. t<sub>rr</sub> WAVEFORMS AND DEFINITIONS

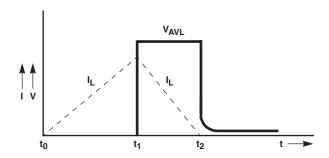


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

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Rev. H