MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	V
Average Rectified Forward Current (At Rated V _R , T _C = 110°C)	Io	1.0	Α
Peak Repetitive Forward Current (At Rated V _R , Square Wave, 100 kHz, T _C = 110°C)	I _{FRM}	2.0	А
Non-Repetitive Peak Surge Current (Non-Repetitive peak surge current, halfwave, single phase, 60 Hz)	I _{FSM}	50	А
Storage Temperature	T _{stg}	-55 to 150	°C
Operating Junction Temperature	T _J	-55 to 125	°C
Voltage Rate of Change (Rated V _R , T _J = 25°C)	dv/dt	10,000	V/μs

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance, Junction–to–Lead (Anode) (Note 1) Thermal Resistance, Junction–to–Tab (Cathode) (Note 1) Thermal Resistance, Junction–to–Ambient (Note 1)	R _{tjl} R _{tjtab} R _{tja}	35 23 277	°C/W

^{1.} Mounted with minimum recommended pad size, PC Board FR4, See Figures 9 & 10

ELECTRICAL CHARACTERISTICS

Rating	Symbol	Value		Unit
Maximum Instantaneous Forward Voltage (Note 2), See Figure 2	V _F	T _J = 25°C	T _J = 85°C	V
$(I_F = 0.1 \text{ A})$ $(I_F = 1.0 \text{ A})$ $(I_F = 3.0 \text{ A})$		0.36 0.55 0.85	0.30 0.515 0.88	
Maximum Instantaneous Reverse Current (Note 2), See Figure 4	I _R	T _J = 25°C	T _J = 85°C	mA
$(V_R = 40 \text{ V})$ $(V_R = 20 \text{ V})$		0.5 0.15	25 18	

^{2.} Pulse Test: Pulse Width $\leq 250~\mu s,$ Duty Cycle $\leq 2\%$

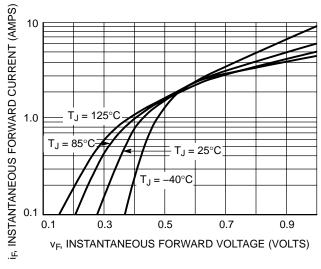


Figure 1. Typical Forward Voltage

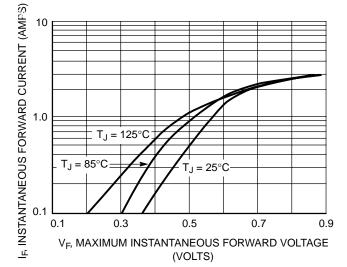
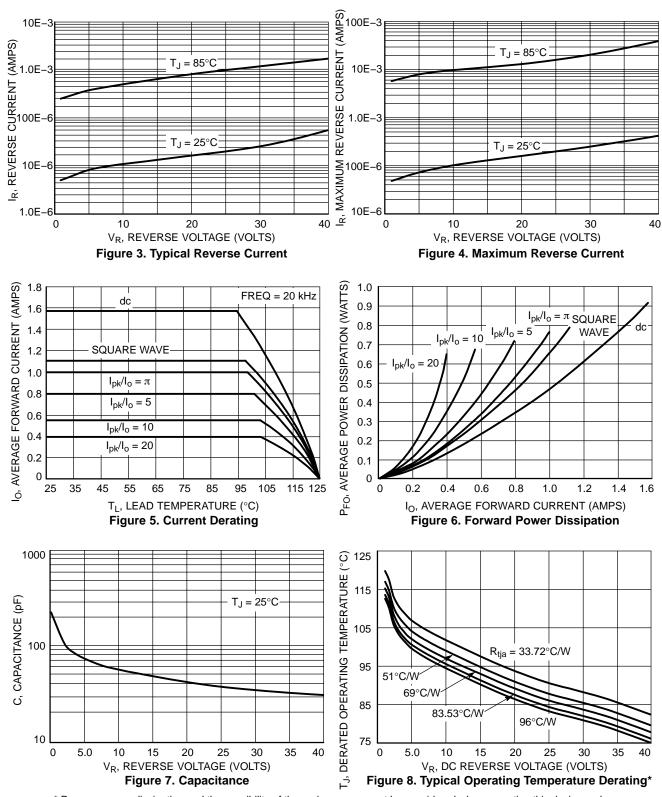


Figure 2. Maximum Forward Voltage



^{*} Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of T_J therefore must include forward and reverse power effects. The allowable operating T_J may be calculated from the equation: $T_J = T_{Jmax} - r(t)(Pf + Pr)$ where

r(t) = thermal impedance under given conditions,

Pf = forward power dissipation, and

Pr = reverse power dissipation

This graph displays the derated allowable T_J due to reverse bias under DC conditions only and is calculated as $T_J = T_{Jmax} - r(t)Pr$, where r(t) = Rthja. For other power applications further calculations must be performed.

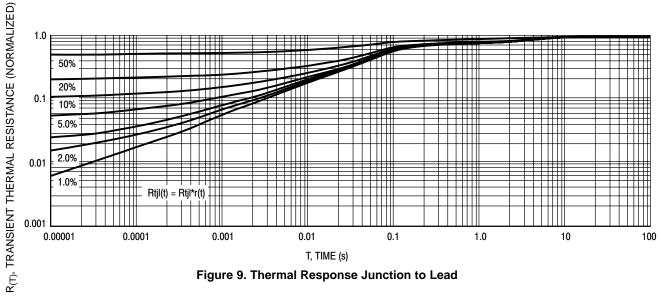


Figure 9. Thermal Response Junction to Lead

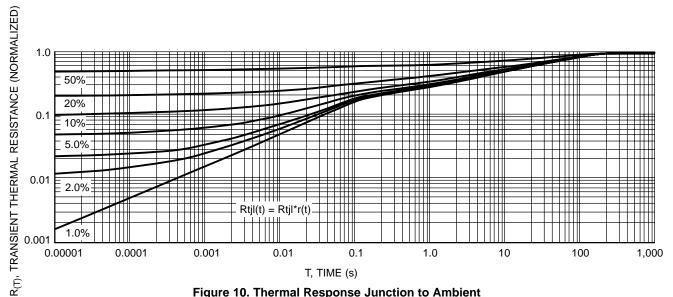
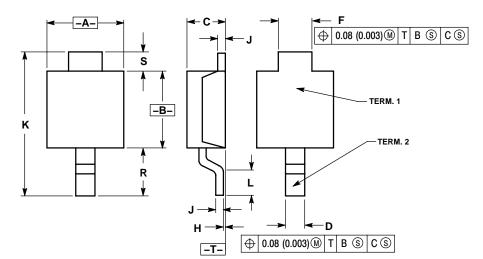


Figure 10. Thermal Response Junction to Ambient

PACKAGE DIMENSIONS

POWERMITE

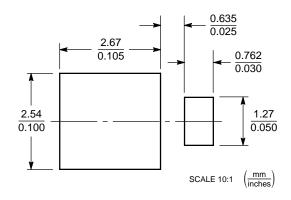
CASE 457-04 ISSUE D



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETER.
- DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.

	MILLIN	IETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	1.75	2.05	0.069	0.081	
В	1.75	2.18	0.069	0.086	
C	0.85	1.15	0.033	0.045	
D	0.40	0.69	0.016	0.027	
F	0.70	1.00	0.028	0.039	
Н	-0.05	+0.10	-0.002	+0.004	
ſ	0.10	0.25	0.004	0.010	
K	3.60	3.90	0.142	0.154	
L	0.50	0.80	0.020	0.031	
R	1.20	1.50	0.047	0.059	
S	0.50 REF		0.019	REF	

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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