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## Vishay Semiconductors

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
	V <sub>FM</sub> <sup>(1)</sup>	20 A	T <sub>J</sub> = 25 °C	0.84	V			
Maximum forward voltage drop		10 A	T <sub>J</sub> = 125 °C	0.57				
		20 A	1j = 125 C	0.72				
Maximum instantaneous reverse current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	Rated DC voltage	0.1	mA			
Maximum instantaneous reverse current		T <sub>J</sub> = 125 °C	Haleu DC Vollage	15				
Threshold voltage	V <sub>F(TO)</sub>	T <sub>.1</sub> = T <sub>.1</sub> maximum	0.354	V				
Forward slope resistance	r <sub>t</sub>	ij = ij maximum	17.6	mΩ				
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal ran	600	pF				
Typical series inductance	L <sub>S</sub>	Measured from top of term	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

#### Note

 $^{(1)}$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction temperature range	TJ		- 65 to 150	°C				
Maximum storage temperature range	T <sub>Stg</sub>		- 65 to 175	C				
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation	2.0	°C/W				
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased (only for TO-220)	0.50	0/10				
Approximate weight			2	g				
Approximate weight			0.07	oz.				
Mounting torgue			6 (5)	kgf ⋅ cm				
maximum			12 (10)	(lbf ⋅ in)				
Marking davias		Case of the TO 220AC	MBR1035					
Marking device		Case style TO-220AC		1045				

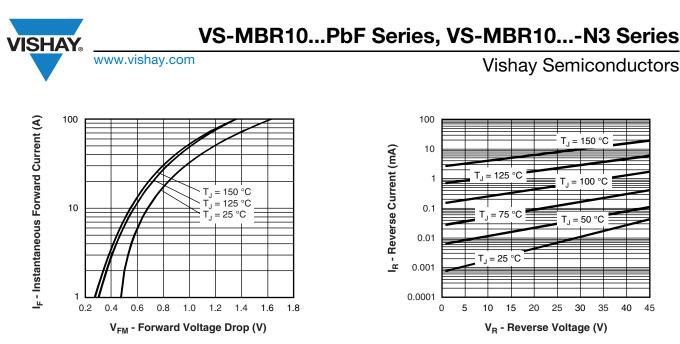
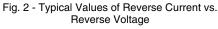


Fig. 1 - Maximum Forward Voltage Drop Characteristics



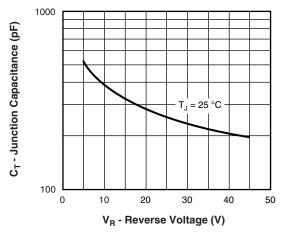
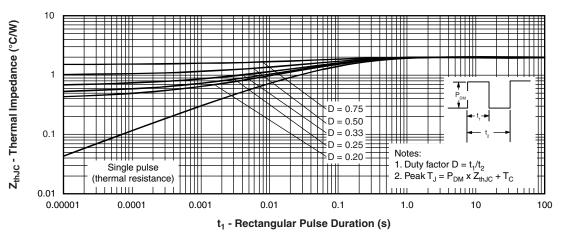


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage





Revision: 29-Aug-11	3	Document Number: 94284
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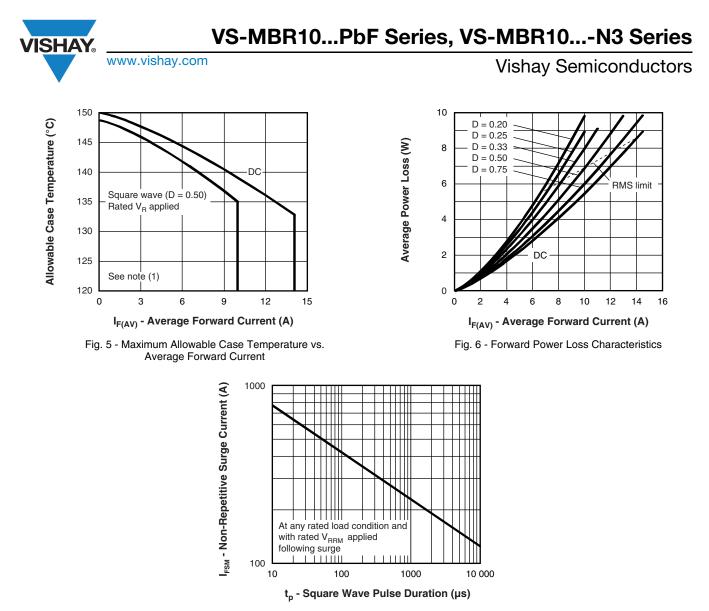


Fig. 7 - Maximum Non-Repetitive Surge Current

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 - D); } \mbox{I}_{R} \mbox{ at } \mbox{V}_{R1} = \mbox{Rated V}_{R} \end{array}$ 



**Vishay Semiconductors** 

### **ORDERING INFORMATION TABLE**

VS-**Device code MBR** 10 45 4 2 1 (3) Vishay Semiconductors product 1 2

3

4

5

Schottky MBR series

Currrent rating (10 = 10 A)

- 35 = 35 V Voltage ratings 45 = 45 V
- Environmental digit
  - PbF = Lead (Pb)-free and RoHS compliant

PbF

〔5〕

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-MBR1035PbF	50	1000	Antistatic plastic tube						
VS-MBR1035-N3	50	1000	Antistatic plastic tube						
VS-MBR1045PbF	50	1000	Antistatic plastic tube						
VS-MBR1045-N3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95221				
Part marking information	TO-220AC PbF	www.vishay.com/doc?95224				
	TO-220AC -N3	www.vishay.com/doc?95068				
SPICE model		www.vishay.com/doc?95293				



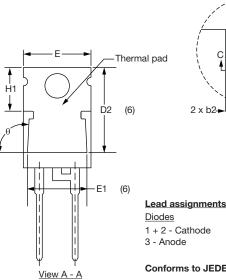
**Vishay Semiconductors** 

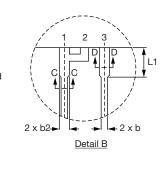
**TO-220AC** 

plane

### **DIMENSIONS** in millimeters and inches









**Diodes** 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220AC

⊕ 0.015 **()** BA()

SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
STIVIDOL	MIN. MAX. MIN. MAX.	STIVIDOL	MIN.	MAX.	MIN.	MAX.						
А	4.25	4.65	0.167	0.183			E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055			E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115			е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040			e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4		H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068			L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4		L1	3.32	3.82	0.131	0.150	2
с	0.36	0.61	0.014	0.024			L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4		L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3		ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6		θ	90° t	o 93°	90° t	o 93°	
E	10.11	10.51	0.398	0.414	3, 6							

Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(4)</sup> Dimension b1, b3 and c1 apply to base metal only
- <sup>(5)</sup> Controlling dimension: inches
- <sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2 and E1
- <sup>(7)</sup> Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- <sup>(8)</sup> Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline

Document Number: 95221 Revision: 07-Mar-11



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