# ASSP For Power Supply Applications

# BIPOLAR Switching Regulator Controller (Low Voltage)

# **MB3800**

## DESCRIPTION

The MB3800 is a single-channel switching regulator control IC for low voltage applications incorporating a soft start function and short circuit detection function. The device has a low minimum operating voltage of 1.8 V and is ideal for the power supply of battery-operated electronic equipment.

## FEATURES

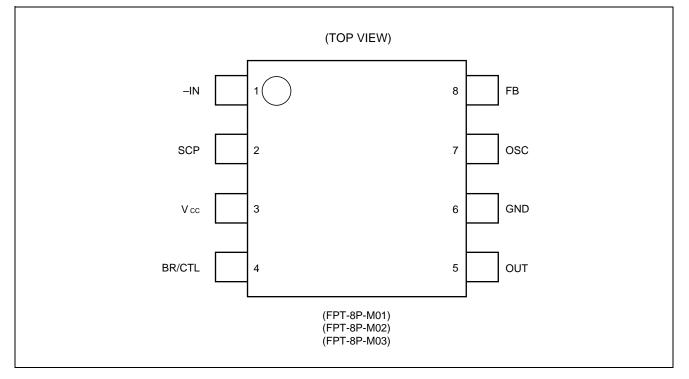
- Wide supply voltage operating range: 1.8 V to 15 V
- Low current consumption: Typically 5.5 mA in operation, 1  $\mu$ A or less in stand-by
- High speed operation is possible: Maximum 1 MHz
- The error amplifier gain is set inside the IC, so peripheral components are minimized.
- Incorporates a soft start circuit.
- Incorporates a timer-latch type short circuit detection circuit (SCP).
- Totem-pole type output with adjustable on/off current (for NPN transistors)
- Incorporates a stand-by function.
- Three types of packages (SOP-8 : 2 types, SSOP-8 : 1 type)

### Application

- LCD monitor/panel
- Power supply module
- Portable apparatus etc.



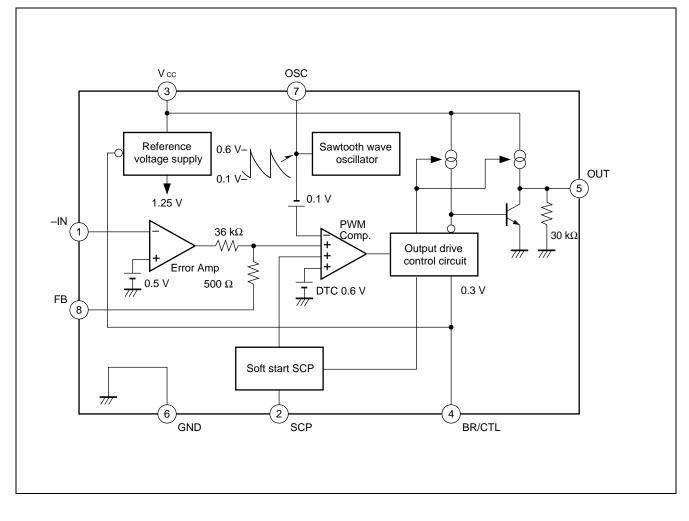
## ■ PIN ASSIGNMENT



# ■ PIN DESCRIPTION

Pin No.	Symbol	I/O	Description	
1	-IN	Ι	Error amplifier inverting input pin	
2	SCP	—	Soft start and SCP setting capacitor connection pin	
3	Vcc	—	Power supply pin	
4	BR/CTL	I	Output current setting and control pin	
5	OUT	0	Totem-pole type output pin	
6	GND	—	Ground pin	
7	OSC	—	Capacitor and resistor connection pin for setting the oscillation frequency	
8	FB	0	Error amplifier output pin	

### ■ BLOCK DIAGRAM



Parameter	Symbol	Condition	Ra	Unit		
Falameter	Symbol	Condition	Min	Max	Onit	
Power supply voltage	Vcc		_	16	V	
Output source current	lo+	—	_	-50	mA	
Output sink current	lo⁻		_	50	mA	
		FPT-8P-M01, Ta ≤ +25°C	_	570*	mW	
Allowable dissipation	PD	FPT-8P-M02, Ta ≤ +25°C	—	430*	mW	
		SSOP-8, Ta ≤ +25°C	—	580*	mW	
Storage temperature	Tstg	_	-55	+125	°C	

### ■ ABSOLUTE MAXIMUM RATINGS

\*: When mounted on a 10 cm square double-sided epoxy board.

**WARNING:** Semiconductor devices can be permanently damaged by application of stress (voltage, current, temperature, etc.) in excess of absolute maximum ratings. Do not exceed these ratings.

### RECOMMENDED OPERATING CONDITIONS

					(Ta = +25°C	
Parameter	Symbol	Value			Unit	
Falameter	Symbol	Min	Тур	Max	Unit	
Power supply voltage	Vcc	1.8	_	15	V	
Error amplifier input voltage	Vi	-0.2	_	+1.0	V	
BR/CTL pin input voltage	Vbr	-0.2	_	Vcc	V	
Output source current	lo+	-40			mA	
Output sink current	lo-	_	_	40	mA	
SCP pin capacitance	Сре	_	0.1		μF	
Phase compensation capacitance	СР	_	0.1	_	μF	
Output current setting resistance	R <sub>β</sub>	150	390	5000	Ω	
Timing resistance	R⊤	1.0	3.0	10.0	kΩ	
Timing capacitance	Ст	100	270	10000	pF	
Oscillation frequency	fosc	10	500	1000	kHz	
Operating Ambient temperature	Та	-30	+25	+85	°C	

# **WARNING:** The recommended operating conditions are required in order to ensure the normal operation of the semiconductor device. All of the device's electrical characteristics are warranted when the device is operated within these ranges.

Always use semiconductor devices within their recommended operating condition ranges. Operation outside these ranges may adversely affect reliability and could result in device failure.

No warranty is made with respect to uses, operating conditions, or combinations not represented on the data sheet. Users considering application outside the listed conditions are advised to contact their representatives beforehand.

# ■ ELECTRICAL CHARACTERISTICS

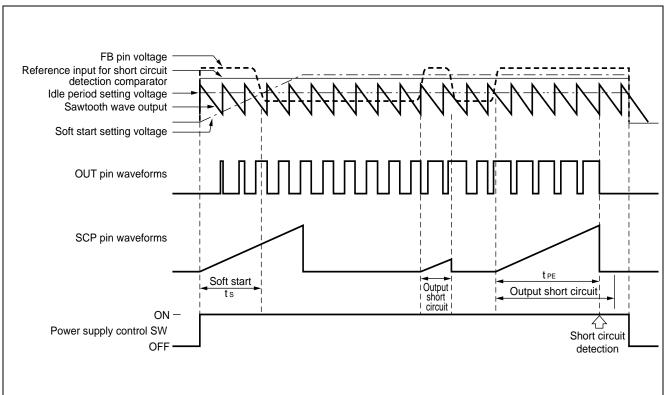
				(VCC	= +2 V	, Ta = -	+25 °C)
Bor	omotor	Symbol	Condition	Value			Unit
Parameter		Symbol Condition		Min	Тур	Max	
Circuit to prevent malfunction at low	Reset voltage	Vr	_			0.9	V
input voltage (U.V.L.O.)	Threshold voltage	VTH		1.1	1.3	1.5	V
	Charging current	lcs	VSCP = 0 V	-1.5	-1.0	-0.7	μΑ
Soft start	Voltage at soft start completion	Vts	_	0.7	0.8	0.9	V
Short circuit	Charging current	Ісрс	VSCP = 0 V	-1.5	-1.0	-0.7	μΑ
detection (S.C.P.)	Threshold voltage	VtPC	—	0.7	0.8	0.9	V
	Oscillation frequency	focs	$R_T = 3.0 \text{ k}\Omega, C_T = 270 \text{ pF}$	400	500	600	kHz
Sawtooth wave oscillator (OSC)	Frequency input stability	fd∨	Vcc = 2 V to 15 V	_	2	10	%
	Frequency variation with temperature	fат	Ta = -30°C to +85°C	_	5	_	%
	Input threshold voltage	Vτ	V <sub>FB</sub> = 450 mV	480	500	520	mV
	V⊤ input stability	Vtdv	Vcc = 2 V to 15 V	-	5	20	mV
	V⊤ variation with temperature	Vtdt	Ta = -30°C to +85°C	_	1		%
	Input bias current	Ів	$V_{IN} = 0 V$	-1.0	-0.2	+1.0	μA
Error amplifier	Voltage gain	Av	_	70	100	145	V/V
	Frequency bandwidth	BW	$A_V = 0 dB$	-	6	—	MHz
	Maximum output	Vом+		0.78	0.87	—	V
	voltage range	Vом⁻			0.05	0.2	V
	Output source current	Іом+		-	-40	-24	μA
	Output sink current	Іом⁻	Vfb = 0.45 V	24	40		μA
Idle period adjustment section	Maximum duty cycle	<b>t</b> duty		65	75	85	%

NCC 2V T 

#### (Continued)

Parameter		Symbol	Symbol Condition		Value		
		Symbol	Condition	Min	Тур	Max	Unit
	Output voltage	Voh1	R <sub>B</sub> = 390 Ω, lo = -15 mA	1.0	1.2	—	V
		Vон2	$R_B = 750 $ Ω, $V_{CC} = 1.8 $ V $I_0 = -10 $ mA	0.8	1.0	_	V
		Vol1	R <sub>B</sub> = 390 Ω, lo = 15 mA	_	0.1	0.2	V
Output section		Vol2		_	0.1	0.2	V
	Output source current	lo+	R <sub>B</sub> = 390 Ω, Vo = 0.9 V	_	-30	-20	mA
	Output sink current Io-		R <sub>B</sub> = 390 Ω, V <sub>0</sub> = 0.3 V	30	60		mA
	Pull down resistance	Ro	—	20	30	40	kΩ
	Pin voltage	Vbr	R <sub>B</sub> = 390 Ω	0.2	0.3	0.4	V
Output current	Input off condition	OFF	FF		_	0	μA
setting section/ Control section	Input on condition	Ion		_	_	-45	μA
	Pin current range	BR		-1.8	—	-0.1	mA
	Stand-by current Iccs		BR/CTL pin open or Vcc	—	—	1	μA
Entire device	Average supply current	Icc	R <sub>B</sub> = 390 Ω		5.5	9.3	mA

# DIAGRAM



# ■ HOW TO SET THE TIME CONSTANT FOR SOFT START AND SHORT CIRCUIT DETECTION

#### 1. Soft Start

At power on, the capacitor CPE connected to the SCP pin (pin 2) starts charging. The PWM comparator compares the soft start setting voltage as a proportion of the voltage at the SCP pin with the sawtooth waveform. The comparison controls the ON duty of the OUT pin (pin 5), causing the soft start operation. On completion of soft start operation, the voltage at the SCP pin stays low, the soft start setting voltage stays high, and the circuit enters the output short circuit detection wait state.

Soft start time (The time until the output ON duty reaches approximately 50%)

ts [s]  $\cong 0.35 \times C_{\text{PE}} \; [\mu F]$ 

#### 2. Short Circuit Protection

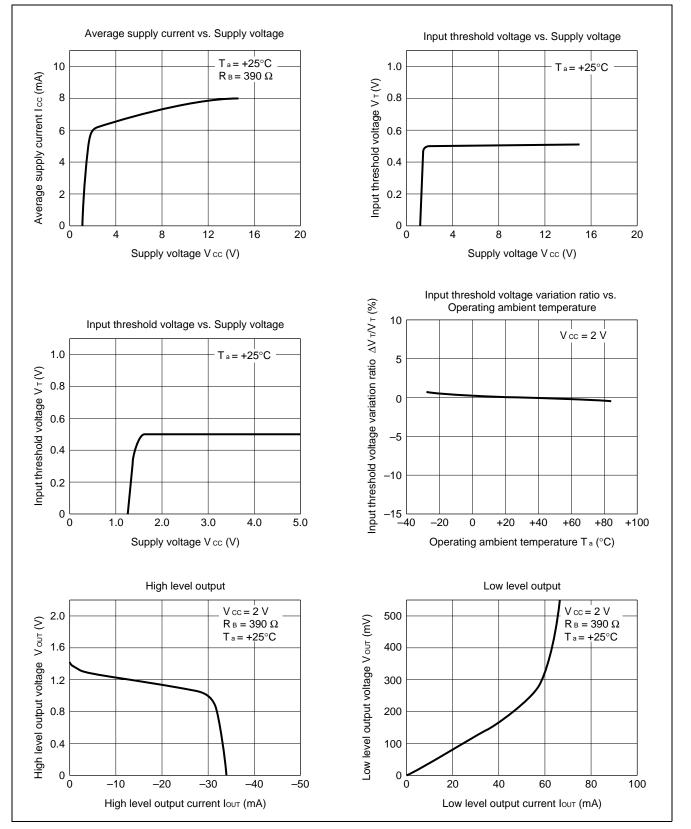
If the switching regulator output suddenly drops due to load effect, the error amplifier output (FB pin (pin 8)) is fixed at VOM<sup>+</sup> and capacitor  $C_{PE}$  starts charging. When the voltage at the SCP pin reaches approximately 0.8 V, the OUT pin (pin 5) is set low and the SCP pin (pin 2) stays low.

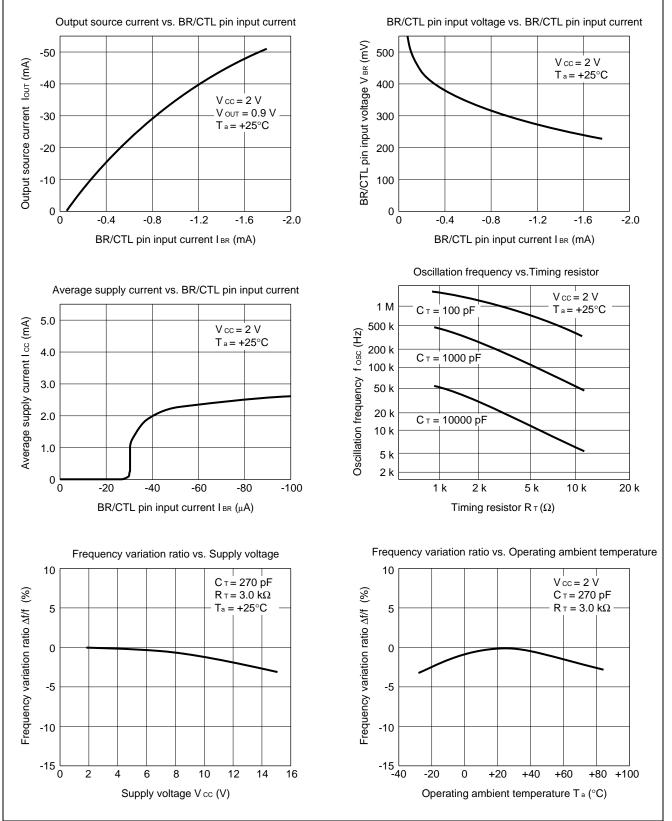
Once the protection circuit operates, the circuit can be restored by resetting the power supply.

• Short circuit detection time

tpe [s]  $\cong 0.8 \times C_{\text{PE}} \; [\mu F]$ 

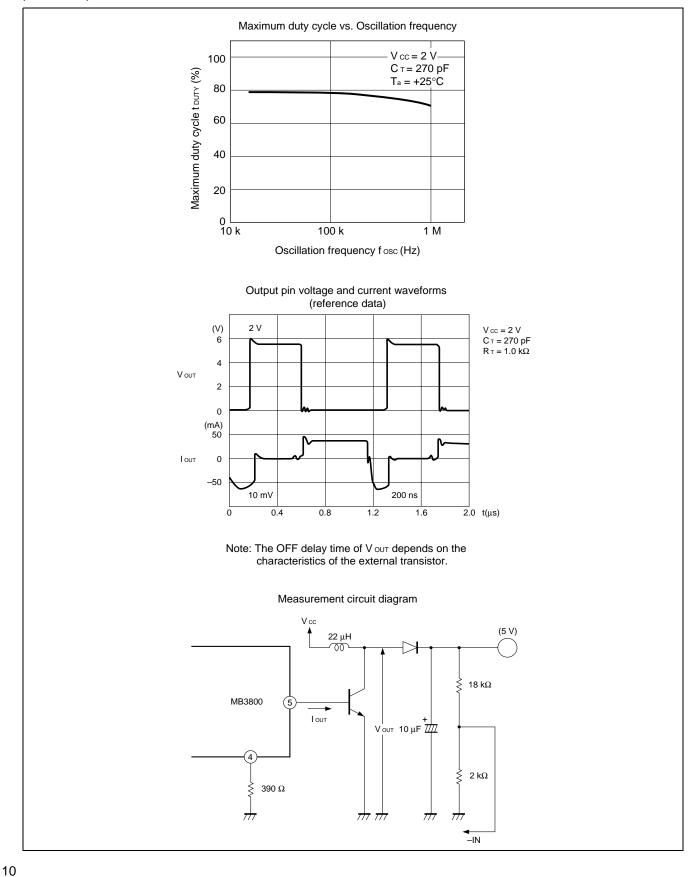
# TYPICAL CHARACTERISTICS





<sup>(</sup>Continued)

(Continued)



#### ■ FUNCTIONAL DESCRIPTION

#### 1. Switching Regulator Function

#### (1) Reference voltage circuit

The reference voltage circuit generates a temperature-compensated reference voltage ( $\cong$ 1.25V) from voltage supplied from the V<sub>CC</sub> pin (pin 3). In addition to providing the reference voltage for the switching regulator, the circuit also sets the idle period.

#### (2) Sawtooth wave oscillator

The sawtooth oscillator generates a sawtooth wave (up to 1 MHz) that is stable with respect to the supply voltage and temperature. The capacitor and resistor that set the oscillation frequency are connected to the OSC pin (pin 7).

#### (3) Error amplifier (Error Amp)

The error amplifier detects the output voltage of the switching regulator and outputs the PWM control signal. The voltage gain is fixed, and connecting a phase compensation capacitor to the FB pin (pin 8) provides stable phase compensation for the system.

#### (4) PWM comparator (PWM Comp.)

The voltage comparator has one inverting and three non-inverting inputs. The comparator is a voltage/pulse width converter that controls the ON time of the output pulse depending on the input voltage. The output level is high (H) when the sawtooth wave is lower than the error amplifier output voltage, soft start setting voltage, and idle period setting voltage.

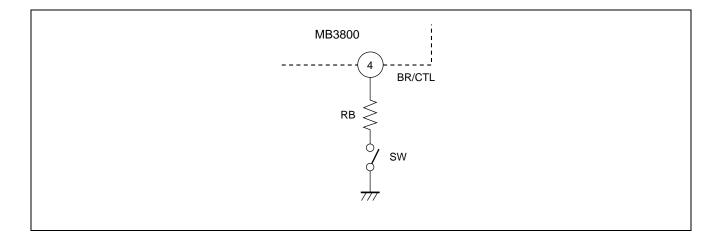
#### (5) Output circuit

The output circuit has a totem pole type configuration and can drive an external NPN transistor directly. The value of the ON/OFF current can be set by a resistor connected to the BR/CTL pin (pin 4).

#### 2. Power Supply Control Function

Stand-by mode (supply current 1  $\mu$ A or less) can be set by connecting the BR/CTL pin (pin 4) to V<sub>cc</sub> or by making the pin open circuit.

SW	Mode
OFF	Stand-by mode
ON	Operating mode



#### 3. Other Functions

#### (1) Soft start and short circuit detection

Soft start operation is set by connecting capacitor CPE to the SCP pin (pin 2). Soft start prevents a current spike on start-up.

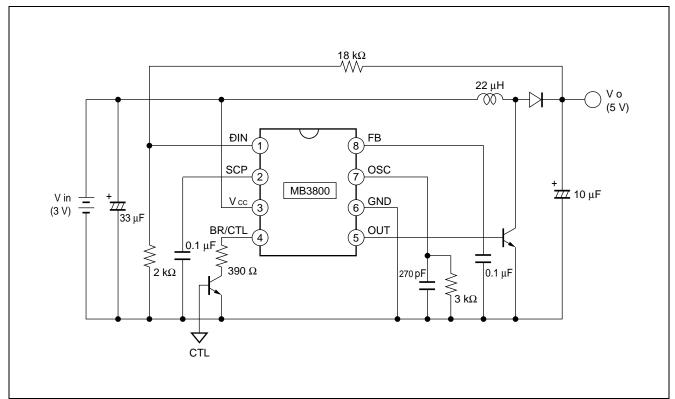
On completion of soft start operation, the SCP pin (pin 2) stays low and enters the short circuit detection wait state. When an output short circuit occurs, the error amplifier output is fixed at  $V_{OM}$ + and capacitor  $C_{PE}$  starts charging. After charging to approximately 0.8 V, the OUT pin (pin 5) is set low and the SCP pin (pin 2) stays low.

Once the protection circuit operates, the circuit can be restored by resetting the power supply. (See "■ HOW TO SET THE TIME CONSTANT FOR SOFT START AND SHORT CIRCUIT DETECTION".)

#### (2) Circuit to prevent malfunction at low input voltage

Transients when powering on or instantaneous glitches in the supply voltage can lead to malfunction of the control IC and cause system damage or failure. The circuit to prevent malfunction at low input voltage detects a low input voltage by comparing the supply voltage to the internal reference voltage. On detection, the circuit fixes the OUT pin (pin 5) to low.

The system recovers when the supply voltage rises back above the threshold voltage of the malfunction prevention circuit.



### APPLICATION EXAMPLE

### NOTES ON USE

- Take account of common impedance when designing the earth line on a printed wiring board.
- Take measures against static electricity.
  - For semiconductors, use antistatic or conductive containers.
  - When storing or carrying a printed circuit board after chip mounting, put it in a conductive bag or container.
  - The work table, tools and measuring instruments must be grounded.
  - The worker must put on a grounding device containing 250 k $\Omega$  to 1 M $\Omega$  resistors in series.
- Do not apply a negative voltage
  - Applying a negative voltage of –0.3 V or less to an LSI may generate a parasitic transistor, resulting in malfunction.

Part number	Package	Remarks	
MB3800PF	8-pin Plastic SOP (FPT-8P-M01)	Conventional version	
MB3800PNF-ユロユ	8-pin Plastic SOP (FPT-8P-M02)	Conventional version	
MB3800PFV-DDD	8-pin Plastic SSOP (FPT-8P-M03)	Conventional version	
MB3800PF-□□□E1	8-pin Plastic SOP (FPT-8P-M01)	Lead Free version	
MB3800PNF-DDE1	8-pin Plastic SOP (FPT-8P-M02) Lead Free version		
MB3800PFV-DDDE1	8-pin Plastic SSOP (FPT-8P-M03)	Lead Free version	

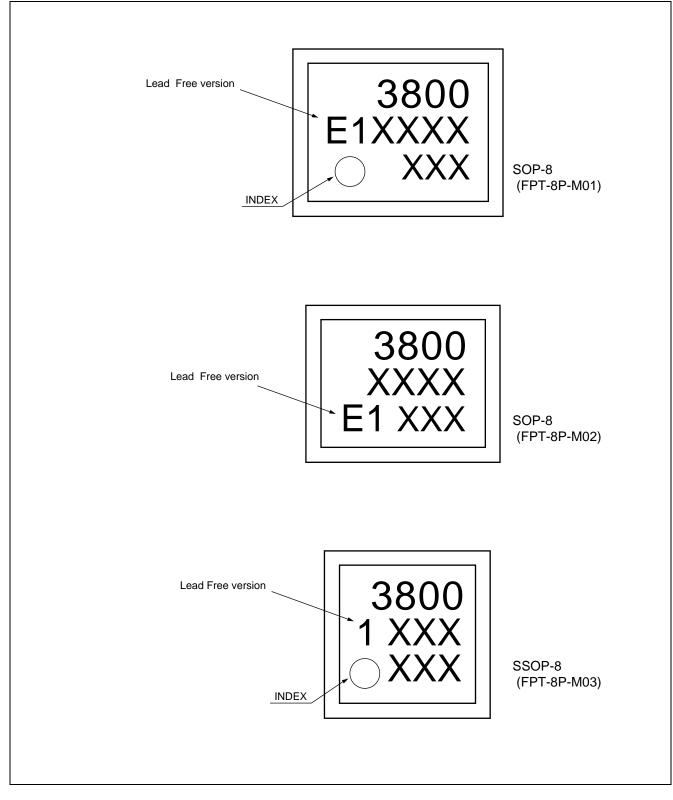
#### ORDERING INFORMATION

#### ■ RoHS Compliance Information of Lead (Pb) Free version

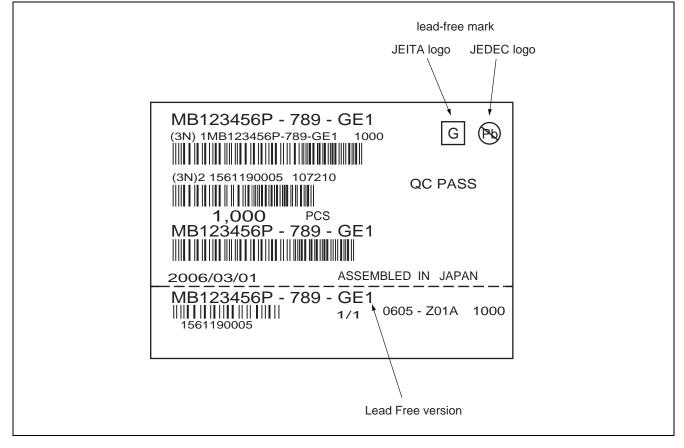
The LSI products of Fujitsu Microelectronics with "E1" are compliant with RoHS Directive , and has observed the standard of lead, cadmium, mercury, Hexavalent chromium, polybrominated biphenyls (PBB) , and polybrominated diphenyl ethers (PBDE) .

The product that conforms to this standard is added "E1" at the end of the part number.

### MARKING FORMAT (Lead Free version)



## ■ LABELING SAMPLE (Lead free version)

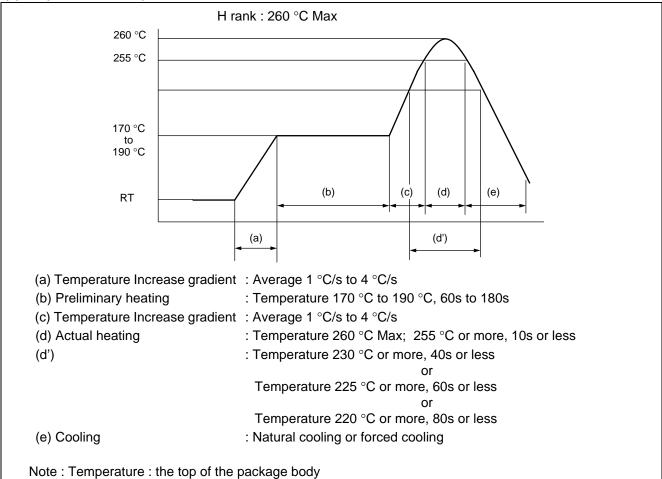


### ■ MB3800PF-□□E1, MB3800PNF-□□E1, MB3800PFV-□□E1 RECOMMENDED CONDITIONS of MOISTURE SENSITIVITY LEVEL

Item	Con	dition	
Mounting Method	IR (infrared reflow), Manual soldering (partial heating method)		
Mounting times	2 times		
Storage period	Before opening	Please use it within two years after Manufacture.	
	From opening to the 2nd reflow	Less than 8 days	
	When the storage period after opening was exceededPlease processes within 8 after baking (125 °C, 24		
Storage conditions	5 °C to 30 °C, 70%RH or less (the lowest possible humidity)		

#### [Temperature Profile for FJ Standard IR Reflow]

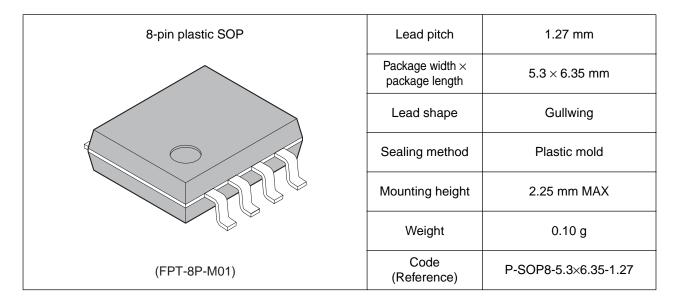
#### (1) IR (infrared reflow)

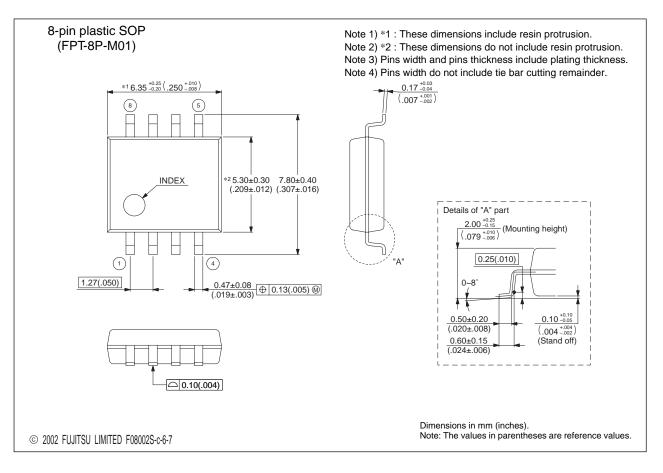


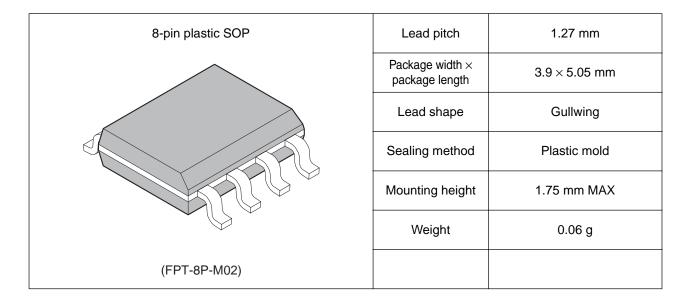
#### (2) Manual soldering (partial heating method)

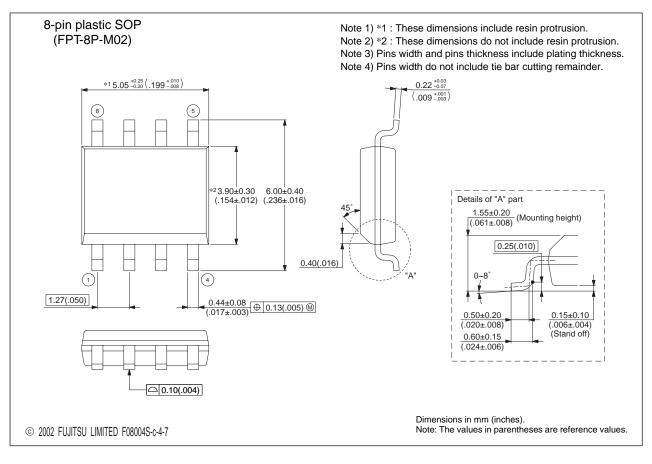
Conditions : Temperature 400 °C Max Times : 5 s max/pin

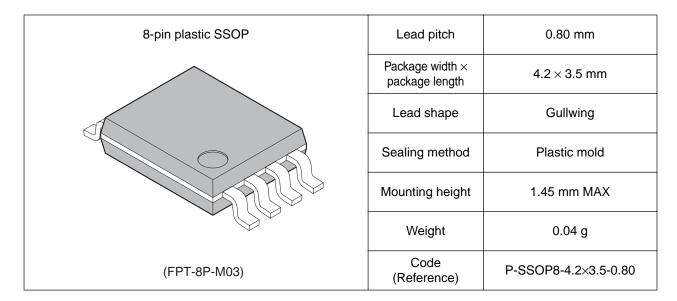
### PACKAGE DIMENSIONS

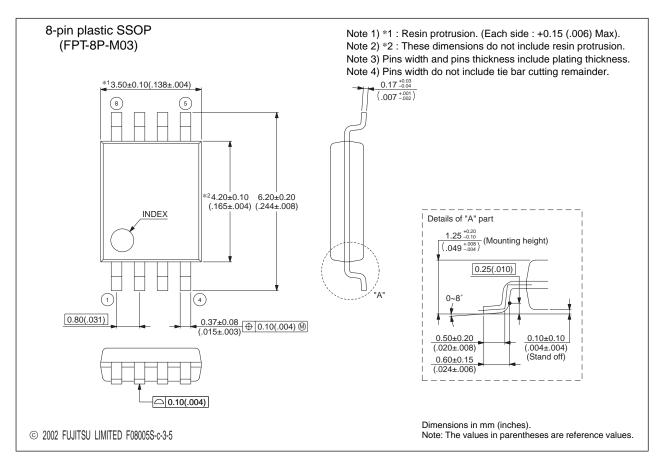












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