#### **ABSOLUTE MAXIMUM RATINGS**

V+, RS+, RS-, <del>PG</del> 0.3V to +36V	Or
OUT0.3V to (V+ + 0.3V)	1
Differential Input Voltage, VRS+ - VRS- ±700mV	Sto
Current into Any Pin ±50mA	Le
Continuous Power Dissipation ( $T_A = +70^{\circ}C$ )	
SO (derate 5.88mW/°C above +70°C)471mW	
μMAX (derate 4.10mW/°C above +70°C)330mW	

Operating Temperature Range	
MAX4172E_A	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering,	10sec)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS**

 $(V+ = +3V \text{ to } +32V; \text{ RS+}, \text{ RS-} = 0V \text{ to } 32V; \text{ T}_A = \text{T}_{MIN} \text{ to T}_{MAX}; \text{ unless otherwise noted. Typical values are at V+ = +12V, RS+ = 12V, TA = +25°C.})$ 

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Operating Voltage Range	V+			3		32	V
Input Voltage Range	V <sub>RS</sub> -			0		32	V
Supply Current	I <sub>V+</sub>	I <sub>OUT</sub> = 0mA			0.8	1.6	mA
Input Offset Voltage	Vos	V+ = 12V, V <sub>RS+</sub> = 12V	MAX4172ESA		±0.1	±0.75	mV
		V+ - 12V, VRS+ - 12V	MAX4172EUA		±0.2	±1.6	
		V <sub>RS+</sub> ≤ 2.0V			4		1
Positive Input Bias Current	I <sub>RS+</sub>	$V_{RS+} > 2.0V$ , $I_{OUT} = 0mA$		0	27	42.5	μΑ
	IKS+	$V_{RS+} \le 2.0V$ , $I_{OUT} = 0mA$		-325		42.5	
Negative Input Bias Current	Inc	$I_{RS}$ $V_{RS+} > 2.0V$ $V_{RS+} \le 2.0V$		0	50	85	μΑ
Negative input bias current	IKS-			-650		85	
Maximum V <sub>SENSE</sub> Voltage				150	175		mV
Low-Level Current Error		V <sub>SENSE</sub> = 6.25mV, V <sub>+</sub> = 12V, V <sub>RS+</sub> = 12V (Note 1)	MAX4172ESA			±8.0	μΑ
			MAX4172EUA			±15	
Output Current Error		Vsense = 100mV, V+ = 12V, V <sub>RS+</sub> = 12V	MAX4172ESA, T <sub>A</sub> = -40°C to 0°C			±20	- μΑ
			MAX4172EUA, T <sub>A</sub> = -40°C to 0°C			±50	
			MAX4172ESA, $T_A = 0^{\circ}C \text{ to } +85^{\circ}C$			±10	
			MAX4172EUA, T <sub>A</sub> = 0°C to +85°C			±15	
OUT Power-Supply Rejection Ratio	Δl <sub>OUT</sub> / ΔV+	3V ≤ V+ ≤ 32V, V <sub>RS+</sub> > 2.0V			0.2		μA/V
OUT Common-Mode Rejection Ratio	ΔI <sub>OUT</sub> / ΔV <sub>RS+</sub>	2.0V < V <sub>RS+</sub> < 32V			0.03		μΑ/V

#### **ELECTRICAL CHARACTERISTICS (continued)**

 $(V+=+3V \text{ to } +32V; \text{RS+, RS-} = 0V \text{ to } 32V; T_A=T_{MIN} \text{ to } T_{MAX}; \text{ unless otherwise noted. Typical values are at } V+=+12V, RS+=12V, T_A=+25^{\circ}C.)$ 

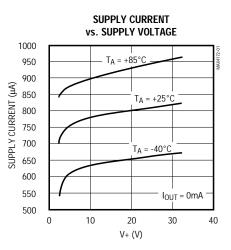
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Maximum Output Voltage (OUT)		I <sub>OUT</sub> ≤ 1.5mA				V+ - 1.2	V
Bandwidth		VSENSE = 100mV VSENSE = 6.25mV (Note 1)			800		- kHz
Danuwiuin					200		
Maximum Output Current	lout			1.5	1.75		mA
T	6	$G_{m} = I_{OUT} / (V_{RS+} - V_{RS-}),$	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	9.8	10	10.2	mA/V
Transconductance	Gm	$V_{SENSE} = 100$ mV, $V_{RS+} > 2.0$ V	$T_A = -40^{\circ}C$ to $0^{\circ}C$	9.7	10	10.3	
V+ Threshold for PG Output		V+ rising V+ falling			2.77		V
Low (Note 2)					2.67		- V
PG Output Low Voltage	V <sub>OL</sub>	I <sub>SINK</sub> = 1.2mA, V+ = 2.9V, T <sub>A</sub> = +25°C				0.4	V
Leakage Current into PG		V+ = 2.5V, T <sub>A</sub> = +25°C				1	μΑ
Power-Off Input Leakage Current (RS+, RS-)		V+ = 0V, V <sub>RS+</sub> = V <sub>RS-</sub> = 32V			0.1	1	μА
OUT Rise Time		V <sub>SENSE</sub> = 0mV to 100mV, 10% to 90%			400		ns
OUT Fall Time		V <sub>SENSE</sub> = 100mV to 0mV, 90% to 10%			800		ns
OUT Settling Time to 1%		I Vsense = 5mV to 100mV ⊢──	Rising		1.3		
			Falling		6		- µs
OUT Output Resistance		V <sub>SENSE</sub> = 150mV			20		МΩ

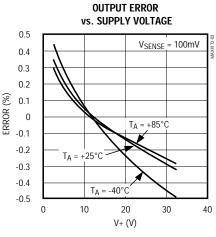
Note 1: 6.25mV = 1/16 of typical full-scale sense voltage (C/16).

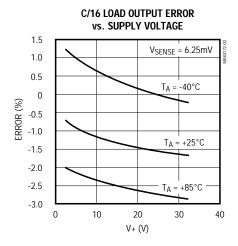
**Note 2:** Valid operation of the MAX4172 is guaranteed by design when  $\overline{PG}$  is low.

## Typical Operating Characteristics

 $(V + = +12V, V_{RS+} = 12V, R_{OUT} = 1k\Omega, T_A = +25^{\circ}C, unless otherwise noted.)$ 

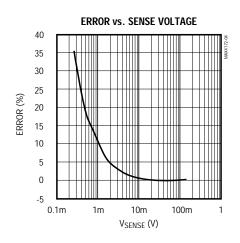


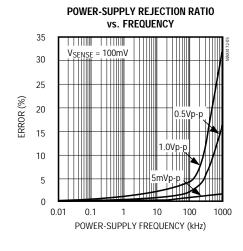


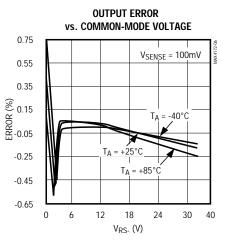


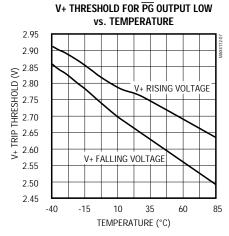
## Typical Operating Characteristics (continued)

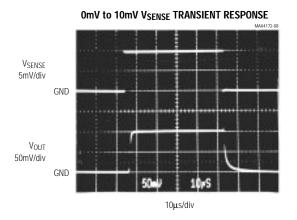
 $(V+ = +12V, V_{RS+} = 12V, R_{OUT} = 1k\Omega, T_A = +25^{\circ}C, unless otherwise noted.)$ 

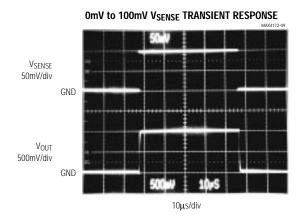








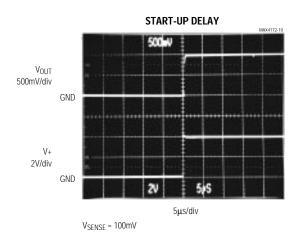


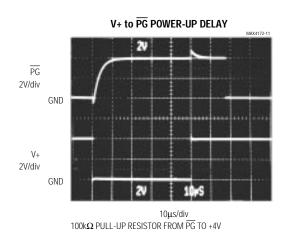


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### Typical Operating Characteristics (continued)

(V+ = +12V,  $V_{RS+}$  = 12V,  $R_{OUT}$  = 1k $\Omega$ ,  $T_A$  = +25°C, unless otherwise noted.)





### Pin Description

PIN	NAME	FUNCTION		
1	RS+	Power connection to the external sense resistor. The "+" indicates the direction of current flow.		
2	RS-	Load-side connection for the external sense resistor. The "-" indicates the direction of current flow.		
3, 4	N.C.	No Connect. No internal connection. Leave open or connect to GND.		
5	GND	Ground		
6	OUT	Current Output. OUT is proportional to the magnitude of the sense voltage ( $V_{RS+}$ - $V_{RS-}$ ). A 1k $\Omega$ resistor from OUT to ground will result in a voltage equal to 10V/V of sense voltage.		
7	PG	Power Good Open-Collector Logic Output. A low level indicates that V+ is sufficient to power the MAX4172, and adequate time has passed for power-on transients to settle out.		
8	V+	Supply Voltage Input for the MAX4172		

## Detailed Description

The MAX4172 is a unidirectional, high-side current-sense amplifier with an input common-mode range that is independent of supply voltage. This feature not only allows the monitoring of current flow into a battery in deep discharge, but also enables high-side current sensing at voltages far in excess of the supply voltage (V+).

The MAX4172 current-sense amplifier's unique topology simplifies current monitoring and control. The MAX4172's amplifier operates as shown in Figure 1. The battery/load current flows through the external sense resistor (RSENSE), from the RS+ node to the RS-

node. Current flows through  $R_{G1}$  and Q1, and into the current mirror, where it is multiplied by a factor of 50 before appearing at OUT.

To analyze the circuit of Figure 1, assume that current flows from RS+ to RS-, and that OUT is connected to GND through a resistor. Since A1's inverting input is high impedance, no current flows though  $R_{G2}$  (neglecting the input bias current), so A1's negative input is equal to Vsource - (ILOAD x RSENSE). A1's open-loop gain forces its positive input to essentially the same voltage level as the negative input. Therefore, the drop across  $R_{G1}$  equals  $I_{LOAD}$  x  $R_{SENSE}$ . Then, since  $I_{RG1}$ 

flows through RG1, IRG1 x RG1 = ILOAD x RSENSE. The internal current mirror multiplies IRG1 by a factor of 50 to give IOUT =  $50 \times IRG1$ . Substituting IOUT /  $50 \times IRG1$ , (IOUT /  $50 \times IRG1$ ) x RG1 = ILOAD x RSENSE, or:

The internal current gain of 50 and the factory-trimmed resistor  $R_{G1}$  combine to result in the MAX4172 transconductance  $(G_m)$  of 10mA/V.  $G_m$  is defined as being equal to lout / (VRS+ - VRS-). Since (VRS+ - VRS-) = ILOAD x RSENSE, the output current (IOUT) can be calculated with the following formula:

$$IOUT = Gm x (VRS+ - VRS-) = (10mA/V) x (ILOAD x RSENSE)$$

#### **Current Output**

The output voltage equation for the MAX4172 is given below:

where  $V_{OUT}$  = the desired full-scale output voltage,  $I_{LOAD}$  = the full-scale current being sensed,  $R_{SENSE}$  = the current-sense resistor,  $R_{OUT}$  = the voltage-setting resistor, and  $G_m$  = MAX4172 transconductance (10mA/V).

The full-scale output voltage range can be set by changing the R<sub>OUT</sub> resistor value, but the output voltage must be no greater than V+ - 1.2V. The above equation can be modified to determine the R<sub>OUT</sub> required for a particular full-scale range:

$$ROUT = (VOUT) / (ILOAD \times RSENSE \times Gm)$$

OUT is a high-impedance current source that can be integrated by connecting it to a capacitive load.

#### **PG** Output

The PG output is an open-collector logic output that indicates the status of the MAX4172's V+ power supply. A logic low on the  $\overline{PG}$  output indicates that V+ is sufficient to power the MAX4172. This level is temperature dependent (see Typical Operating Characteristics graphs), and is typically 2.7V at room temperature. The internal PG comparator has a 100mV (typical) hysteresis to prevent possible oscillations caused by repeated toggling of the PG output, making the device ideal for power-management systems lacking soft-start capability. An internal delay (15µs typical) in the PG comparator allows adequate time for power-on transients to settle out. The PG status indicator greatly simplifies the design of closed-loop systems by ensuring that the components in the control loop have sufficient voltage to operate correctly.

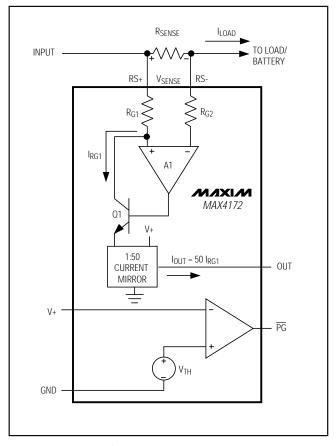


Figure 1. Functional Diagram

## Applications Information

# Suggested Component Values for Various Applications

The *Typical Operating Circuit* is useful in a wide variety of applications. Table 1 shows suggested component values and indicates the resulting scale factors for various applications required to sense currents from 100mA to 10A.

Adjust the RSENSE value to monitor higher or lower current levels. Select RSENSE using the guidelines and formulas in the following section.

#### Sense Resistor, Rsense

Choose RSENSE based on the following criteria:

 Voltage Loss: A high RSENSE value causes the power-source voltage to degrade through IR loss. For minimal voltage loss, use the lowest RSENSE value.

Table 1. Suggested Component Values

FULL-SCALE LOAD CURRENT (A)	CURRENT-SENSE RESISTOR, RSENSE $(m\Omega)$	OUTPUT RESISTOR, ROUT ( $k\Omega$ )	FULL-SCALE OUTPUT VOLTAGE, V <sub>OUT</sub> (V)	SCALE FACTOR, Vout/Isense (V/A)
0.1	1000	3.48	3.48	34.8
1	100	3.48	3.48	3.48
5	20	3.48	3.48	0.696
10	10	3.48	3.48	0.348

- Accuracy: A high Rsense value allows lower currents to be measured more accurately. This is because offsets become less significant when the sense voltage is larger. For best performance, select Rsense to provide approximately 100mV of sense voltage for the full-scale current in each application.
- Efficiency and Power Dissipation: At high current levels, the I<sup>2</sup>R losses in R<sub>SENSE</sub> can be significant. Take this into consideration when choosing the resistor value and its power dissipation (wattage) rating. Also, the sense resistor's value might drift if it is allowed to heat up excessively.
- Inductance: Keep inductance low if ISENSE has a large high-frequency component. Wire-wound resistors have the highest inductance, while metal film is somewhat better. Low-inductance metal-film resistors are also available. Instead of being spiral wrapped around a core, as in metal-film or wirewound resistors, they are a straight band of metal and are available in values under  $1\Omega$ .
- **Cost:** If the cost of RSENSE is an issue, you might want to use an alternative solution, as shown in Figure 2. This solution uses the PC board traces to create a sense resistor. Because of the inaccuracies of the copper resistor, the full-scale current value must be adjusted with a potentiometer. Also, copper's resistance temperature coefficient is fairly high (approximately 0.4%/°C).

In Figure 2, assume that the load current to be measured is 10A, and that you have determined a 0.3-inchwide, 2-ounce copper to be appropriate. The resistivity of 0.1-inch-wide, 2-ounce (70µm thickness) copper is  $30m\Omega/\text{ft}$ . For 10A, you might want RSENSE =  $5m\Omega$  for a 50mV drop at full scale. This resistor requires about 2 inches of 0.1-inch-wide copper trace.

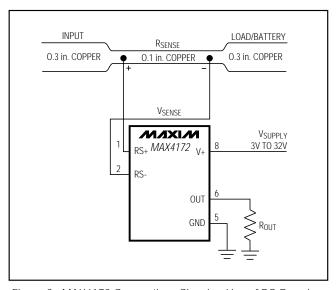


Figure 2. MAX4172 Connections Showing Use of PC Board

# Current-Sense Adjustment (Resistor Range, Output Adjust)

Choose R<sub>OUT</sub> after selecting R<sub>SENSE</sub>. Choose R<sub>OUT</sub> to obtain the full-scale voltage you require, given the full-scale I<sub>OUT</sub> determined by R<sub>SENSE</sub>. OUT's high impedance permits using R<sub>OUT</sub> values up to  $200k\Omega$  with minimal error. OUT's load impedance (e.g., the input of an op amp or ADC) must be much greater than R<sub>OUT</sub> (e.g., 100 x R<sub>OUT</sub>) to avoid degrading measurement accuracy.

#### **High-Current Measurement**

The MAX4172 can achieve high-current measurements by using low-value sense resistors, which can be paralleled to further increase the current-sense limit. As an alternative, PC board traces can be adjusted over a wide range.

#### **Power-Supply Bypassing and Grounding**

In most applications, grounding the MAX4172 requires no special precautions. However, in high-current systems, large voltage drops can develop across the ground plane, which can add to or subtract from V<sub>OUT</sub>. Use a single-point star ground for the highest current-measurement accuracy.

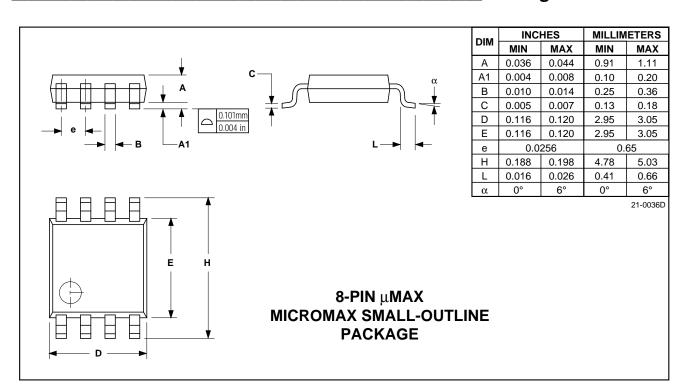
The MAX4172 requires no special bypassing and responds quickly to transient changes in line current. If the noise at OUT caused by these transients is a problem, you can place a 1µF capacitor at the OUT pin to ground. You can also place a large capacitor at the RS terminal (or load side of the MAX4172) to decouple the load, reducing the current transients. These capacitors are not required for MAX4172 operation or stability. The RS+ and RS- inputs can be filtered by placing a capacitor (e.g., 1µF) between them to average the sensed current.

## \_\_Chip Information

**TRANSISTOR COUNT: 177** 

SUBSTRATE CONNECTED TO GND

### \_Package Information



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