

Low-Resistance DPDT Switches with Negative Rail

Detailed Description

The MAX14535E–MAX14539E are low on-resistance and high ESD-protected single DPDT switches that operate from a +2.4V to +5.5V supply and are designed to multiplex AC-coupled analog signals. These switches combine the low on-capacitance (CON) and low on-resistance (RON) necessary for high-performance switching applications. The negative signal capability of the analog channel allows signals below ground to pass through without distortion.

Analog Signal Levels

The MAX14535E–MAX14539E are bidirectional, allowing NO₋, NC₋, and COM₋ to be configured as either inputs or outputs. Note that NC₋ and NO₋ are only protected against ESD up to ±2kV (Human Body Model) and may require additional ESD protection if used as outputs. These devices feature a charge pump that generates a negative supply to allow analog signals as low as -1.5V to pass through NO₋, NC₋, or COM₋. This allows AC-coupled signals that drop below ground to pass even when operating from a 3.0V to 5.5V supply. For the MAX14537E/MAX14538E, the negative charge pump is controlled by the enable input and is active when EN is high. When EN is driven low, the negative charge pump is disabled, which puts the devices in the lowest possible current consumption, and the signal range is 0 to V_{CC}. The negative charge pump is always active for the MAX14535E/MAX14536E/MAX14539E, therefore, a negative signal (at most -1.5V) can be applied through NC₋, NO₋, or COM₋, even when EN is driven low. A negative rail signal (signal voltage < 0) must not be applied to the switch unless the negative charge pump is active.

Digital Control Input

The MAX14535E–MAX14539E provide a single-bit control logic input, CB. CB controls the switch position as shown in the *Functional Diagrams*. Drive CB rail-to-rail to minimize power consumption.

Enable Input

The MAX14535E–MAX14539E feature a shutdown mode that reduces the supply current (less than 1μA for MAX14537E/MAX14538E) and places the switches in high impedance. Drive EN low to place the device in shutdown mode. Drive EN high for normal operation.

Shunt Resistors

(MAX14535E/MAX14537E/MAX14539E)

When EN is high, the shunt resistors are controlled by CB. When CB is low, NC₋ is connected to COM₋ and NO₋ is connected to shunt resistors. When CB is high, NO₋ is connected to COM₋ and NC₋ is connected to shunt resistors (MAX14539E). When EN is low, all the switches are open and all the shunt resistors are active.

Click-and-Pop Suppression

The 500Ω shunt resistors on the MAX14535E/MAX14537E/MAX14539E automatically discharge any capacitance at the NO₋ terminals (or NC₋ terminals, MAX14539E) when they are unconnected from COM₋. This reduces audio click-and-pop sounds that may occur when switching between capacitively coupled audio sources.

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Applications Information

Extended ESD Protection

ESD protection structures are incorporated on all pins to protect against electrostatic discharges up to $\pm 2\text{kV}$ (HBM) encountered during handling and assembly. COM1 and COM2 are further protected against ESD up to $\pm 15\text{kV}$ (HBM) without damage. The ESD structures withstand high ESD both in normal operation and when the device is powered down. After an ESD event, the MAX14535E-MAX14539E continue to function without latchup.

ESD Test Conditions

ESD performance depends on a variety of conditions. Contact Maxim for a reliability report that documents test setup, test methodology, and test results.

Human Body Model

Figure 3a shows the Human Body Model. Figure 3b shows the current waveform it generates when discharged into a low impedance. This model consists of a 100pF capacitor charged to the ESD voltage of interest that is then discharged into the device through a $1.5\text{k}\Omega$ resistor.

IEC 61000-4-2

The main difference between tests done using the Human Body Model and IEC 61000-4-2 is higher peak current in the IEC 61000-4-2. Because series resistance is lower in the IEC 61000-4-2 ESD test model (Figure 3c) the ESD withstand voltage measured using the Human Body Model. Figure 3d shows the current waveform for the $\pm 8\text{kV}$ IEC 61000-4-2 Level 4 ESD Contact Discharge test.

The Air-Gap Discharge test involves approaching the device with a charged probe. The Contact Discharge method connects the probe to the device before the probe is energized.

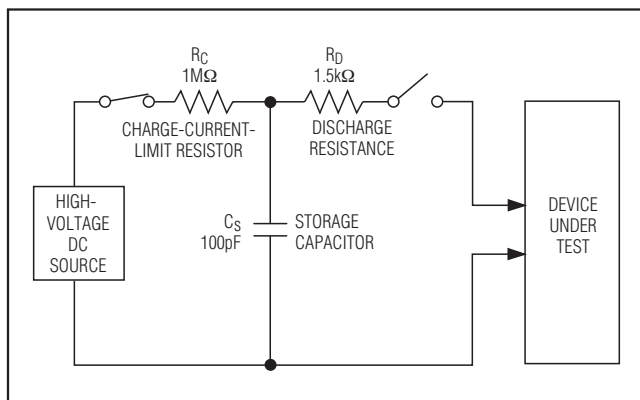


Figure 3a. Human Body ESD Test Model

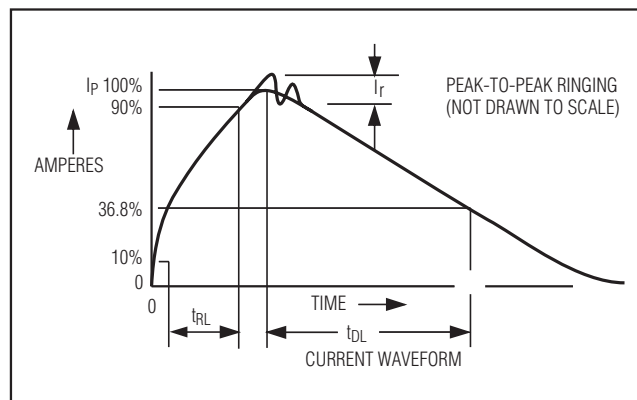


Figure 3b. Human Body Current Waveform

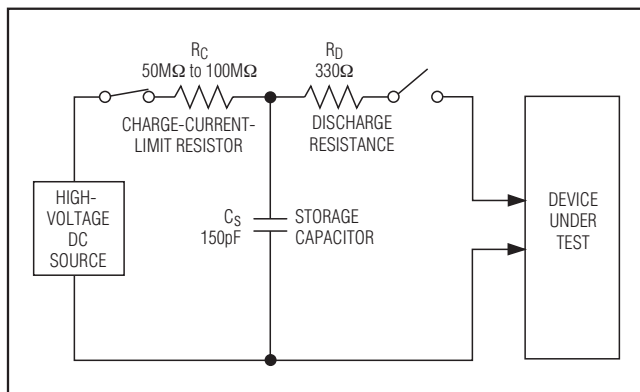


Figure 3c. IEC 61000-4-2 ESD Test Model

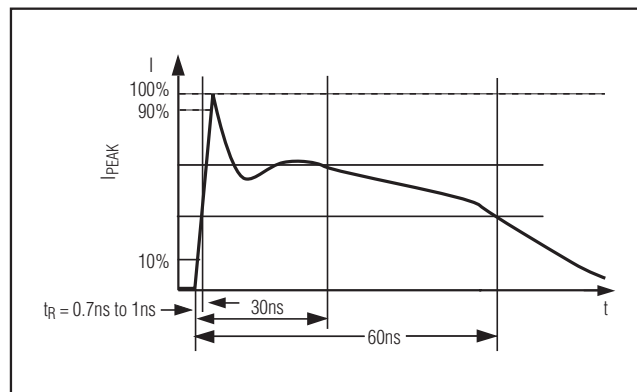


Figure 3d. IEC 61000-4-2 ESD Generator Current Waveform

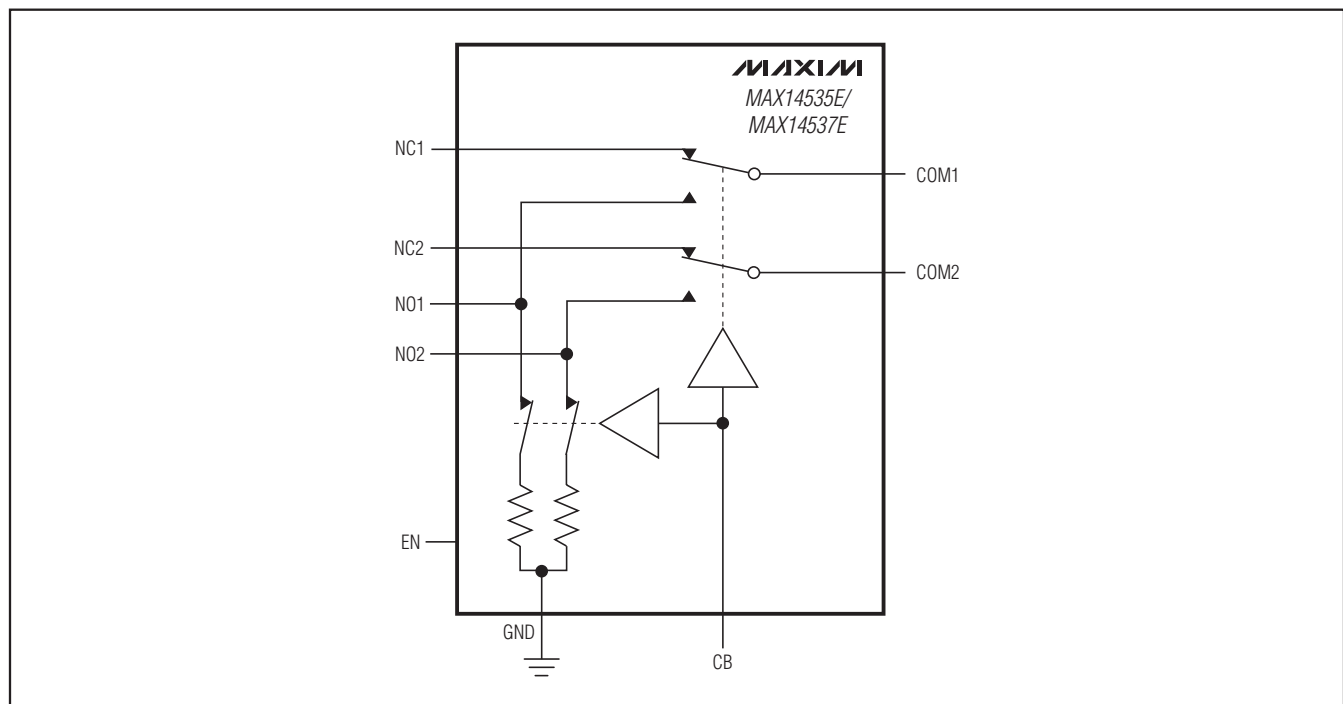
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Power-Supply Sequencing

Caution: Do not exceed the absolute maximum ratings because stresses beyond the listed ratings may cause permanent damage to the device.

Proper power-supply sequencing is recommended for all devices. Apply VCC before applying analog signals, especially if the analog signal is not current limited.

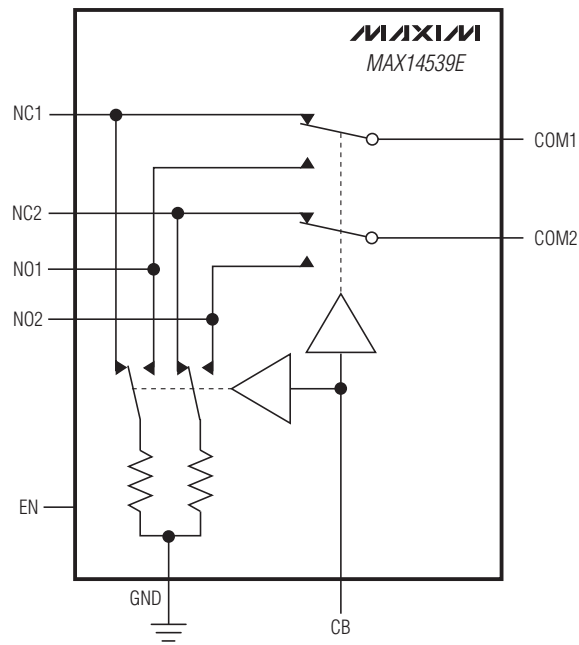
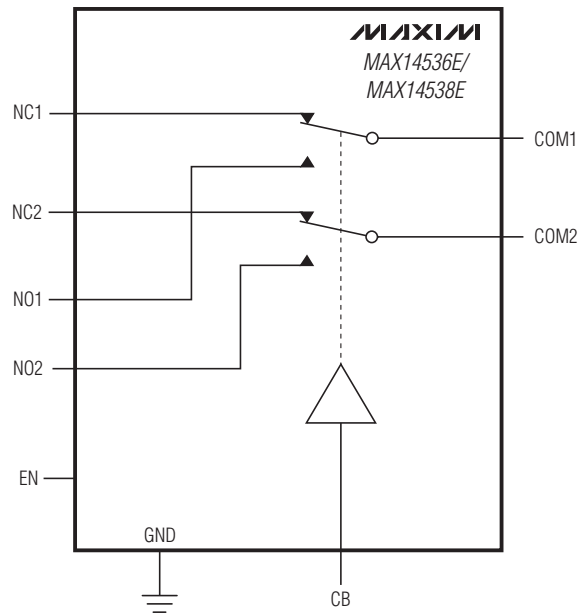
Functional Diagrams



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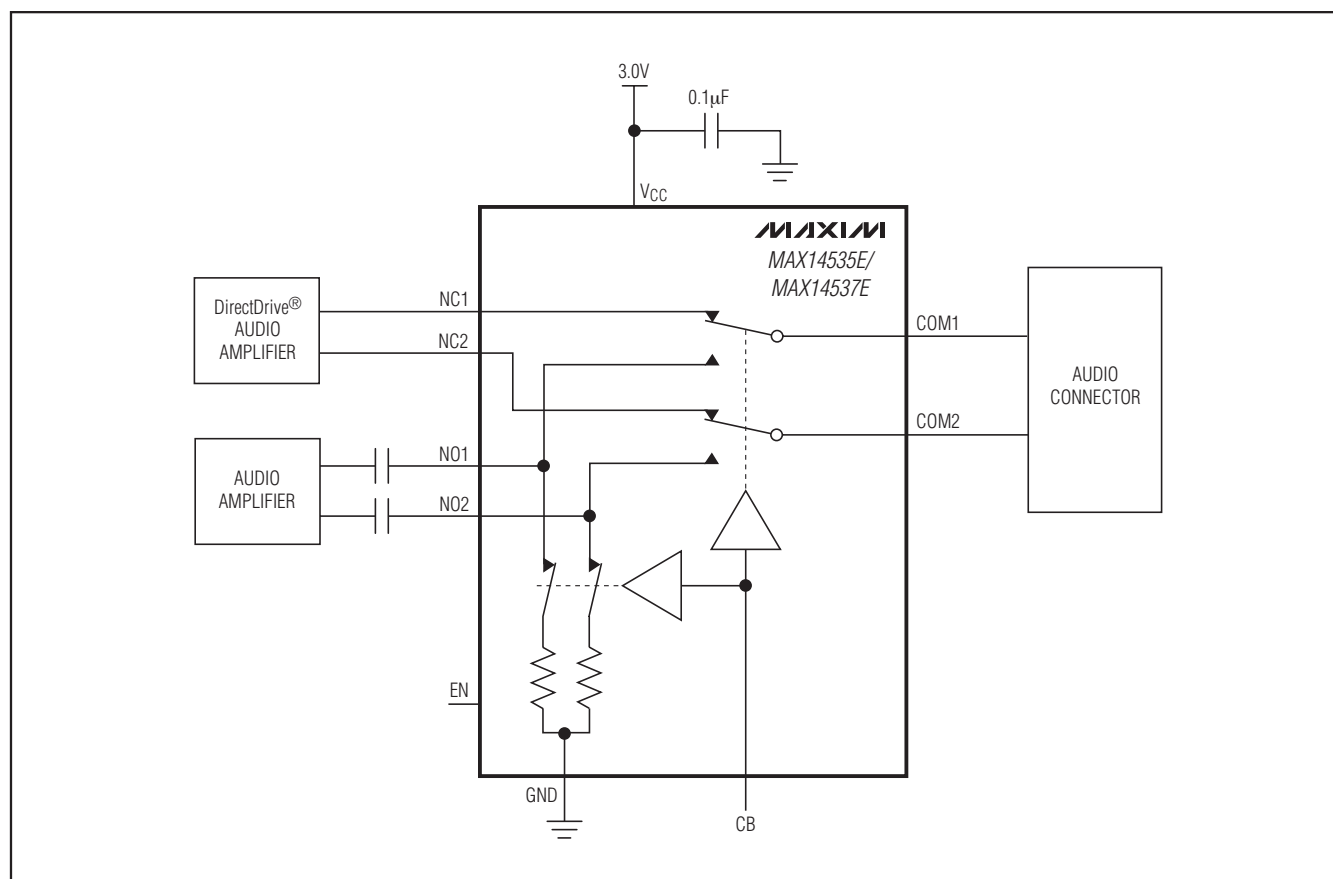
Functional Diagrams (continued)

MAX14535E-MAX14539E



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Typical Operating Circuits

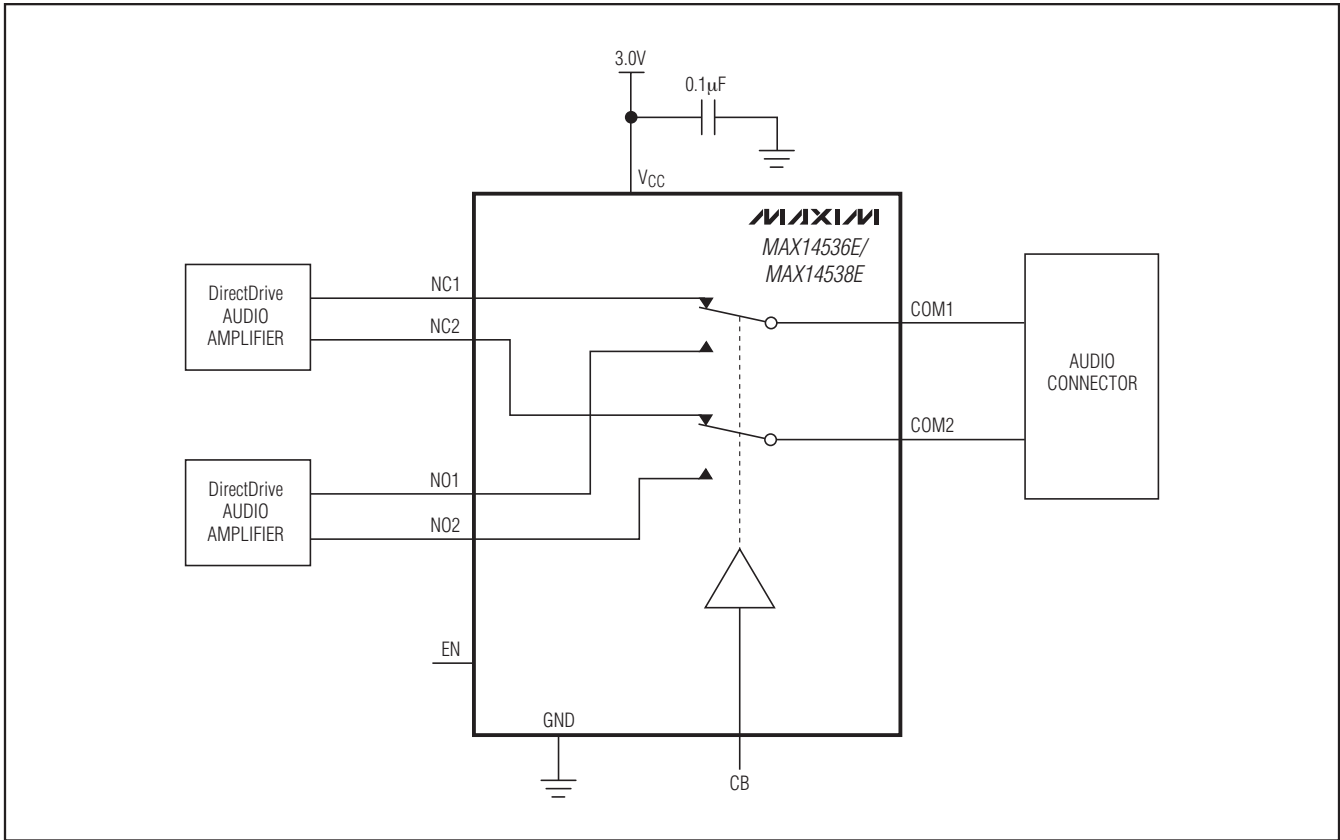


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Typical Operating Circuits (continued)

MAX14535E-MAX14539E



Chip Information

PROCESS: BiCMOS

Package Information

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
10 UTQFN	V101A1CN+1	21-0028

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Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	2/09	Initial release	—
1	4/09	Removed future product asterisk for MAX14536E and updated <i>Electrical Characteristics</i> table.	1

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