# LB1909MC

#### Allowable Operating Range at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V <sub>CC</sub>		2.5 to 16	V
Input high level voltage	VIH	Pins ENA, IN1, IN2	1.8 to 10	V
Input low level voltage	VIL		-0.3 to +0.7	V

#### **Electrical Characteristics** at $Ta = 25^{\circ}C$ , $V_{CC} = 12V$

Parameter	Querchiel	Oradikiana		1.1				
Parameter	Symbol	Conditions	min	typ	max	Unit		
Power source current	ICC0	ENA = L		0.1	10	μA		
	I <sub>CC</sub> 1	ENA = H		25	35	mA		
Output saturation voltage	VOUT1	I <sub>OUT</sub> = 200mA		0.25	0.35	V		
	V <sub>OUT</sub> 2	I <sub>OUT</sub> = 400mA		0.50	0.75	V		
Input current	IIN	V <sub>IN</sub> = 5V		120	160	μΑ		
Thermal protection block *1								
Thermal shutdown operation temperature	Ttsd	Design guarantee *2		180		°C		
Temperature hysteresis width	∆Ttsd			60		°C		
Spark killer diode								
Reverse current	I <sub>S</sub> (leak)				30	μΑ		
orward voltage	V <sub>SF</sub>	I <sub>OUT</sub> = 400mA			1.7	V		

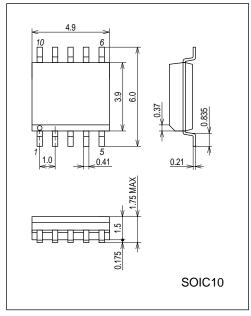
\*1 The thermal protection function is a feature to prevent the product from smoking and firing under unusual conditions. It is not intended to guarantee operation of the product under an ambient temperature exceeding the operating temperature range.

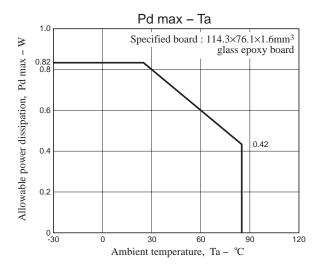
\*2 Design guarantee is not tested in individual units.

# **Package Dimensions**

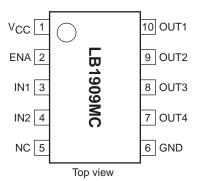
unit : mm (typ)

3426A





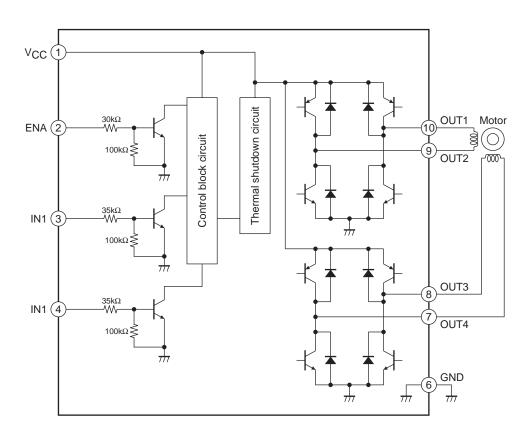
### Pin Assignment



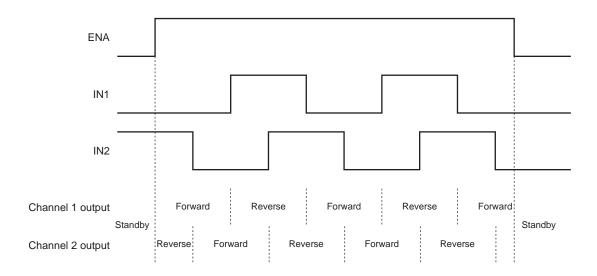
### Truth table

Input		Output				Demeric		
ENA	IN1	IN2	OUT1	OUT2	OUT3	OUT4	Remarks	
L	×	×	OFF	OFF	OFF	OFF	Standby mode	
	L		Н	H L		Forward		
н	н		L	н			Channel 1	Reverse
		L			н	L		Forward
		н			L	н	Channel 2	Reverse

## **Block Diagram**



#### Timing Chart (2 phase excitation drive)



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal