

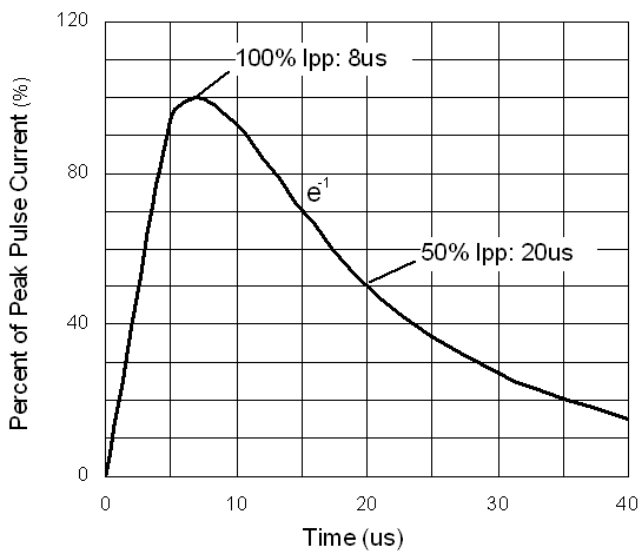


Figure 1. 8/20 us pulse waveform according to IEC 61000-4-5

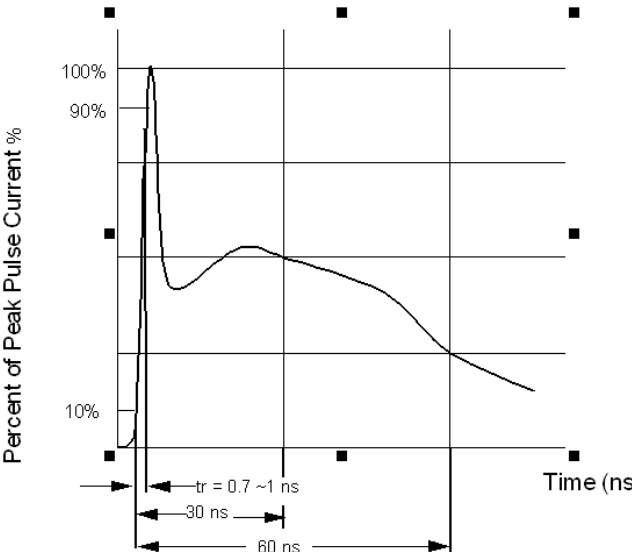


Figure 2. ESD pulse waveform according to IEC 61000-4-2

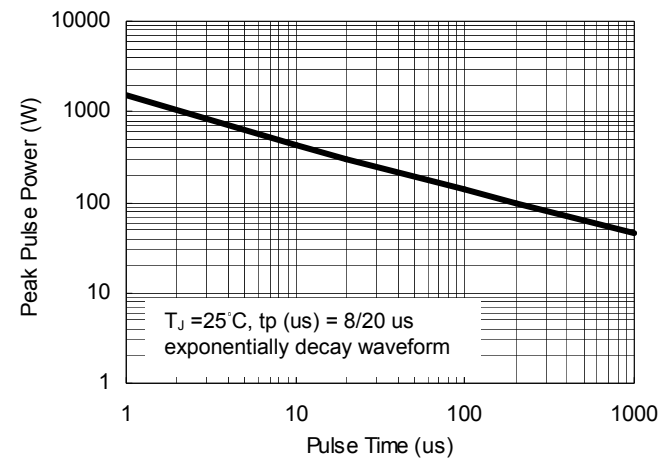


Figure 3. Power Dissipation versus Pulse Time

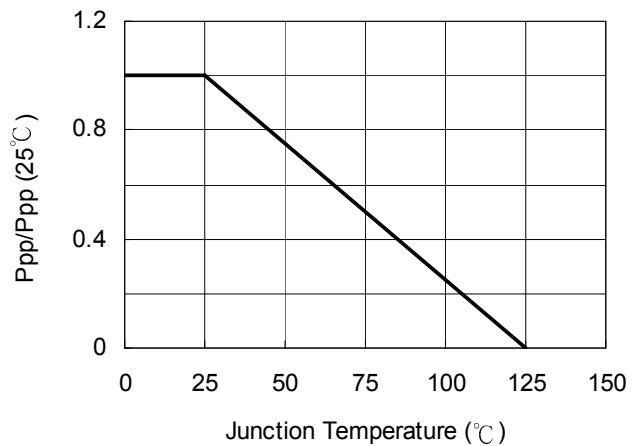


Figure 4. Peak pulse power versus T_j

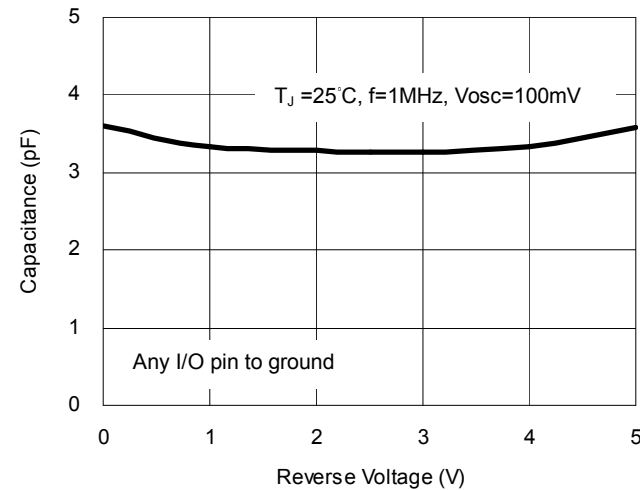


Figure 5. Typical Junction Capacitance

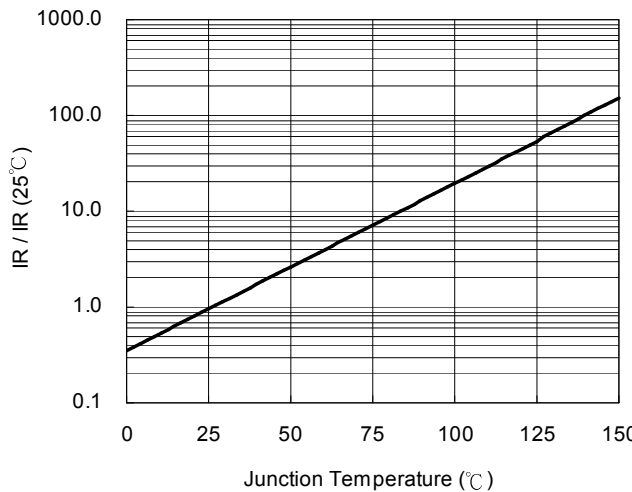


Figure 6. Reverse Leakage Current versus T_j

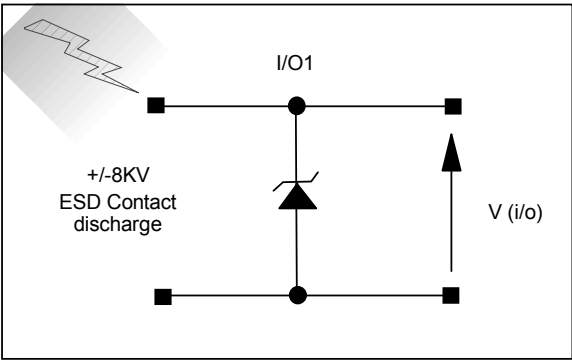


Figure 7. ESD Test Configuration

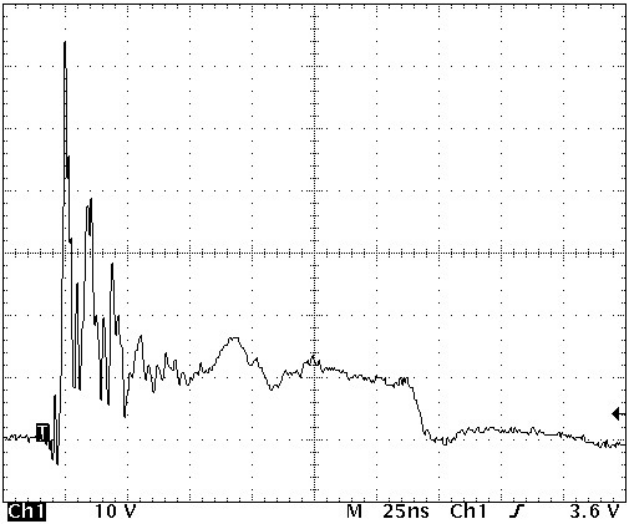


Figure 8. Clamped +8 kV ESD voltage waveform

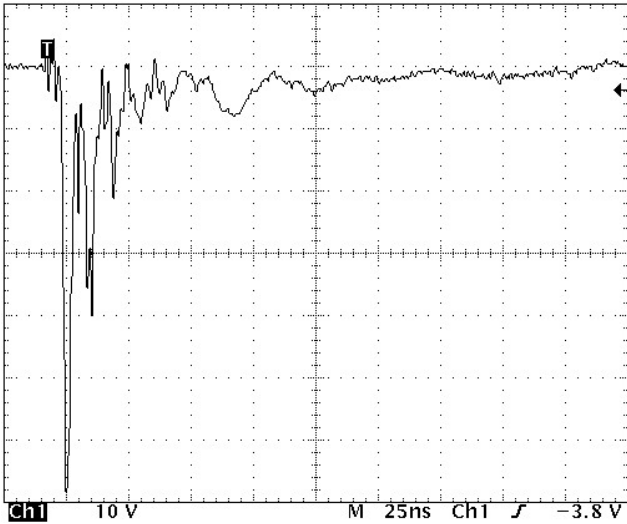


Figure 9. Clamped -8 kV ESD voltage waveform

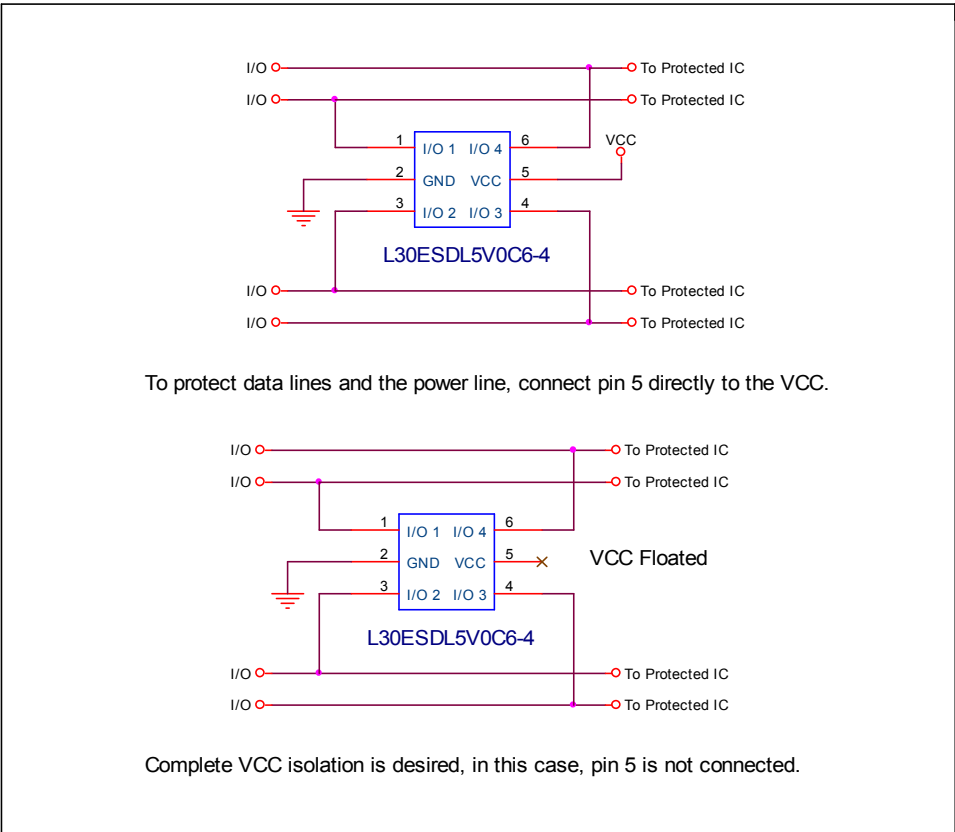


Figure 10. Device Connection Options

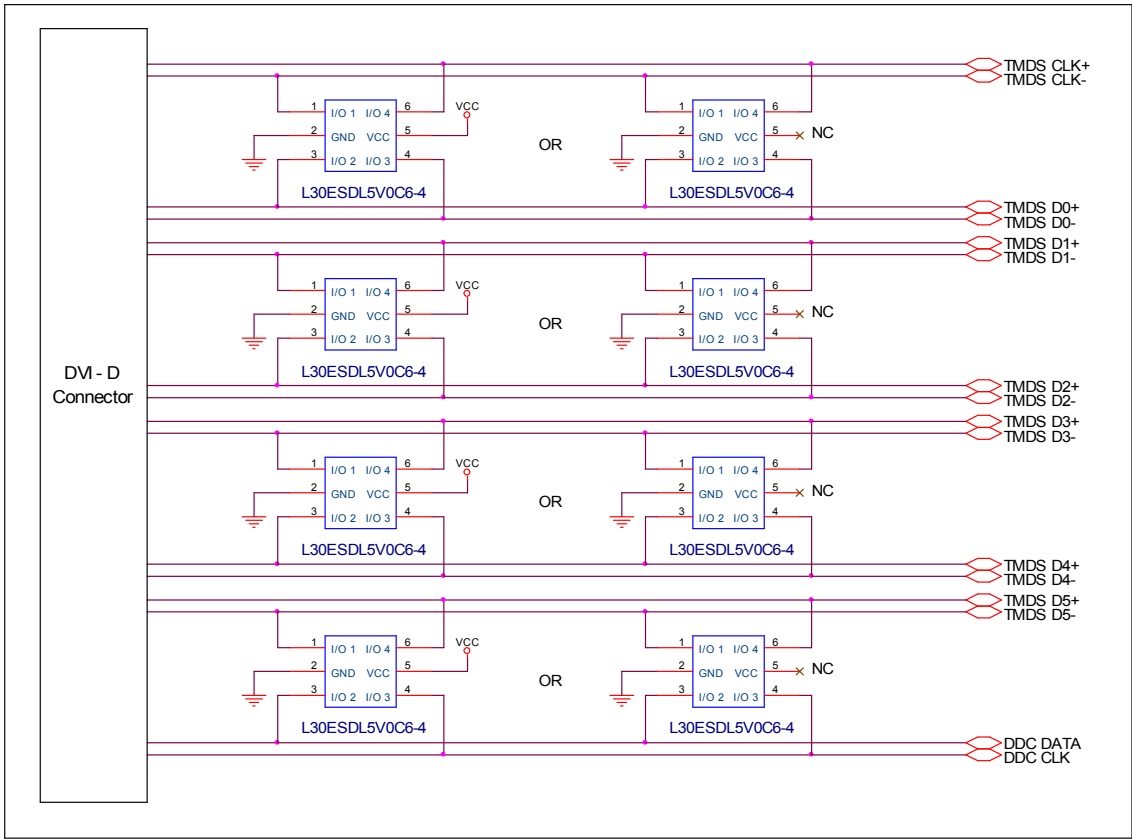


Figure 11. DVI Interface ESD Protection

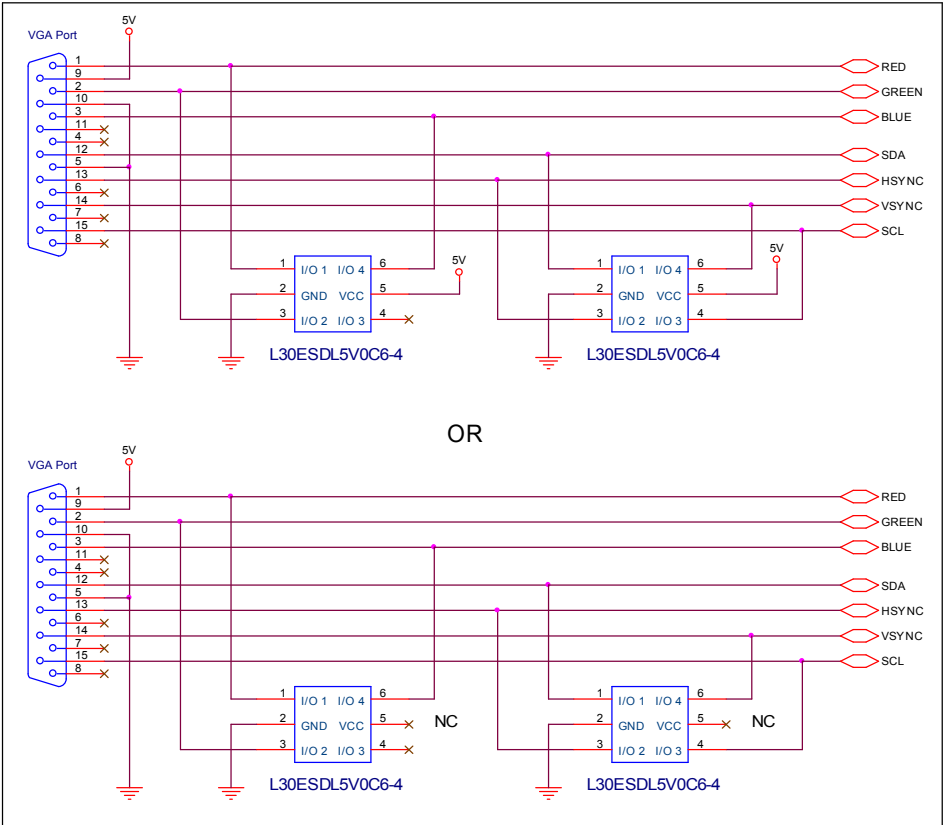


Figure 12. VGA Interface ESD Protection

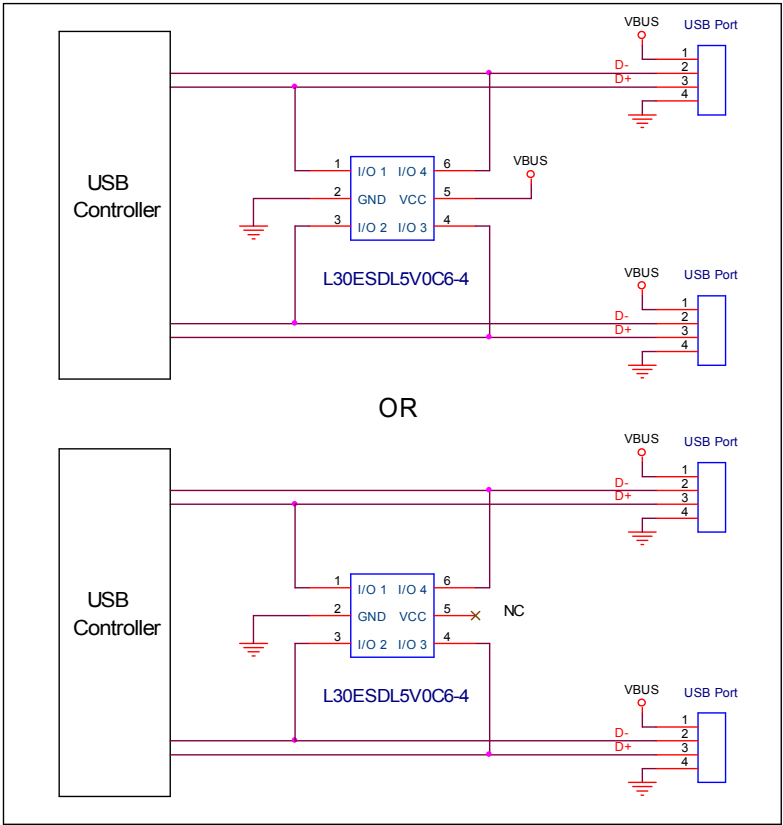
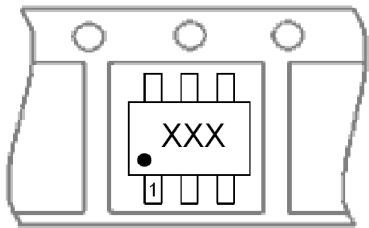


Figure 13. USB2.0 Interface ESD Protection



Marking & Orientation

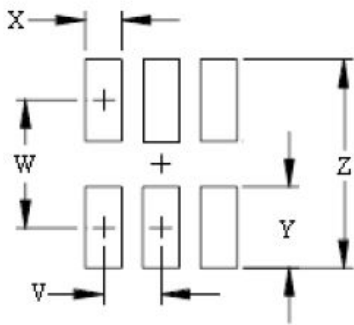


DEVICE	Wire	Marking	Packing
L30ESDL5V0C6-4	Au	VC5	Taping / Reel

Packaging Information

DEVICE	Q'TY/REEL (PCS)	REEL DIA. (INCH)	Q'TY/BOX (PCS)	Q'TY/CARTON (PCS)
L30ESDL5V0C6-4	3000	7	45000	90K/180K

SOT23-6L Soldering Pad Layout



Dim.	Millimeters	Inches
Z	3.60	0.141
X	0.80	0.031
W	2.60	0.102
Y	1.00	0.039
V	0.95	0.037

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