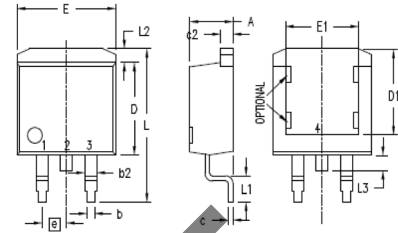


Symbol	Test Conditions	Characteristic Values ($T_j = 25^\circ\text{C}$ unless otherwise specified)		
		Min.	Typ.	Max.
g_{fs}	$V_{DS} = 10\text{ V}; I_D = 0.5 I_{D25}$, pulse test	5	8	S
C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 25\text{ V}, f = 1\text{ MHz}$		1050	pF
C_{oss}			120	pF
C_{rss}			12	pF
$t_{d(on)}$	$V_{GS} = 10\text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = I_{D25}$ $R_G = 18\ \Omega$ (External)		22	ns
t_r			28	ns
$t_{d(off)}$			65	ns
t_f			23	ns
$Q_{g(on)}$	$V_{GS} = 10\text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}$		20	nC
Q_{gs}			7	nC
Q_{gd}			7	nC
R_{thJC}	(TO-220)		0.25	0.83°C/W
R_{thCS}				$^\circ\text{C/W}$

Source-Drain Diode

Symbol	Test Conditions	Characteristic Values ($T_j = 25^\circ\text{C}$, unless otherwise specified)		
		Min.	Typ.	Max.
I_S	$V_{GS} = 0\text{ V}$			8 A
I_{SM}	Repetitive			14 A
V_{SD}	$I_F = I_S, V_{GS} = 0\text{ V}$, Pulse test, $t \leq 300\ \mu\text{s}$, duty cycle $d \leq 2\%$			1.5 V
t_{rr}	$I_F = 8\text{ A}, V_{GS} = 0\text{ V}, V_R = 100\text{ V}$ $-di/dt = 100\text{ A}/\mu\text{s}$		400	ns

TO-263 (IXTA) Outline



1. GATE
2. DRAIN (COLLECTOR)
3. SOURCE (EMITTER)
4. DRAIN (COLLECTOR) BOTTOM SIDE

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.160	.190	4.06	4.83
A1	.080	.110	2.03	2.79
b	.020	.039	0.51	0.99
b2	.045	.055	1.14	1.40
c	.016	.029	0.40	0.74
c2	.045	.055	1.14	1.40
D	.340	.380	8.64	9.65
D1	.315	.350	8.00	8.89
E	.380	.410	9.65	10.41
E1	.245	.320	6.22	8.13
e	.100 BSC		2.54 BSC	
L	.575	.625	14.61	15.88
L1	.090	.110	2.29	2.79
L2	.040	.055	1.02	1.40
L3	.050	.070	1.27	1.78
L4	0	.005	0	0.13

TO-220 (IXTP) Outline

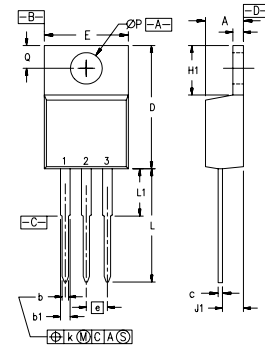


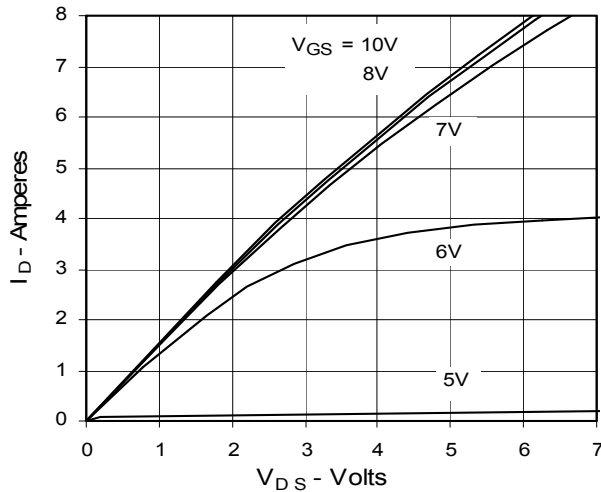
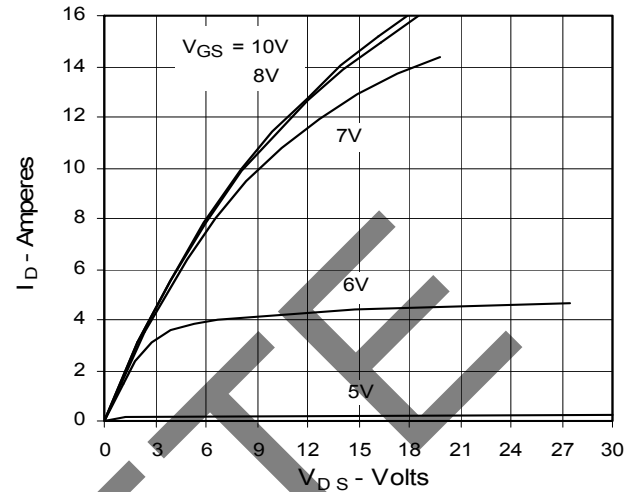
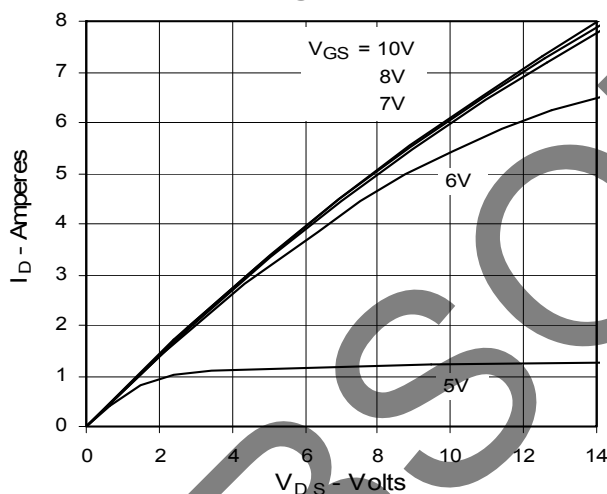
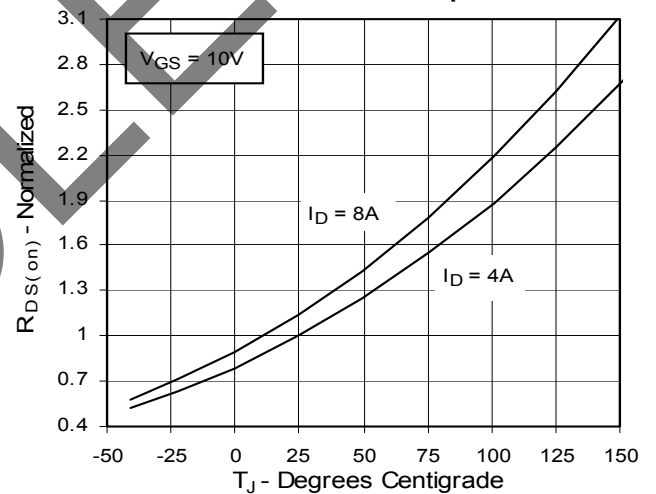
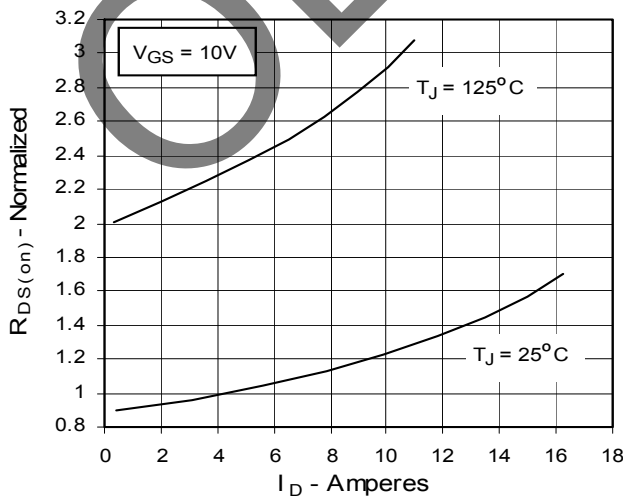
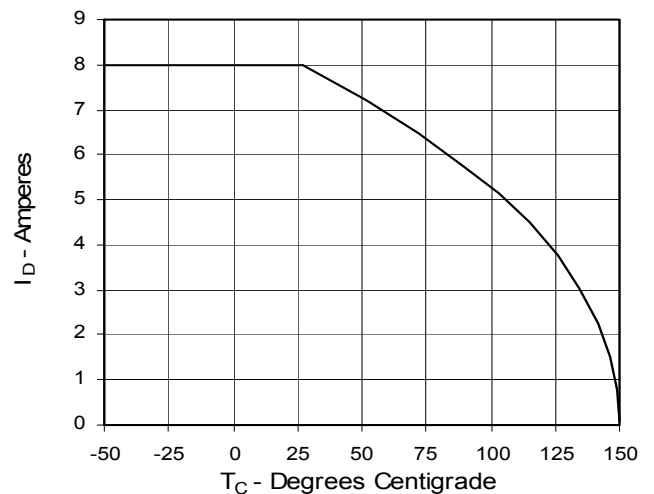
Fig. 1. Output Characteristics
@ 25°C

Fig. 2. Extended Output Characteristics
@ 25°C

Fig. 3. Output Characteristics
@ 125°C

Fig. 4. $R_{DS(on)}$ Normalized to 0.5 I_{D25} Value vs. Junction Temperature

Fig. 5. $R_{DS(on)}$ Normalized to 0.5 I_{D25} Value vs. I_D

Fig. 6. Drain Current vs. Case Temperature


Fig. 7. Input Admittance

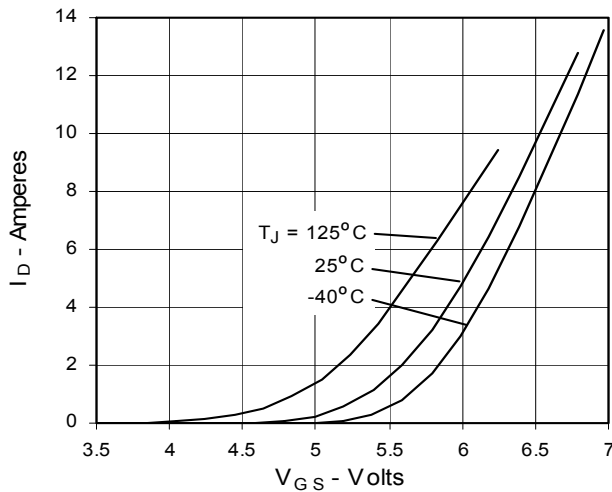


Fig. 8. Transconductance

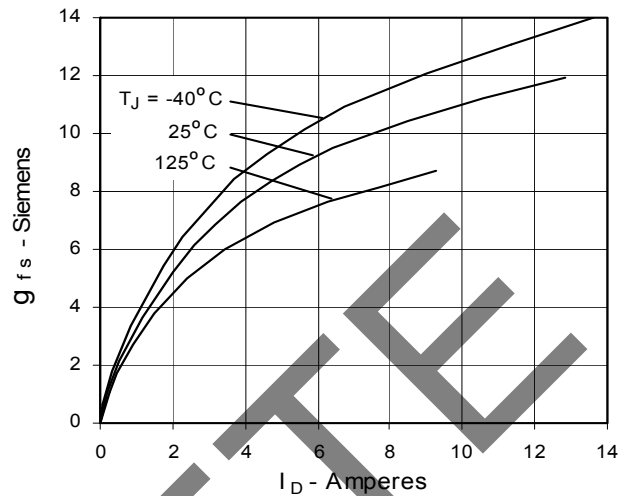


Fig. 9. Source Current vs. Source-To-Drain Voltage

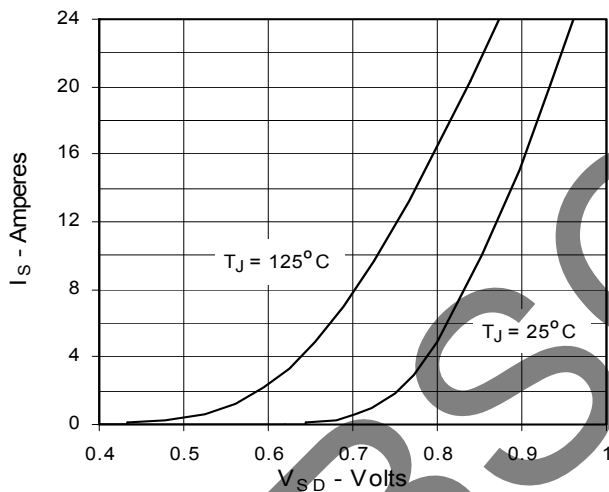


Fig. 10. Gate Charge

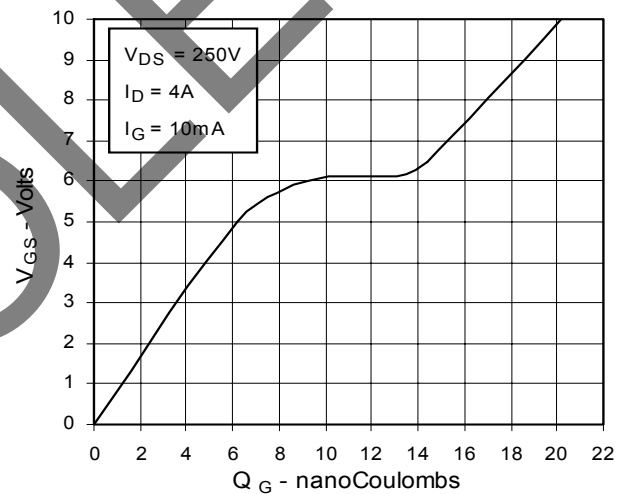


Fig. 11. Capacitance

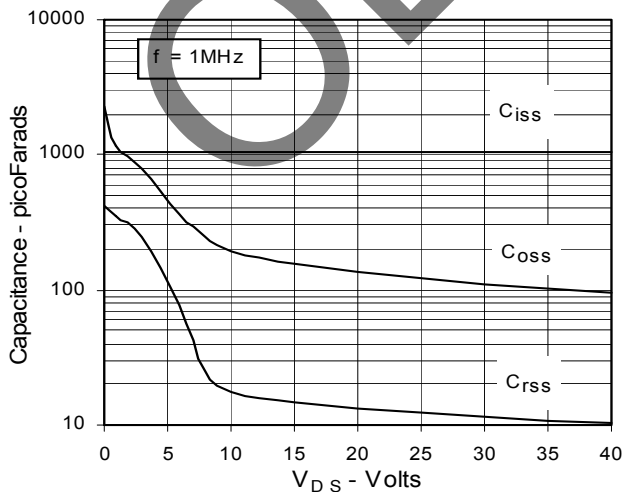


Fig. 12. Forward-Bias Safe Operating Area

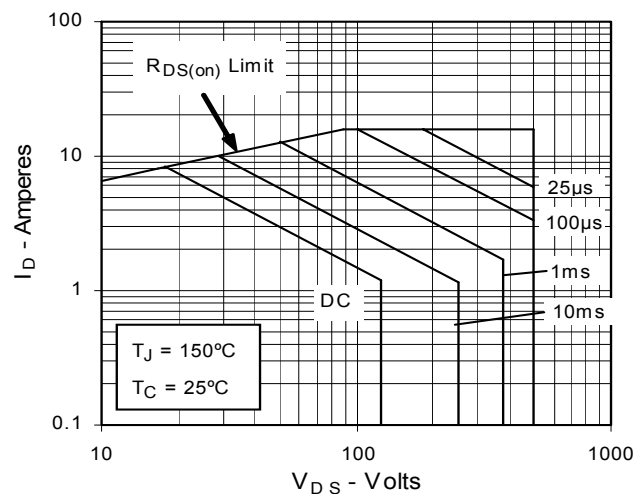
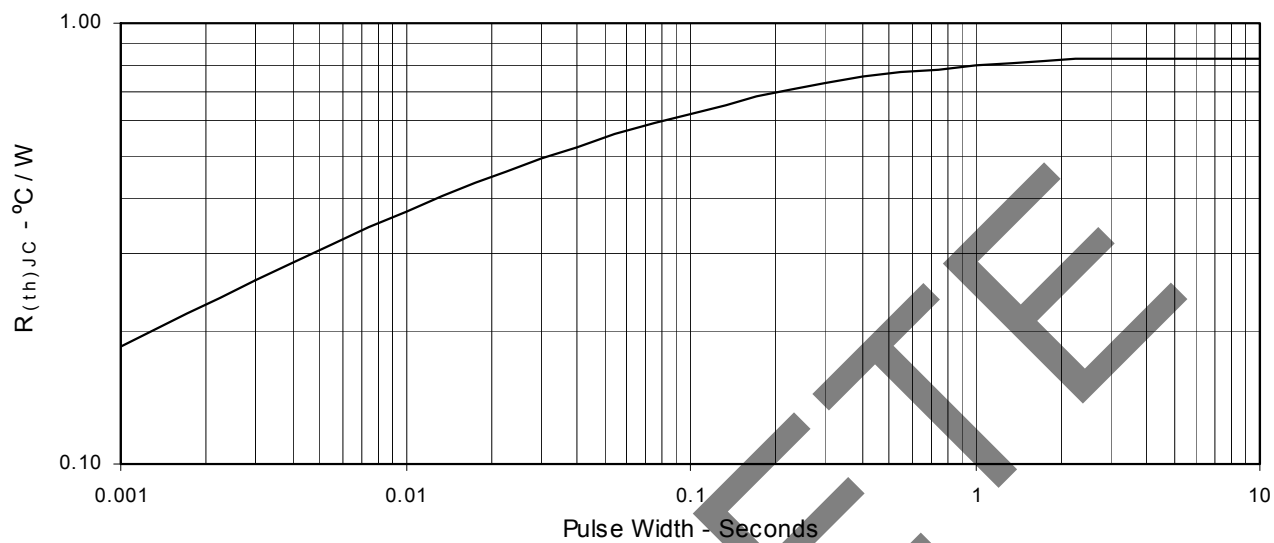


Fig. 13. Maximum Transient Thermal Resistance





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