

## Absolute Maximum Ratings

Absolute Maximum Ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM. The Thermal Resistance and Power Dissipation ratings are measured under board mounted and still air conditions. Additional information is shown in Figures 28 through 35.

| Symbol     | Definition                                                             | Min.           | Max.           | Units              |
|------------|------------------------------------------------------------------------|----------------|----------------|--------------------|
| $V_B$      | High Side Floating Supply Voltage                                      | -0.3           | 625            | V                  |
| $V_S$      | High Side Floating Supply Offset Voltage                               | $V_B - 25$     | $V_B + 0.3$    |                    |
| $V_{HO}$   | High Side Floating Output Voltage                                      | $V_S - 0.3$    | $V_B + 0.3$    |                    |
| $V_{CC}$   | Low Side Fixed Supply Voltage                                          | -0.3           | 25             |                    |
| $V_{LO}$   | Low Side Output Voltage                                                | -0.3           | $V_{CC} + 0.3$ |                    |
| $V_{DD}$   | Logic Supply Voltage                                                   | -0.3           | $V_{SS} + 25$  |                    |
| $V_{SS}$   | Logic Supply Offset Voltage                                            | $V_{CC} - 25$  | $V_{CC} + 0.3$ |                    |
| $V_{IN}$   | Logic Input Voltage (HIN, LIN & SD)                                    | $V_{SS} - 0.3$ | $V_{DD} + 0.3$ |                    |
| $dV_S/dt$  | Allowable Offset Supply Voltage Transient (Figure 2)                   | —              | 50             | V/ns               |
| $P_D$      | Package Power Dissipation @ $T_A \leq +25^\circ\text{C}$ (14 Lead DIP) | —              | 1.6            | W                  |
|            | (16 Lead SOIC)                                                         | —              | 1.25           |                    |
| $R_{THJA}$ | Thermal Resistance, Junction to Ambient (14 Lead DIP)                  | —              | 75             | $^\circ\text{C/W}$ |
|            | (16 Lead SOIC)                                                         | —              | 100            |                    |
| $T_J$      | Junction Temperature                                                   | —              | 150            | $^\circ\text{C}$   |
| $T_S$      | Storage Temperature                                                    | -55            | 150            |                    |
| $T_L$      | Lead Temperature (Soldering, 10 seconds)                               | —              | 300            |                    |

## Recommended Operating Conditions

The Input/Output logic timing diagram is shown in Figure 1. For proper operation the device should be used within the recommended conditions. The  $V_S$  and  $V_{SS}$  offset ratings are tested with all supplies biased at 15V differential. Typical ratings at other bias conditions are shown in Figures 36 and 37.

| Symbol   | Definition                                 | Min.         | Max.          | Units            |
|----------|--------------------------------------------|--------------|---------------|------------------|
| $V_B$    | High Side Floating Supply Absolute Voltage | $V_S + 10$   | $V_S + 20$    | V                |
| $V_S$    | High Side Floating Supply Offset Voltage   | Note 1       | 600           |                  |
| $V_{HO}$ | High Side Floating Output Voltage          | $V_S$        | $V_B$         |                  |
| $V_{CC}$ | Low Side Fixed Supply Voltage              | 10           | 20            |                  |
| $V_{LO}$ | Low Side Output Voltage                    | 0            | $V_{CC}$      |                  |
| $V_{DD}$ | Logic Supply Voltage                       | $V_{SS} + 3$ | $V_{SS} + 20$ |                  |
| $V_{SS}$ | Logic Supply Offset Voltage                | -5 (Note 2)  | 5             |                  |
| $V_{IN}$ | Logic Input Voltage (HIN, LIN & SD)        | $V_{SS}$     | $V_{DD}$      |                  |
| $T_A$    | Ambient Temperature                        | -40          | 125           | $^\circ\text{C}$ |

**Note 1:** Logic operational for  $V_S$  of -5V to +600V. Logic state held for  $V_S$  of -5V to  $-V_{BS}$ . (Please refer to the Design Tip DT97-3 for more details).

**Note 2:** When  $V_{DD} < 5V$ , the minimum  $V_{SS}$  offset is limited to  $-V_{DD}$ .

## Dynamic Electrical Characteristics

V<sub>BIAS</sub> (V<sub>CC</sub>, V<sub>BS</sub>, V<sub>DD</sub>) = 15V, C<sub>L</sub> = 1000 pF, T<sub>A</sub> = 25°C and V<sub>SS</sub> = COM unless otherwise specified. The dynamic electrical characteristics are measured using the test circuit shown in Figure 3.

| Symbol           | Definition                          | Figure | Min. | Typ. | Max. | Units | Test Conditions       |
|------------------|-------------------------------------|--------|------|------|------|-------|-----------------------|
| t <sub>on</sub>  | Turn-On Propagation Delay           | 7      | —    | 125  | 180  | ns    | V <sub>S</sub> = 0V   |
| t <sub>off</sub> | Turn-Off Propagation Delay          | 8      | —    | 105  | 160  |       | V <sub>S</sub> = 600V |
| t <sub>sd</sub>  | Shutdown Propagation Delay          | 9      | —    | 105  | 160  |       | V <sub>S</sub> = 600V |
| t <sub>r</sub>   | Turn-On Rise Time                   | 10     | —    | 80   | 130  |       |                       |
| t <sub>f</sub>   | Turn-Off Fall Time                  | 11     | —    | 40   | 65   |       |                       |
| MT               | Delay Matching, HS & LS Turn-On/Off | —      | —    | —    | 30   |       |                       |

## Static Electrical Characteristics

V<sub>BIAS</sub> (V<sub>CC</sub>, V<sub>BS</sub>, V<sub>DD</sub>) = 15V, T<sub>A</sub> = 25°C and V<sub>SS</sub> = COM unless otherwise specified. The V<sub>IN</sub>, V<sub>TH</sub> and I<sub>IN</sub> parameters are referenced to V<sub>SS</sub> and are applicable to all three logic input leads: HIN, LIN and SD. The V<sub>O</sub> and I<sub>O</sub> parameters are referenced to COM and are applicable to the respective output leads: HO or LO.

| Symbol             | Definition                                                    | Figure | Min. | Typ. | Max. | Units | Test Conditions                                                      |
|--------------------|---------------------------------------------------------------|--------|------|------|------|-------|----------------------------------------------------------------------|
| V <sub>IH</sub>    | Logic "1" Input Voltage                                       | 12     | 9.5  | —    | —    | V     |                                                                      |
| V <sub>IL</sub>    | Logic "0" Input Voltage                                       | 13     | —    | —    | 6.0  |       |                                                                      |
| V <sub>OH</sub>    | High Level Output Voltage, V <sub>BIAS</sub> - V <sub>O</sub> | 14     | —    | —    | 100  | mV    | I <sub>O</sub> = 0A                                                  |
| V <sub>OL</sub>    | Low Level Output Voltage, V <sub>O</sub>                      | 15     | —    | —    | 100  |       | I <sub>O</sub> = 0A                                                  |
| I <sub>LK</sub>    | Offset Supply Leakage Current                                 | 16     | —    | —    | 50   | μA    | V <sub>B</sub> = V <sub>S</sub> = 600V                               |
| I <sub>QBS</sub>   | Quiescent V <sub>BS</sub> Supply Current                      | 17     | —    | 25   | 60   |       | V <sub>IN</sub> = 0V or V <sub>DD</sub>                              |
| I <sub>QCC</sub>   | Quiescent V <sub>CC</sub> Supply Current                      | 18     | —    | 80   | 180  |       | V <sub>IN</sub> = 0V or V <sub>DD</sub>                              |
| I <sub>QDD</sub>   | Quiescent V <sub>DD</sub> Supply Current                      | 19     | —    | 2.0  | 5.0  |       | V <sub>IN</sub> = 0V or V <sub>DD</sub>                              |
| I <sub>IN+</sub>   | Logic "1" Input Bias Current                                  | 20     | —    | 20   | 40   |       | V <sub>IN</sub> = V <sub>DD</sub>                                    |
| I <sub>IN-</sub>   | Logic "0" Input Bias Current                                  | 21     | —    | —    | 1.0  |       | V <sub>IN</sub> = 0V                                                 |
| V <sub>BSUV+</sub> | V <sub>BS</sub> Supply Undervoltage Positive Going Threshold  | 22     | 7.4  | 8.5  | 9.6  | V     |                                                                      |
| V <sub>BSUV-</sub> | V <sub>BS</sub> Supply Undervoltage Negative Going Threshold  | 23     | 7.0  | 8.1  | 9.2  |       |                                                                      |
| V <sub>CCUV+</sub> | V <sub>CC</sub> Supply Undervoltage Positive Going Threshold  | 24     | 7.6  | 8.6  | 9.6  |       |                                                                      |
| V <sub>CCUV-</sub> | V <sub>CC</sub> Supply Undervoltage Negative Going Threshold  | 25     | 7.2  | 8.2  | 9.2  |       |                                                                      |
| I <sub>O+</sub>    | Output High Short Circuit Pulsed Current                      | 26     | 200  | 250  | —    | mA    | V <sub>O</sub> = 0V, V <sub>IN</sub> = V <sub>DD</sub><br>PW ≤ 10 μs |
| I <sub>O-</sub>    | Output Low Short Circuit Pulsed Current                       | 27     | 420  | 500  | —    |       | V <sub>O</sub> = 15V, V <sub>IN</sub> = 0V<br>PW ≤ 10 μs             |

International  
**IOR** Rectifier

| Symbol          | Description                                                 |
|-----------------|-------------------------------------------------------------|
| V <sub>DD</sub> | Logic supply                                                |
| HIN             | Logic input for high side gate driver output (HO), in phase |
| SD              | Logic input for shutdown                                    |
| LIN             | Logic input for low side gate driver output (LO), in phase  |
| V <sub>SS</sub> | Logic ground                                                |
| V <sub>B</sub>  | High side floating supply                                   |
| HO              | High side gate drive output                                 |
| V <sub>S</sub>  | High side floating supply return                            |
| V <sub>CC</sub> | Low side supply                                             |
| LO              | Low side gate drive output                                  |
| COM             | Low side return                                             |

# IR2112(-1-2)(S)PbF

## Lead Assignments

|                                                                                                                                                                                                                                                                                                                                                                                                                                             |     |     |    |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|-----|----|---|---|-----|----|---|----|-----|----|---|----|----|--|---|----|-----|-----|---|----|-----|-----|---|----|--|----|---|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|--|----|---|----|-----|----|---|----|-----|----|---|----|-----|--|--|----|-----|-----|---|----|-----|-----|---|----|-----|-----|---|----|--|----|---|
| <table><tr><td>8</td><td></td><td>HO</td><td>7</td></tr><tr><td>9</td><td>VDD</td><td>VB</td><td>6</td></tr><tr><td>10</td><td>HIN</td><td>VS</td><td>5</td></tr><tr><td>11</td><td>SD</td><td></td><td>4</td></tr><tr><td>12</td><td>LIN</td><td>VCC</td><td>3</td></tr><tr><td>13</td><td>VSS</td><td>COM</td><td>2</td></tr><tr><td>14</td><td></td><td>LO</td><td>1</td></tr></table> <p>14 Lead PDIP<br/><b>IR2112</b></p>             | 8   |     | HO | 7 | 9 | VDD | VB | 6 | 10 | HIN | VS | 5 | 11 | SD |  | 4 | 12 | LIN | VCC | 3 | 13 | VSS | COM | 2 | 14 |  | LO | 1 | <table><tr><td>9</td><td></td><td>HO</td><td>8</td></tr><tr><td>10</td><td></td><td>VB</td><td>7</td></tr><tr><td>11</td><td>VDD</td><td>VS</td><td>6</td></tr><tr><td>12</td><td>HIN</td><td></td><td></td></tr><tr><td>13</td><td>SD</td><td></td><td></td></tr><tr><td>14</td><td>LIN</td><td>VCC</td><td>3</td></tr><tr><td>15</td><td>VSS</td><td>COM</td><td>2</td></tr><tr><td>16</td><td></td><td>LO</td><td>1</td></tr></table> <p>16 Lead SOIC (Wide Body)<br/><b>IR2112S</b></p> | 9 |  | HO | 8 | 10 |     | VB | 7 | 11 | VDD | VS | 6 | 12 | HIN |  |  | 13 | SD  |     |   | 14 | LIN | VCC | 3 | 15 | VSS | COM | 2 | 16 |  | LO | 1 |
| 8                                                                                                                                                                                                                                                                                                                                                                                                                                           |     | HO  | 7  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 9                                                                                                                                                                                                                                                                                                                                                                                                                                           | VDD | VB  | 6  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 10                                                                                                                                                                                                                                                                                                                                                                                                                                          | HIN | VS  | 5  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 11                                                                                                                                                                                                                                                                                                                                                                                                                                          | SD  |     | 4  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 12                                                                                                                                                                                                                                                                                                                                                                                                                                          | LIN | VCC | 3  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 13                                                                                                                                                                                                                                                                                                                                                                                                                                          | VSS | COM | 2  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 14                                                                                                                                                                                                                                                                                                                                                                                                                                          |     | LO  | 1  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 9                                                                                                                                                                                                                                                                                                                                                                                                                                           |     | HO  | 8  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 10                                                                                                                                                                                                                                                                                                                                                                                                                                          |     | VB  | 7  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 11                                                                                                                                                                                                                                                                                                                                                                                                                                          | VDD | VS  | 6  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 12                                                                                                                                                                                                                                                                                                                                                                                                                                          | HIN |     |    |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 13                                                                                                                                                                                                                                                                                                                                                                                                                                          | SD  |     |    |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 14                                                                                                                                                                                                                                                                                                                                                                                                                                          | LIN | VCC | 3  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 15                                                                                                                                                                                                                                                                                                                                                                                                                                          | VSS | COM | 2  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 16                                                                                                                                                                                                                                                                                                                                                                                                                                          |     | LO  | 1  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
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| 8                                                                                                                                                                                                                                                                                                                                                                                                                                           |     | HO  | 7  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 9                                                                                                                                                                                                                                                                                                                                                                                                                                           | VDD | VB  | 6  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 10                                                                                                                                                                                                                                                                                                                                                                                                                                          | HIN | VS  | 5  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 11                                                                                                                                                                                                                                                                                                                                                                                                                                          | SD  |     |    |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 12                                                                                                                                                                                                                                                                                                                                                                                                                                          | LIN | VCC | 3  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 13                                                                                                                                                                                                                                                                                                                                                                                                                                          | VSS | COM | 2  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 14                                                                                                                                                                                                                                                                                                                                                                                                                                          |     | LO  | 1  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 8                                                                                                                                                                                                                                                                                                                                                                                                                                           |     | HO  | 7  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 9                                                                                                                                                                                                                                                                                                                                                                                                                                           | VDD | VB  | 6  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 10                                                                                                                                                                                                                                                                                                                                                                                                                                          | HIN | VS  |    |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 11                                                                                                                                                                                                                                                                                                                                                                                                                                          | SD  |     |    |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 12                                                                                                                                                                                                                                                                                                                                                                                                                                          | LIN | VCC | 3  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 13                                                                                                                                                                                                                                                                                                                                                                                                                                          | VSS | COM | 2  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| 14                                                                                                                                                                                                                                                                                                                                                                                                                                          |     | LO  | 1  |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |
| Part Number                                                                                                                                                                                                                                                                                                                                                                                                                                 |     |     |    |   |   |     |    |   |    |     |    |   |    |    |  |   |    |     |     |   |    |     |     |   |    |  |    |   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |   |  |    |   |    |     |    |   |    |     |    |   |    |     |  |  |    |     |     |   |    |     |     |   |    |     |     |   |    |  |    |   |

# IR2112(-1-2)(S)PbF

International  
**IR** Rectifier

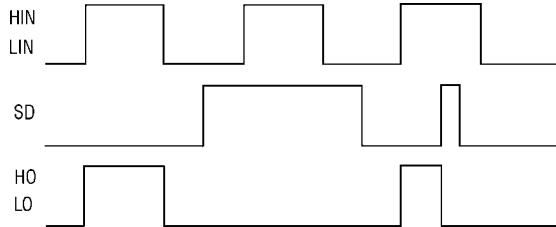


Figure 1. Input/Output Timing Diagram

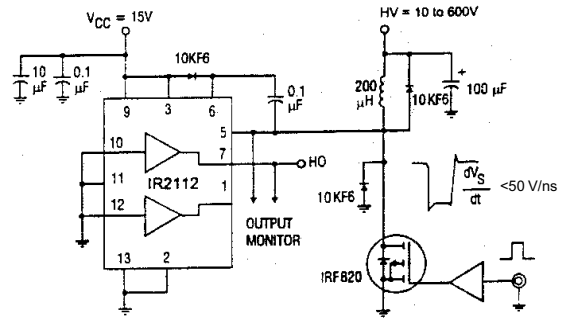


Figure 2. Floating Supply Voltage Transient Test Circuit

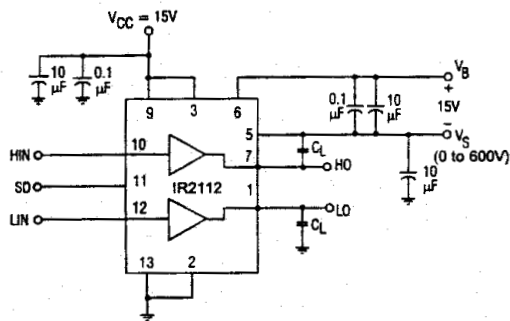


Figure 3. Switching Time Test Circuit

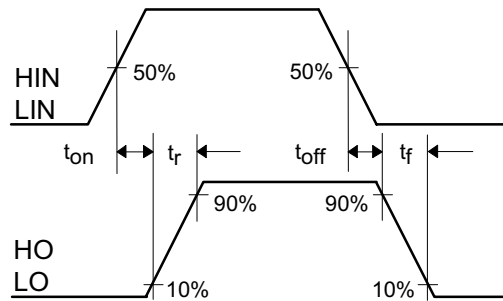


Figure 4. Switching Time Waveform Definition

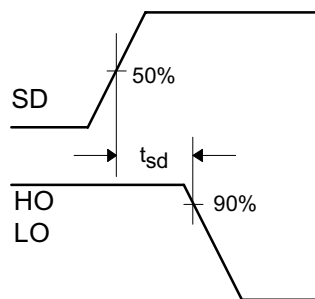


Figure 5. Shutdown Waveform Definitions

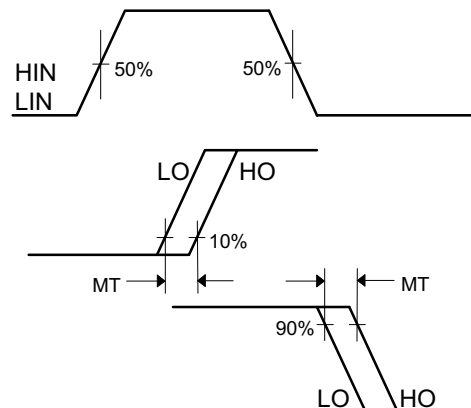


Figure 6. Delay Matching Waveform Definitions

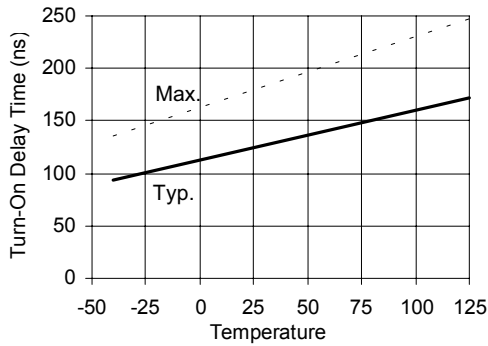


Figure 7A. Turn-On Time vs. Temperature

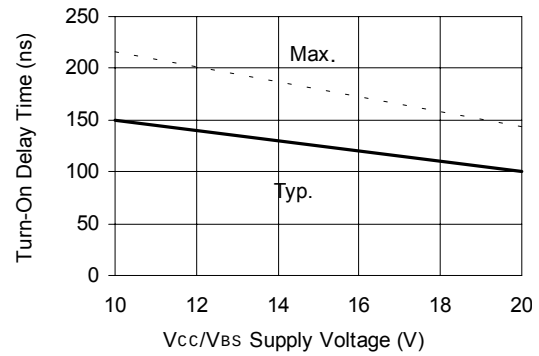


Figure 7B. Turn-On Time vs. Vcc/Vbs Supply Voltage

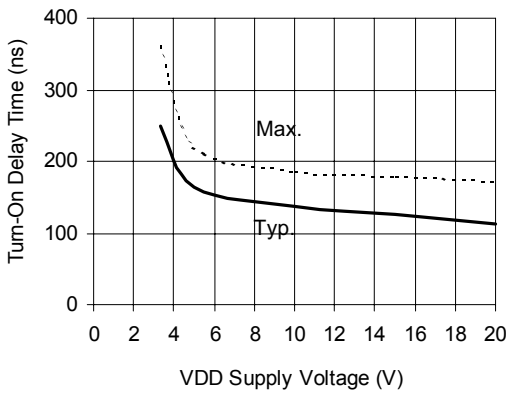


Figure 7C. Turn-On Time vs. Vdd Supply Voltage

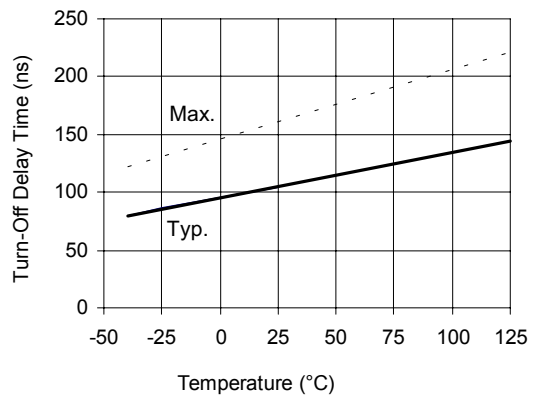


Figure 8A. Turn-Off Time vs. Temperature

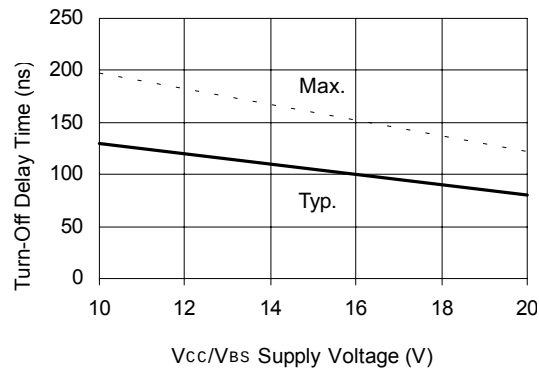


Figure 8B. Turn-Off Time vs. Vcc/Vbs Supply Voltage

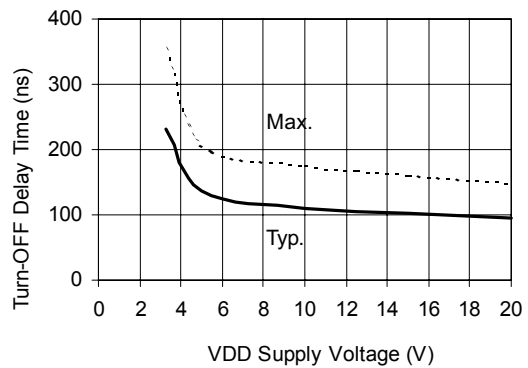
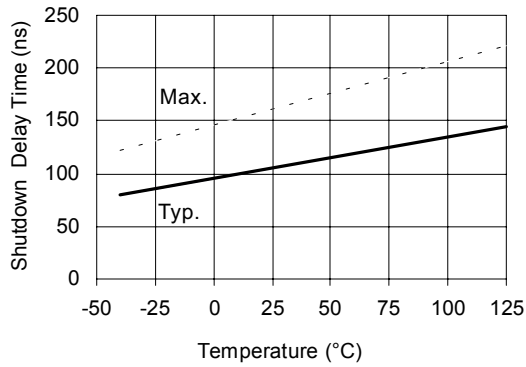


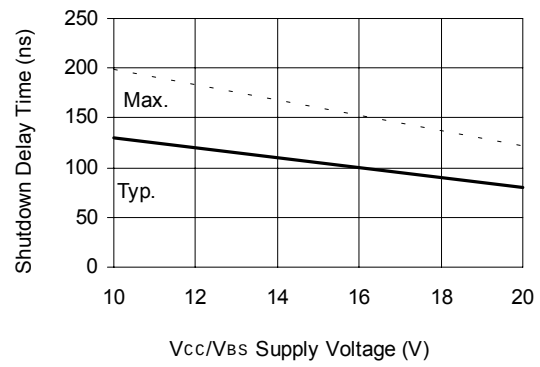
Figure 8C. Turn-Off Time vs. Vdd Supply Voltage

# IR2112(-1-2)(S)PbF

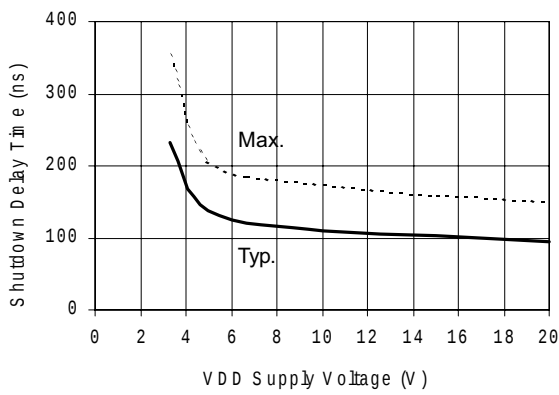
International  
**IR** Rectifier



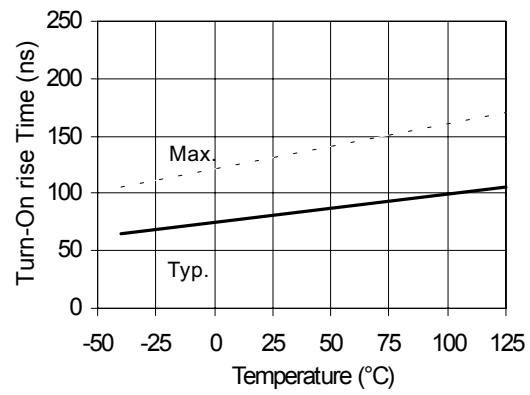
**Figure 9A. Shutdown Time vs. Temperature**



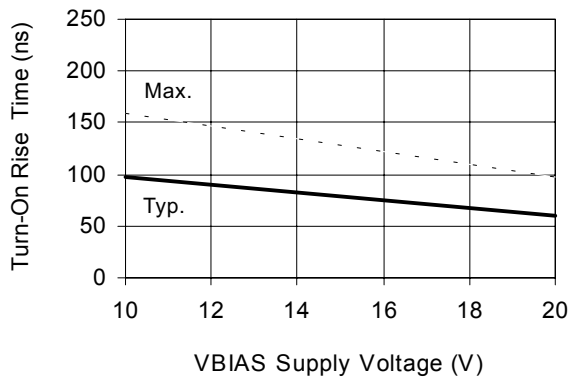
**Figure 9B. Shutdown Delay Time vs. Vcc/Vss Supply Voltage**



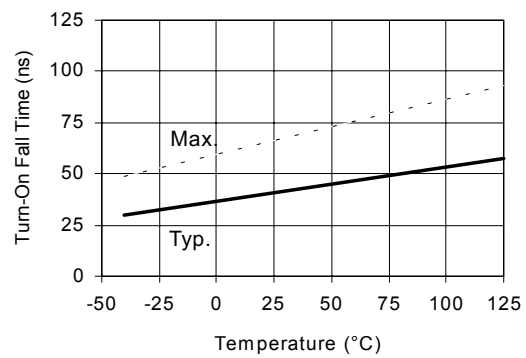
**Figure 9C. Shutdown Time vs. VDD Supply Voltage**



**Figure 10A. Turn-On Rise Time vs. Temperature**



**Figure 10B. Turn-On Rise Time vs. Voltage**



**Figure 11A Turn-On Fall Time vs. Temperature**

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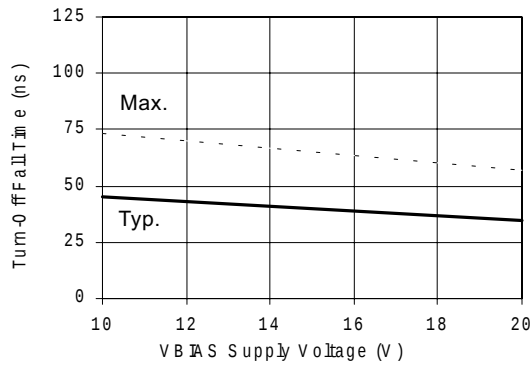


Figure 11B. Turn-Off Fall Time vs. Voltage

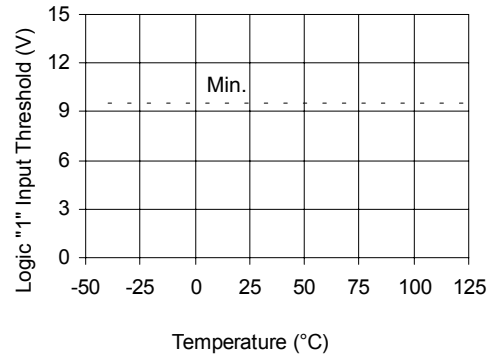


Figure 12A. Logic "1" Input Threshold vs. Temperature

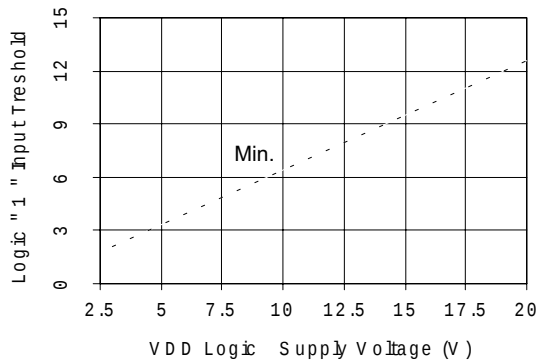


Figure 12B. Logic "1" Input Threshold vs. Voltage

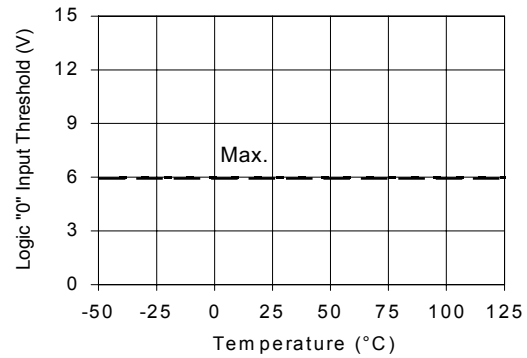


Figure 13A. Logic "0" Input Threshold vs. Temperature

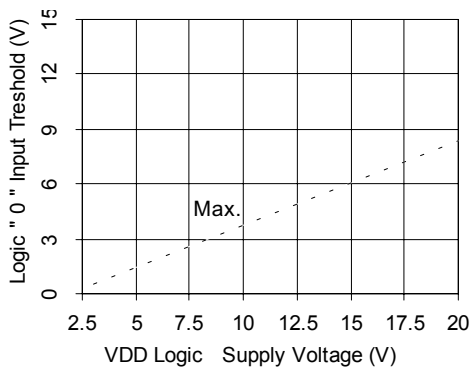


Figure 13B. Logic "0" Input Threshold vs. Voltage

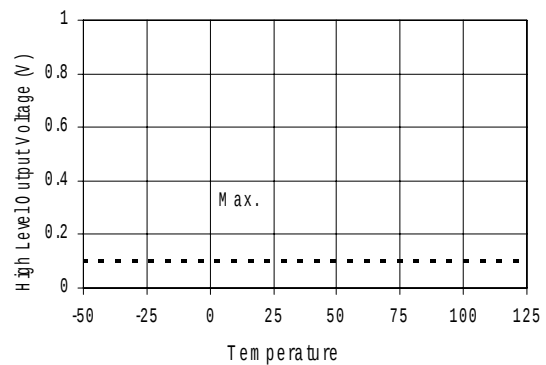


Figure 14A. High Level Output vs. Temperature

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International  
IR Rectifier

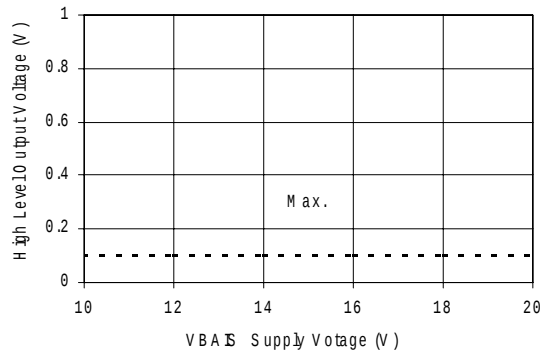


Figure 14B. High Level Output vs. Voltage

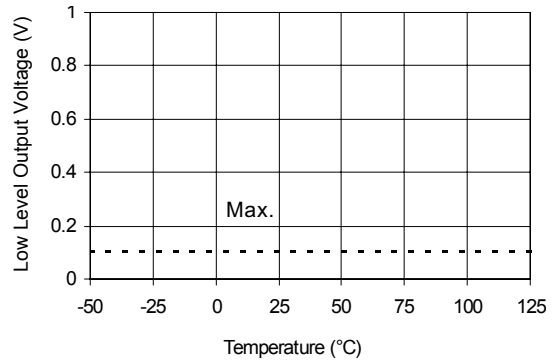


Figure 15A. Low Level Output vs. Temperature

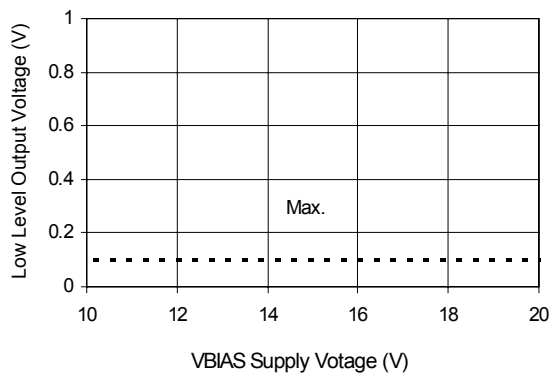


Figure 15B. Low Level Output vs. Voltage

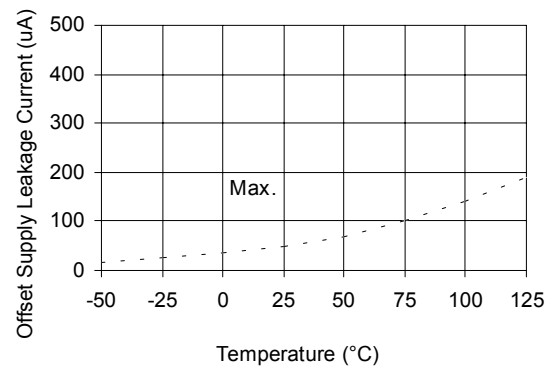


Figure 16A. Offset Supply Current vs. Temperature

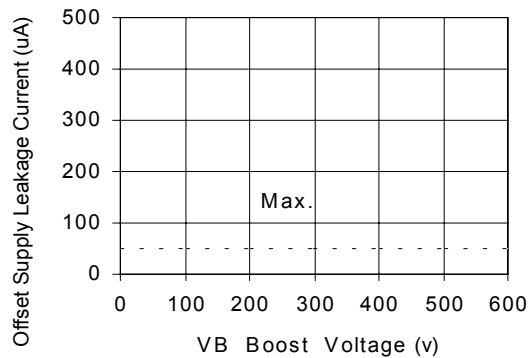


Figure 16B. Offset Supply Current vs. Voltage

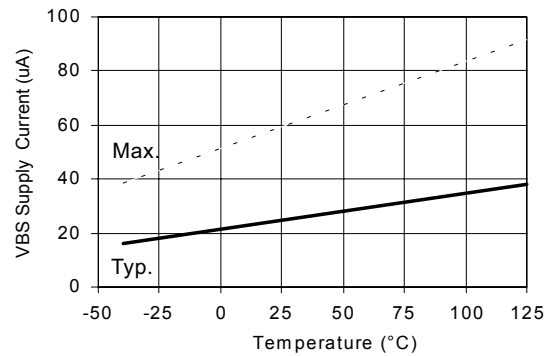


Figure 17A. Vbs Supply Current vs. Temperature

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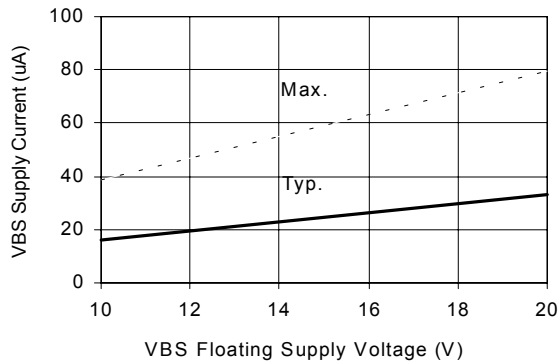


Figure 17B. VBS Supply Current vs. Voltage

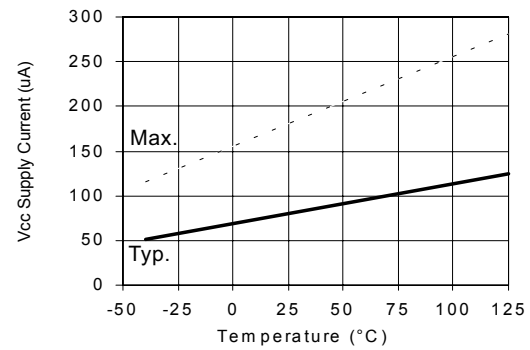


Figure 18A. VCC Supply Current vs. Temperature

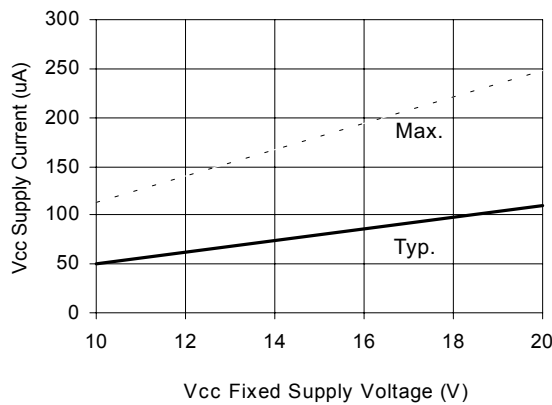


Figure 18B. VCC Supply Current vs. Voltage

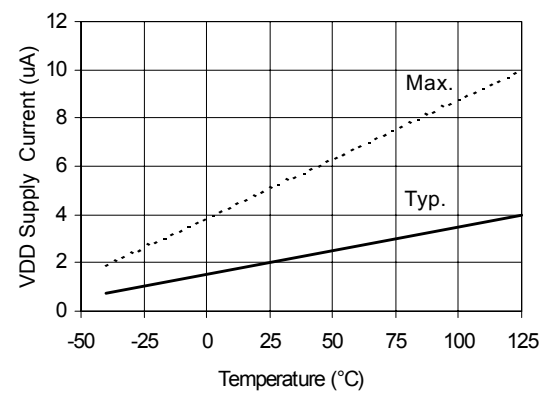


Figure 19A. VDD Supply Current vs. Temperature

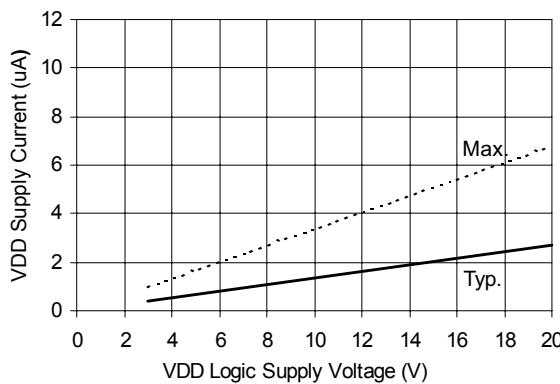


Figure 19B. VDD Supply Current vs. VDD Voltage

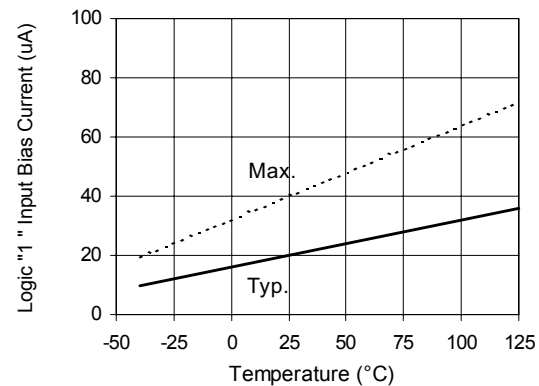


Figure 20A. Logic "1" Input Current vs. Temperature

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International  
**IR** Rectifier

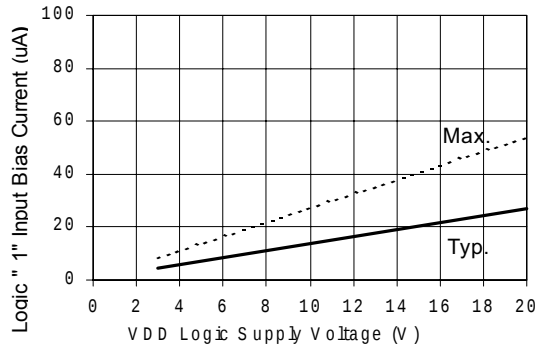


Figure 20B. Logic "1" Input Current vs.  $V_{DD}$  Voltage

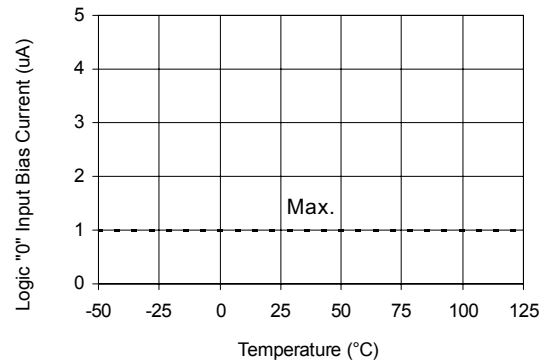


Figure 21A. Logic "0" Input Current vs. Temperature

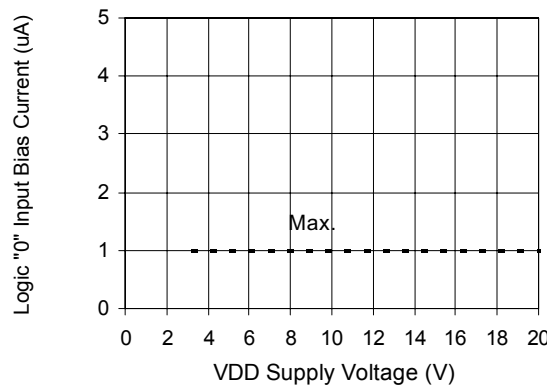


Figure 21B. Logic "0" Input Current vs.  $V_{DD}$  Voltage

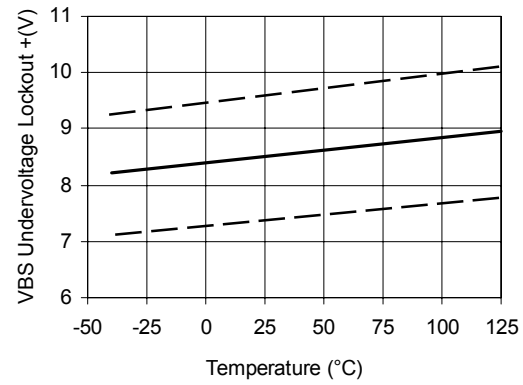


Figure 22.  $V_{bs}$  Undervoltage (+) vs. Temperature

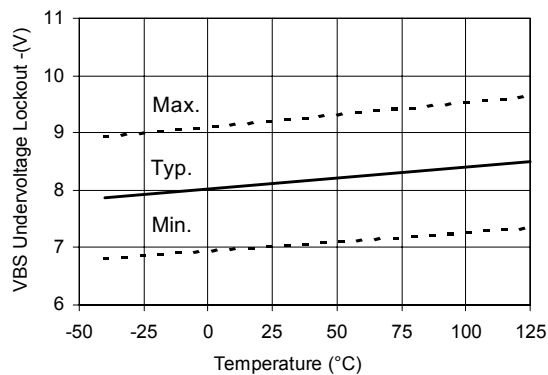


Figure 23.  $V_{bs}$  Undervoltage (-) vs. Temperature

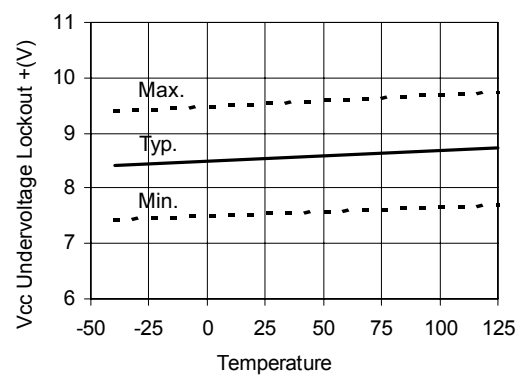


Figure 24.  $V_{cc}$  Undervoltage (-) vs. Temperature

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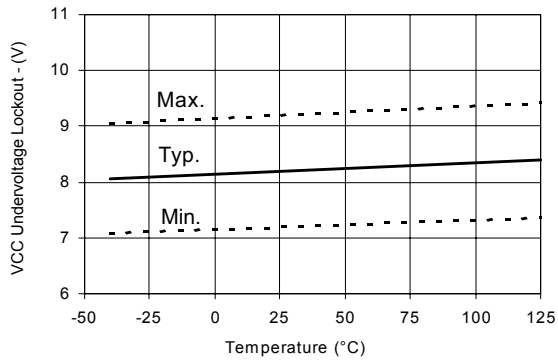


Figure 25. Vcc Undervoltage (-) vs. Temperature

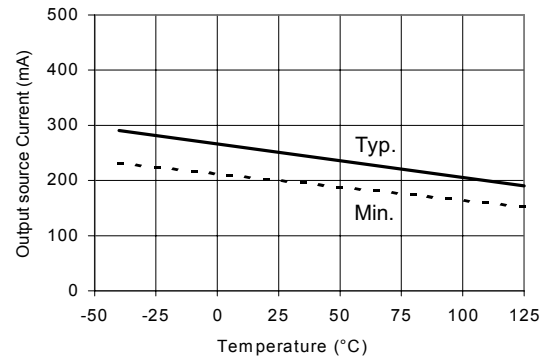


Figure 26A. Output Source Current vs. Temperature

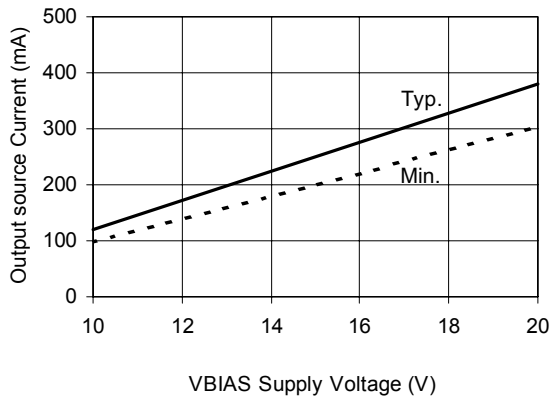


Figure 26B. Output Source Current vs. Voltage

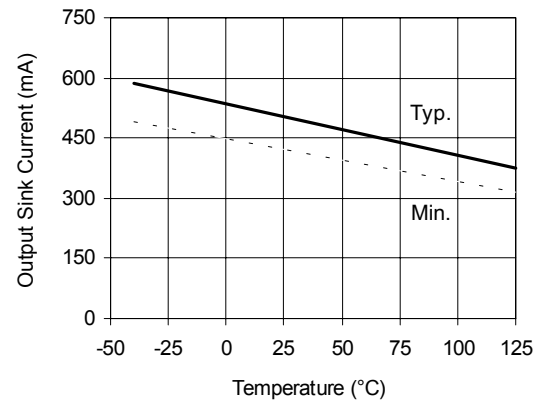


Figure 27A. Output Sink Current vs. Temperature

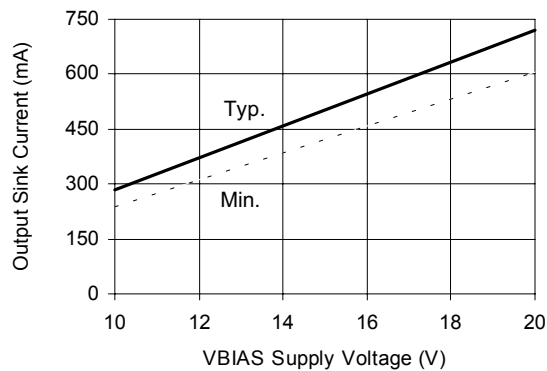
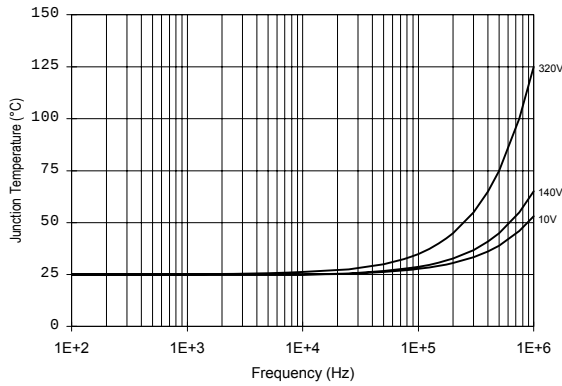


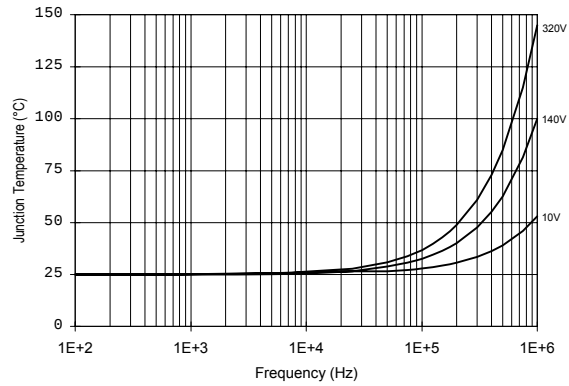
Figure 27B. Output Sink Current vs. Voltage

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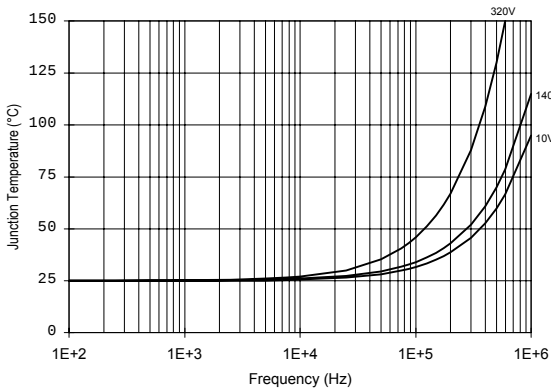
International  
IR Rectifier



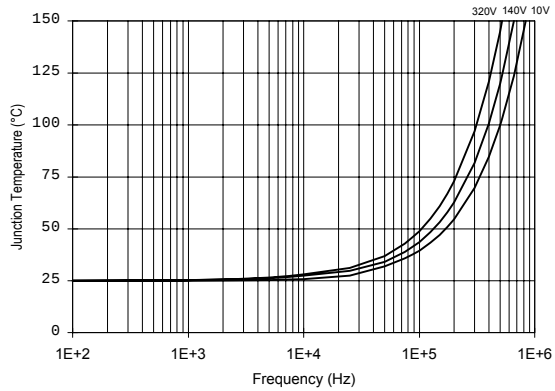
**Figure 28. IR2112  $T_j$  vs. Frequency (IRFBC20)**  
 $R_{\text{GATE}} = 33\Omega$ ,  $V_{\text{CC}} = 15\text{V}$



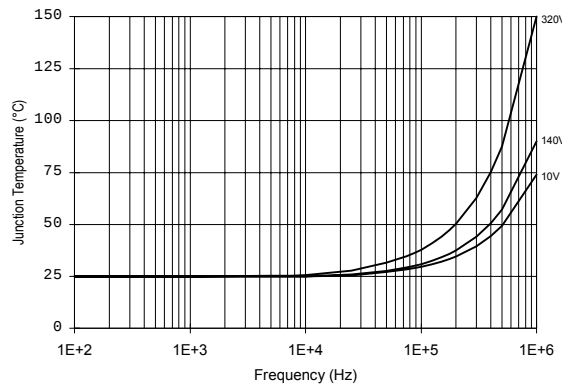
**Figure 29. IR2112  $T_j$  vs. Frequency (IRFBC30)**  
 $R_{\text{GATE}} = 22\Omega$ ,  $V_{\text{CC}} = 15\text{V}$



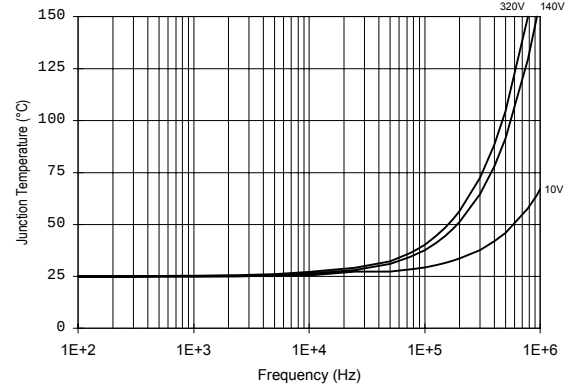
**Figure 30. IR2112  $T_j$  vs. Frequency (IRFBC40)**  
 $R_{\text{GATE}} = 15\Omega$ ,  $V_{\text{CC}} = 15\text{V}$



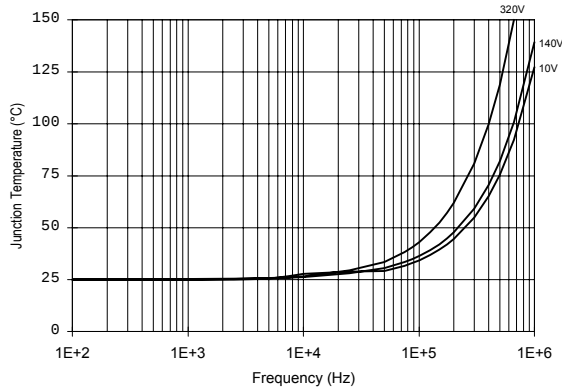
**Figure 31. IR2112  $T_j$  vs. Frequency (IRFPE50)**  
 $R_{\text{GATE}} = 10\Omega$ ,  $V_{\text{CC}} = 15\text{V}$



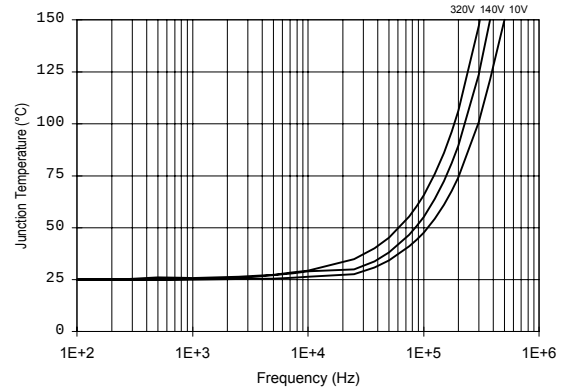
**Figure 32. IR2112S  $T_j$  vs. Frequency (IRFBC20)**  
 $R_{\text{GATE}} = 33\Omega$ ,  $V_{\text{CC}} = 15\text{V}$



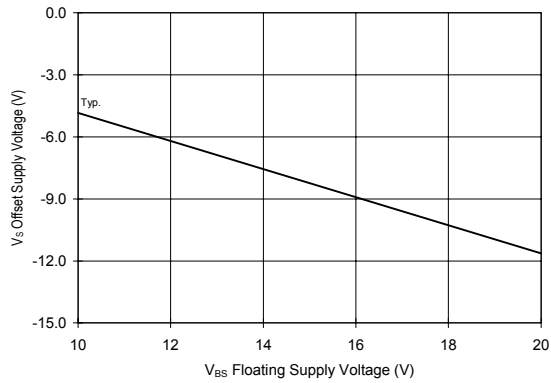
**Figure 33. IR2112S  $T_j$  vs. Frequency (IRFBC30)**  
 $R_{\text{GATE}} = 22\Omega$ ,  $V_{\text{CC}} = 15\text{V}$



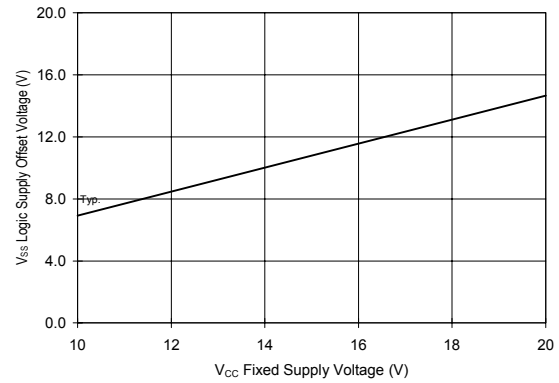
**Figure 34. IR2112S  $T_J$  vs. Frequency (IRFBC40)**  
 $R_{GATE} = 15\Omega$ ,  $V_{CC} = 15V$



**Figure 35. IR2112S  $T_J$  vs. Frequency (IRFPE50)**  
 $R_{GATE} = 10\Omega$ ,  $V_{CC} = 15V$



**Figure 36. Maximum  $V_S$  Negative Offset vs.  $V_{BS}$  Supply Voltage**

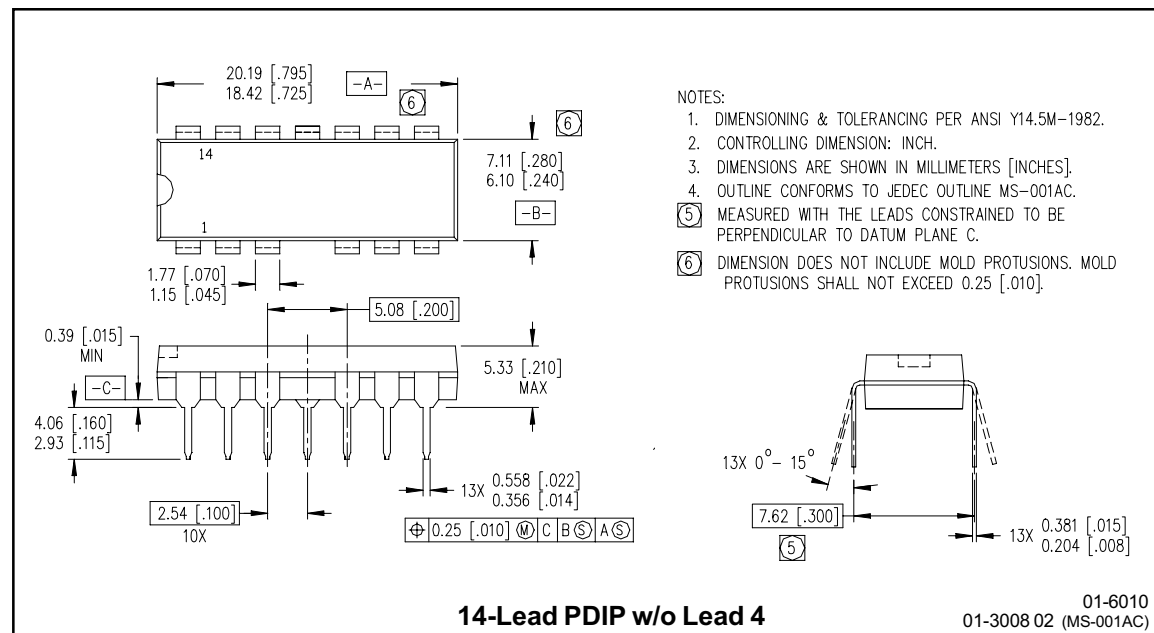
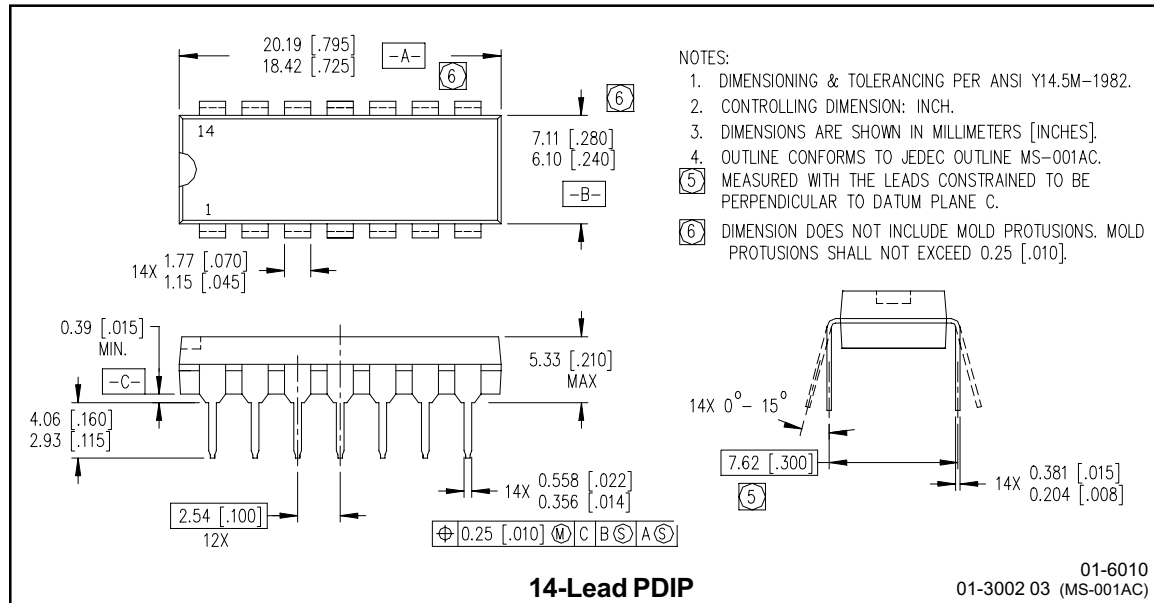


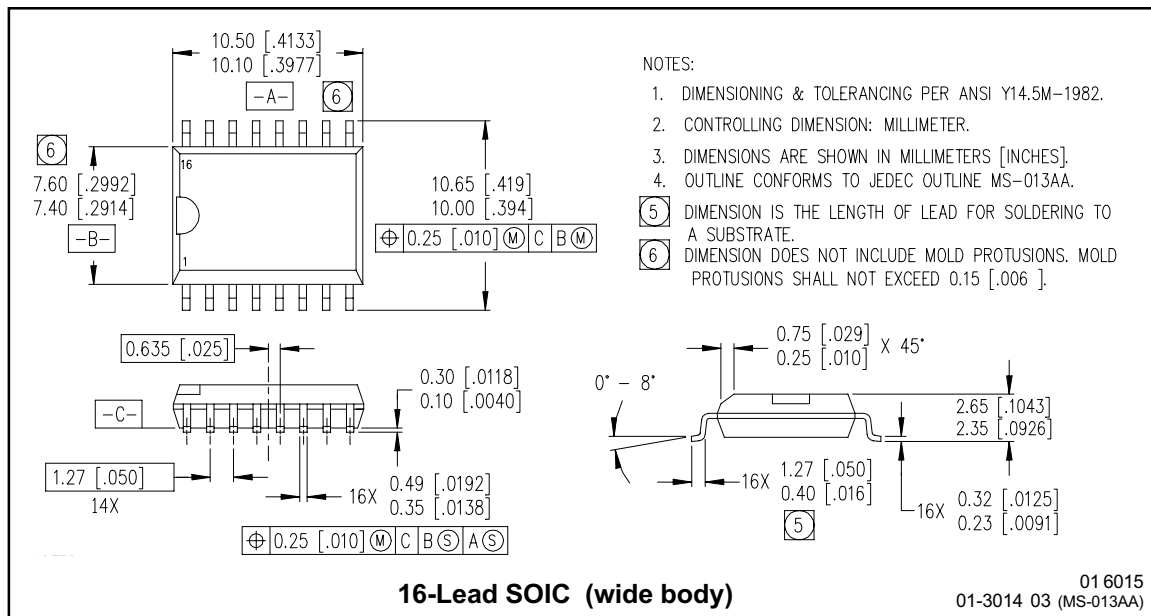
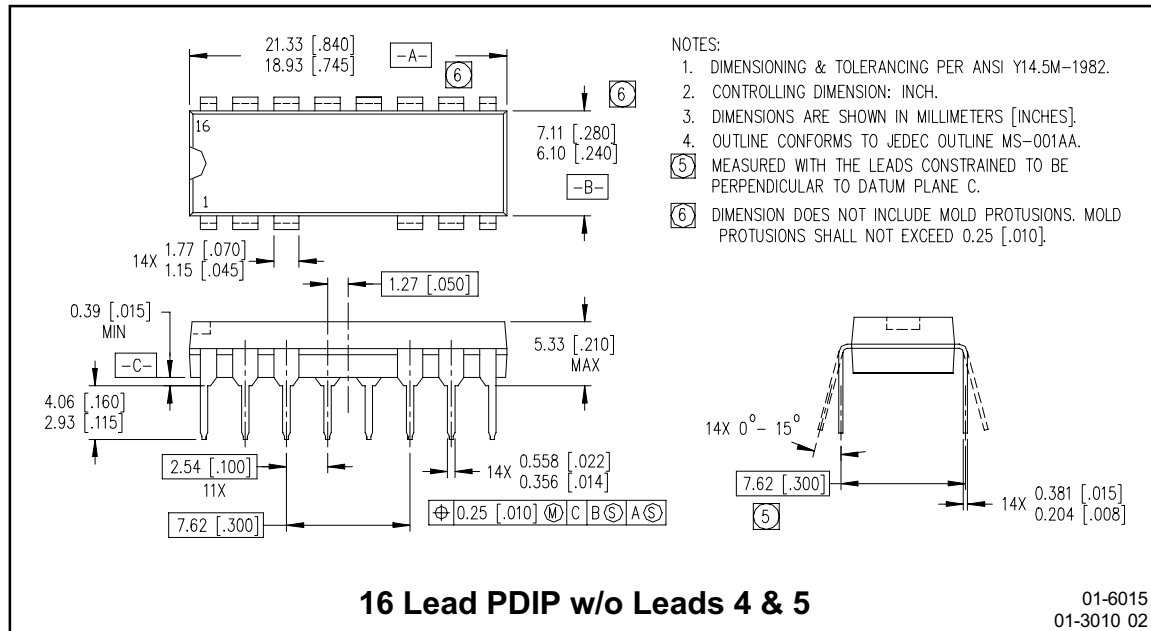
**Figure 37. Maximum  $V_{SS}$  Positive Offset vs.  $V_{CC}$  Supply Voltage**

# IR2112(-1-2)(S)PbF

International  
IR Rectifier

## Case outline

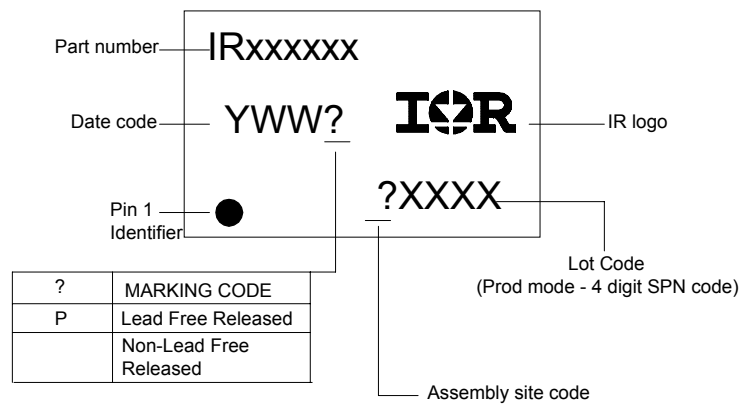




# IR2112(-1-2)(S)PbF

International  
**IR** Rectifier

## LEADFREE PART MARKING INFORMATION



## ORDER INFORMATION

**Part only available Leadfree**

14-Lead PDIP IR2112 order IR2112PbF  
14-Lead PDIP IR2112-1 order IR2112-1PbF  
14-Lead PDIP IR2112-2 order IR2112-2PbF  
16-Lead SOIC IR2112S order IR2112SPbF

International  
**IR** Rectifier

**IR WORLD HEADQUARTERS:** 233 Kansas St., El Segundo, California 90245 Tel: (310) 252-7105

**This product has been qualified per industrial level**

*Data and specifications subject to change without notice. 3/23/2005*