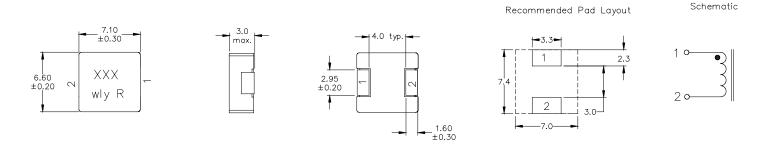
Product Specifications

Part Number ⁶	OCL¹ (µH) ±20%	FLL² (µH) minimum	I 3 (A)	I 4 (A)	DCR (mΩ) typical @ +20 °C	DCR (mΩ) maximum @ +20 °C	K-factor ⁵
HCM0703-R15-R	0.15	0.09	26	52	1.9	2.5	1044
HCM0703-R22-R	0.22	0.13	23	40	2.5	2.8	986
HCM0703-R47-R	0.47	0.28	17.5	26	4.0	4.2	580
HCM0703-R68-R	0.68	0.41	15.5	25	5.0	5.5	455
HCM0703-R82-R	0.82	0.49	13	24	6.7	8.0	439
HCM0703- 1R0-R	1.0	0.60	11	22	9.0	10	374
HCM0703- 1R5-R	1.5	0.90	9.0	18	14	15	366
HCM0703- 2R2-R	2.2	1.3	8.0	14	18	20	281
HCM0703- 3R3-R	3.3	2.0	6.0	13.5	28	30	252
HCM0703- 4R7-R	4.7	2.8	5.5	10	37	40	210
HCM0703- 6R8-R	6.8	4.1	4.5	8.0	54	60	151
HCM0703- 8R2-R	8.2	4.9	4.0	7.5	64	68	142
HCM0703- 100-R	10	6.0	3.2	7.0	71	78	132
HCM0703- 150-R	14.9±15%	10.1	2.2	5.0	113	127	105
HCM0703- 220-R	22	14.1	2.3	3.0	135	149	83
HCM0703- 330-R	33	19.8	1.8	2.2	220	242	76

- 1. Open Circuit Inductance (OCL) Test Parameters: 100 kHz, 0.25 V $_{mat}$, 0.0 Adc, +25°C. 2. Full Load Inductance (FLL) Test Parameters: 100 kHz, 0.25 V $_{mat}$, $_{last}$ ' @ +25°C.
- 3. I_{ms}: DC current for an approximate temperature rise of 40 °C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed +125 °C under worst case operating conditions verified in the end application.
- 4. I_{sat}: Peak current for approximately 20% rolloff at +25 °C.
- 5. K-factor: Used to determine $B_{p,p}$ for core loss (see graph). $Bp-p = K * L * \Delta I$. $B_{p,p}$. (Gauss), K: (K-factor from table), L: (Inductance in μH), ΔI (Peak to peak ripple current in Amps).
- 6. Part Number Definition: HCM0703-xxx-R
- HCM0703 = Product code and size
- -xxx= Inductance value in μH , R = decimal point,
- if no R is present then last character equals number of zeros.
- "-R" suffix = RoHS compliant

Dimensions (mm)



Part marking: XXX=Inductance value in uH, R= decimal point. If no R is present then last character equals number of zeros. wly=date code, R=revision level

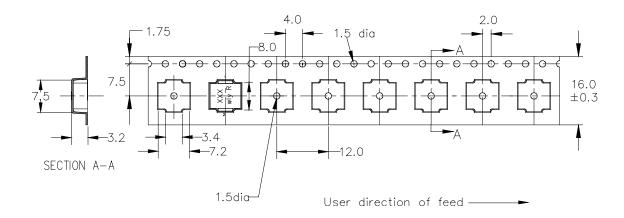
All soldering surfaces to be coplanar within 0.10 millimeters

Tolerances are ±0.3 millimeters unless stated otherwise

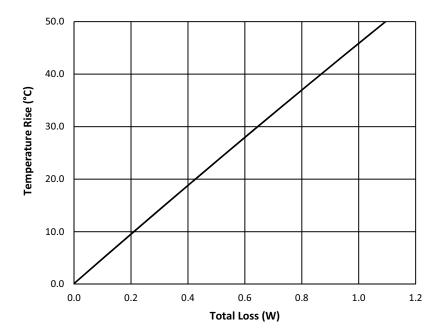
Color: Grey

Packaging information (mm)

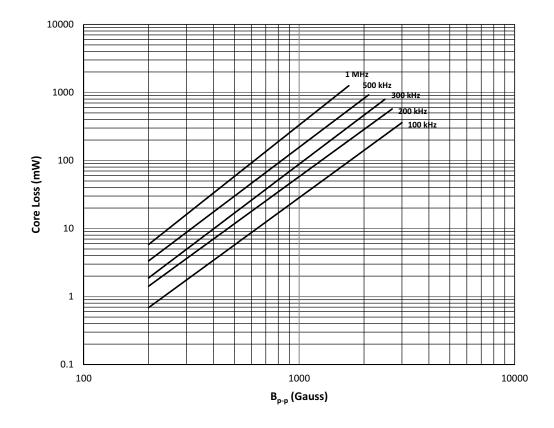
Supplied in tape and reel packaging, 1500 parts per 13" diameter reel.

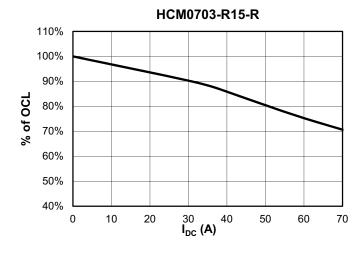


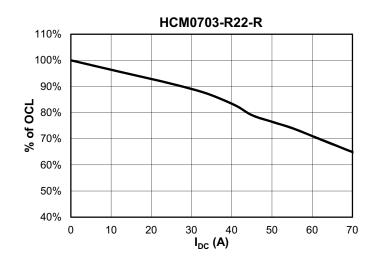
Temperature rise vs. total loss

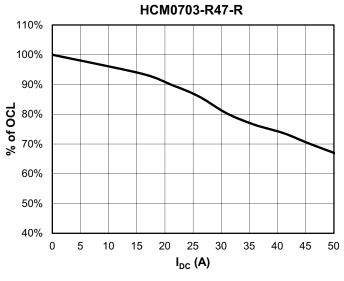


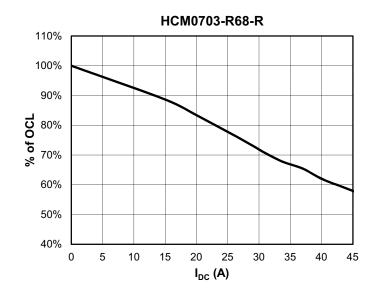
Core loss vs. B_{p-p}

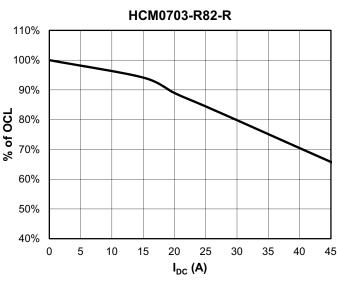


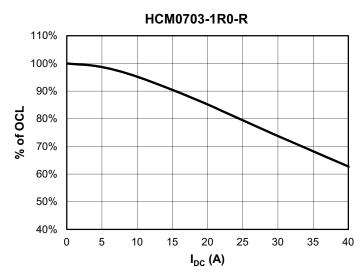


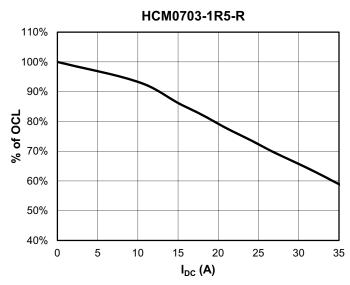


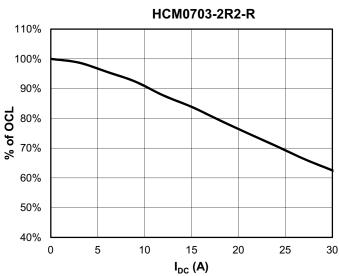


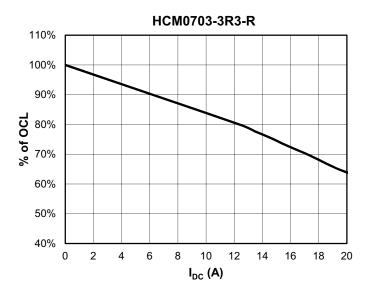


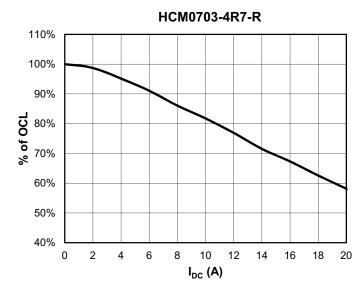


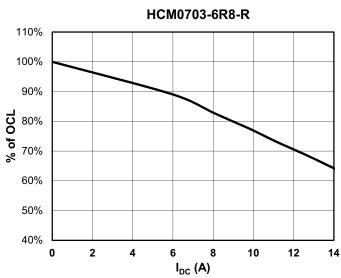


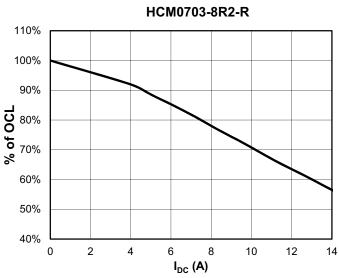


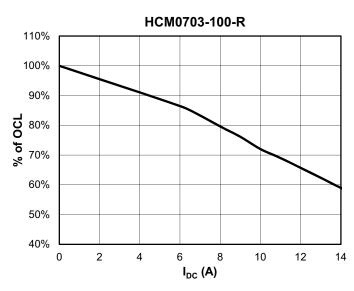


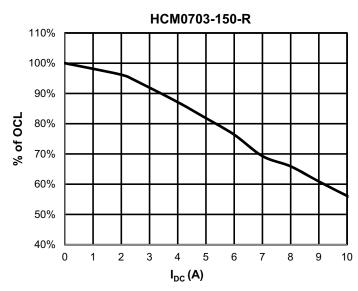


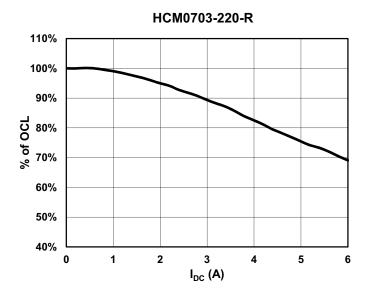


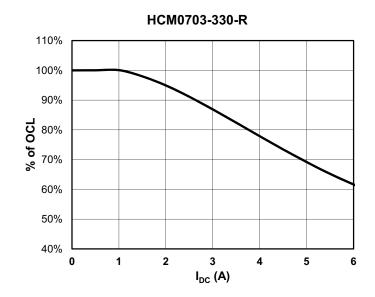




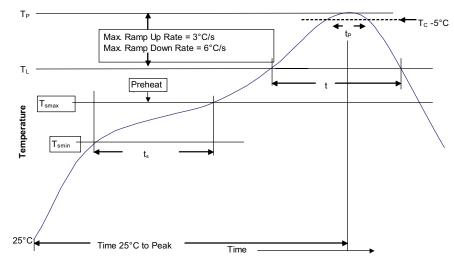








Solder reflow profile



 $-_{T_C-5^{\circ}C}$ Table 1 - Standard SnPb Solder (T_C)

Package Thickness	Volume mm3 <350	Volume mm3 ≥350
<2.5 mm)	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2 - Lead (Pb) Free Solder (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ 350 - 2000	Volume mm³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 – 2.5 mm	260 °C	250 °C	245 °C
>2.5 mm	250 °C	245 °C	245 °C

Reference JDEC J-STD-020

Profile Feature	Standard SnPb Solder	Lead (Pb) Free Solder 150 °C	
Preheat and Soak • Temperature min. (T _{smin})	100 °C		
Temperature max. (T _{smax})	150 °C	200 °C	
• Time (T _{Smin} to T _{Smax}) (t _S)	60-120 seconds	60-120 seconds	
Average ramp up rate T_{Smax} to T_p	3 °C/ second Max.	3 °C/ second Max.	
Liquidous temperature (TL) Time at liquidous (tL)	183°C 60-150 seconds	217°C 60-150 seconds	
Peak package body temperature (Tp)*	Table 1	Table 2	
$\overline{\text{Time } (t_p)^{**} \text{ within 5 °C of the specified classification temperature } (T_c)}$	20 seconds**	30 seconds**	
Average ramp-down rate (T _p to T _{Smax})	6 °C/ second Max.	6 °C/ second Max.	
Time 25 °C to Peak Temperature	6 Minutes Max.	8 Minutes Max.	

 $^{^{\}star}$ Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

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^{**} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.