

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	80	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	44 28	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	80	Α
Maximum Continuous Body Diode Forward Current (Note 5)		I _S	3	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{SM}	80	Α
Avalanche Current, L=0.1mH		I _{AS}	11.6	Α
Avalanche Energy, L=0.1mH		E _{AS}	10.2	mJ

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_{D}	2.7	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	47	°C/W
Total Power Dissipation (Note 6)	P _D	50	W
Thermal Resistance, Junction to Case (Note 6)	Rejc	2.5	°C/W
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

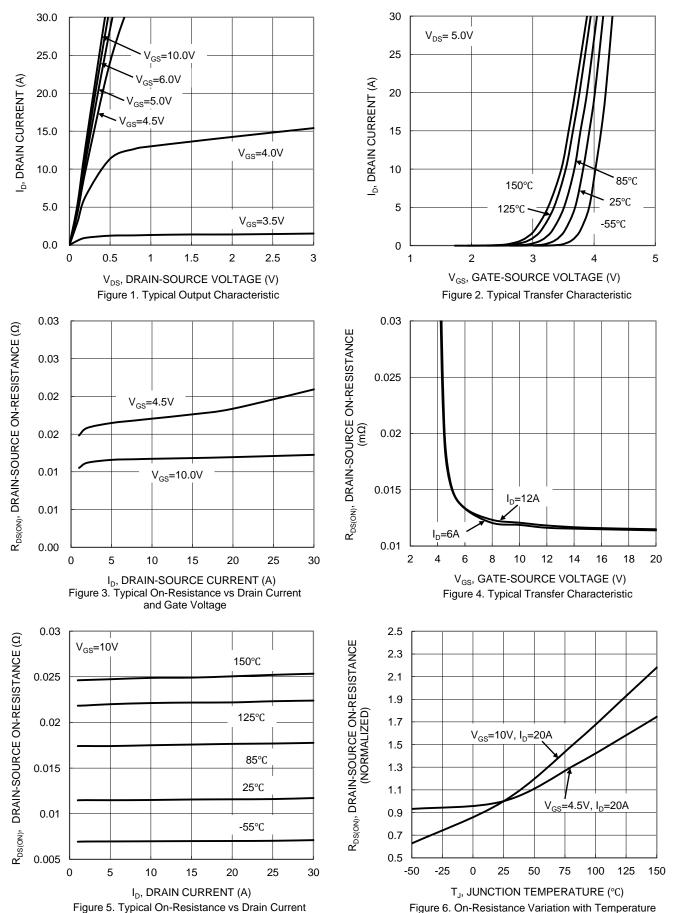
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	80	-	-	V	$V_{GS} = 0V$, $I_D = 1mA$
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μA	V _{DS} = 64V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1	-	3	٧	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance		-	12	17	mΩ	$V_{GS} = 10V, I_D = 12A$
Static Dialii-Source Off-Resistance	R _{DS(ON)}		18.2	22		$V_{GS} = 4.5V, I_D = 6A$
Diode Forward Voltage	V_{SD}	-	0.9	1.2	V	V _{GS} = 0V, I _S = 25A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	-	1,949	-	pF	V _{DS} = 40V, V _{GS} = 0V, f = 1MHz
Output Capacitance	Coss	-	177	-		
Reverse Transfer Capacitance	Crss	-	10	-		
Gate Resistance	Rg	-	0.7	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	15	-		V _{DS} = 40V, I _D = 12A
Total Gate Charge (V _{GS} = 10V)	Qg	-	34	-	nC	
Gate-Source Charge	Q_{gs}	-	6	-	IIC	
Gate-Drain Charge	Q_{gd}	-	4.5	-		
Turn-On Delay Time	t _{D(ON)}	-	4.9	-		$V_{DD} = 40V, V_{GS} = 10V,$ $I_{D} = 12A, R_{G} = 1.6\Omega$
Turn-On Rise Time	t _R	-	3.8	-		
Turn-Off Delay Time	t _{D(OFF)}	-	16.5	-	ns	
Turn-Off Fall Time	t _F	-	3.5	-		
Body Diode Reverse Recovery Time	t _{RR}	-	30.2	-	ns	1 400 11/11 4000/
Body Diode Reverse Recovery Charge	Qrr	-	34.6	-	$I_F = 12A$, di/dt = $100A/\mu s$	

^{5.} Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.6. Device mounted on infinite heat sink and measured by thermal couple attached on bottom heat sink of package.

^{7.} Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.





and Temperature



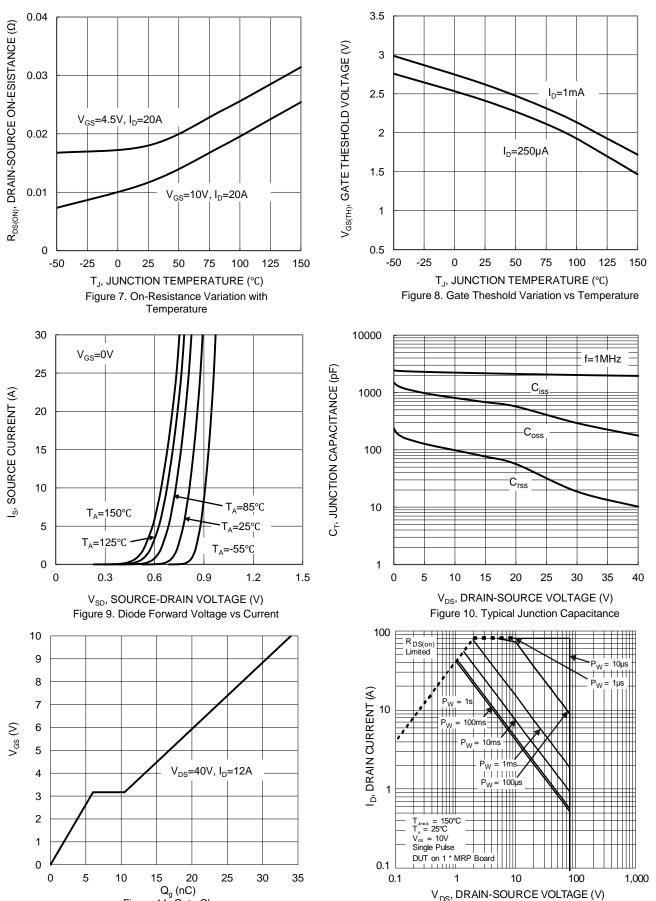


Figure 11. Gate Charge

Figure 12 SOA, Safe Operation Area



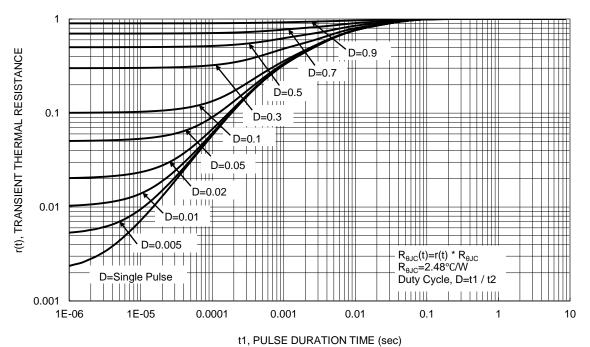
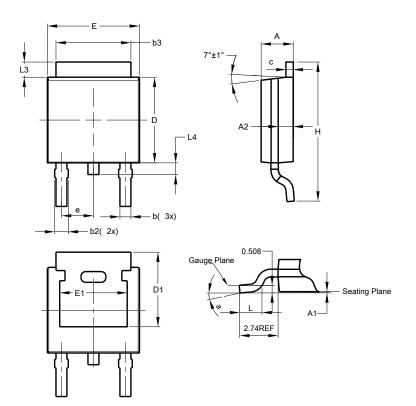


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

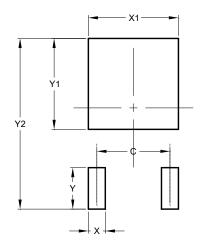
Please see http://www.diodes.com/package-outlines.html for the latest version.



	TO252 (DPAK)					
Dim	Min	Max	Тур			
Α	2.19	2.39	2.29			
A 1	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
С	0.45	0.58	0.531			
D	6.00	6.20	6.10			
D1	5.21	-	-			
е	-	-	2.286			
Е	6.45	6.70	6.58			
E1	4.32	-	ı			
Н	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°	-			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10.700		



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