Maximum Ratings (@T_A = +25°C unless otherwise specified)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	60	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6) V_{GS} = 10V	Steady State	T_A = +25°C T_A = +70°C	I_D	5.0 4.1	A
	t<10s	T_A = +25°C T_A = +70°C	I_D	6.6 5.3	A
Maximum Body Diode Forward Current (Note 6)			I_S	2.5	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I_{DM}	30	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)			I_{SM}	30	A
Avalanche Current (Note 7) L = 0.1mH			I_{AS}	14.2	A
Avalanche Energy (Note 7) L = 0.1mH			E_{AS}	10	mJ

Thermal Characteristics (@T_A = +25°C unless otherwise specified)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T_A = +25°C	P_D	1.3	W
	T_A = +70°C		0.8	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	102	°C/W
	t<10s		61	
Total Power Dissipation (Note 6)	T_A = +25°C	P_D	1.7	W
	T_A = +70°C		1.1	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	75	°C/W
	t<10s		50	
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	14.5	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = 25°C unless otherwise specified)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	—	—	V	V_{GS} = 0V, I_D = 250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	100	nA	V_{DS} = 60V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	1	—	3	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	30	40	mΩ	V_{GS} = 10V, I_D = 4.5A
		—	35	55		V_{GS} = 4.5V, I_D = 3.5A
Forward Transfer Admittance	$\|Y_{FS}\|$	—	4.5	—	S	V_{DS} = 10V, I_D = 4.3A
Diode Forward Voltage	V_{SD}	—	0.7	1.2	V	V_{GS} = 0V, I_S = 1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{ISS}	—	1,287	—	pF	V_{DS} = 25V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{OSS}	—	57	—		
Reverse Transfer Capacitance	C_{RSS}	—	44	—		
Gate Resistance	R_G	—	1.2	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz
Total Gate Charge (V_{GS} = 10V)	Q_G	—	22.4	—	nC	V_{DS} = 30V, I_D = 4.3A
Total Gate Charge (V_{GS} = 4.5V)	Q_G	—	10.4	—		
Gate-Source Charge	Q_{GS}	—	4.9	—		
Gate-Drain Charge	Q_{GD}	—	3.0	—		
Turn-On Delay Time	$t_{D(ON)}$	—	6.6	—	ns	V_{GS} = 10V, V_{DD} = 30V, R_G = 6Ω, I_D = 4.3A
Turn-On Rise Time	t_R	—	8.1	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	20.1	—		
Turn-Off Fall Time	t_F	—	4.0	—		
Body Diode Reverse Recovery Time	t_{RR}	—	18	—	ns	I_S = 4.3A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q_{RR}	—	11.9	—	nC	I_S = 4.3A, di/dt = 100A/µs

Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

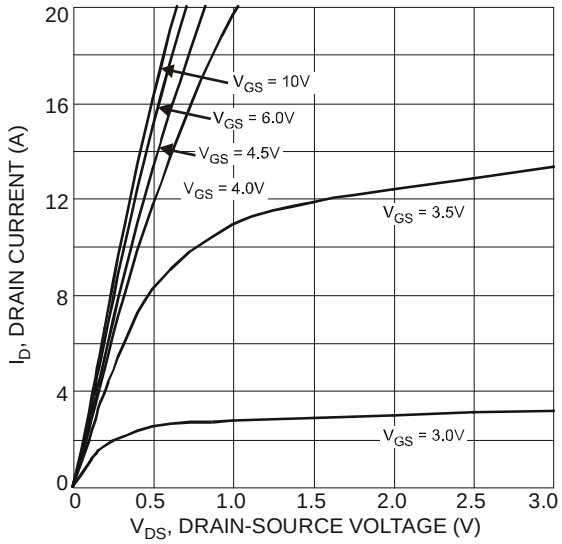


Fig.1 Typical Output Characteristic

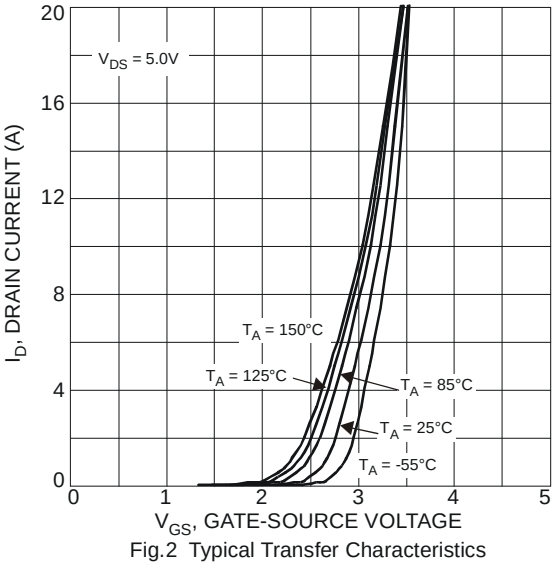


Fig.2 Typical Transfer Characteristics

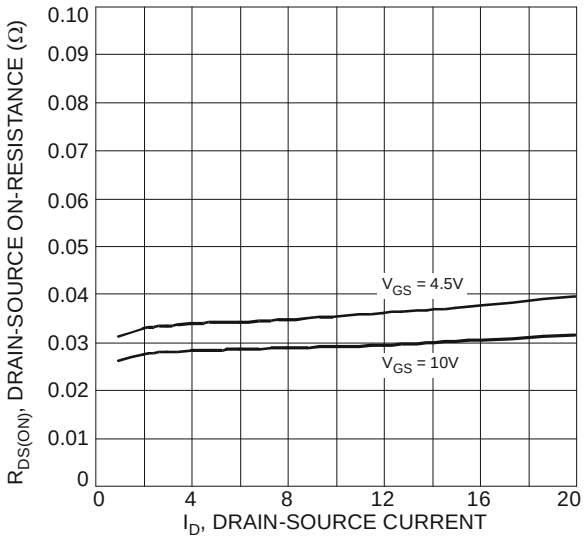


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

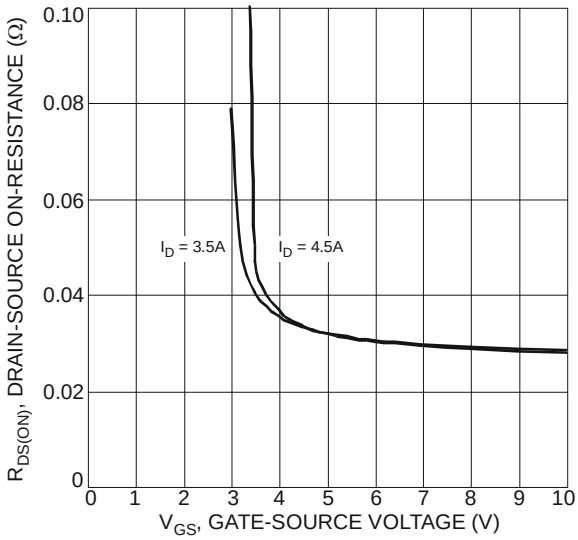


Fig. 4 Typical On-Resistance vs. Drain Current and Gate Voltage

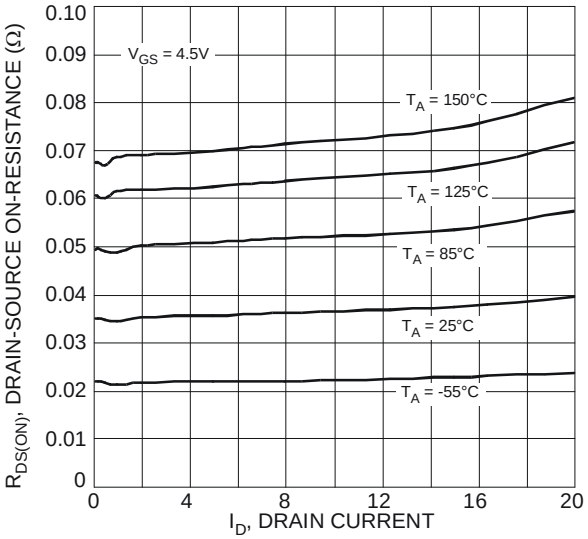


Fig. 5 Typical On-Resistance vs. Drain Current and Temperature

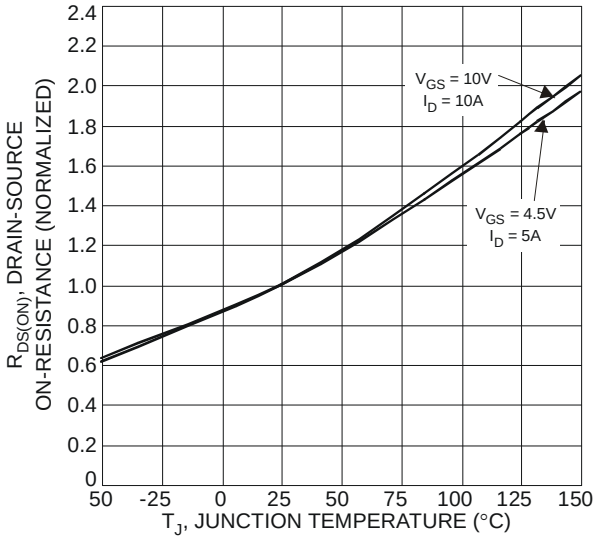


Fig. 6 On-Resistance Variation with Temperature

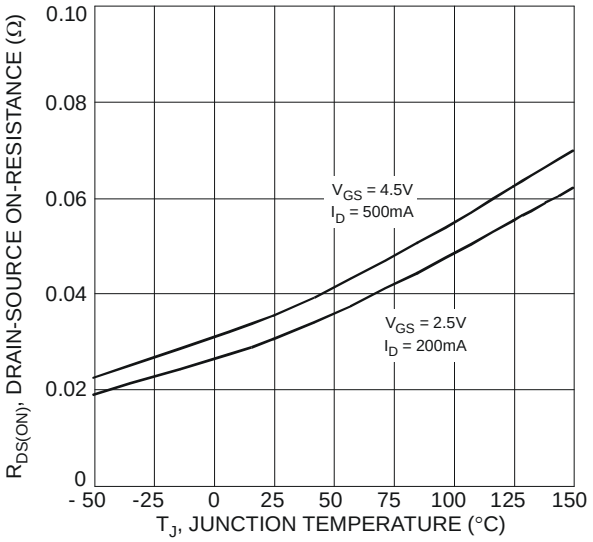


Fig. 7 On-Resistance Variation with Temperature

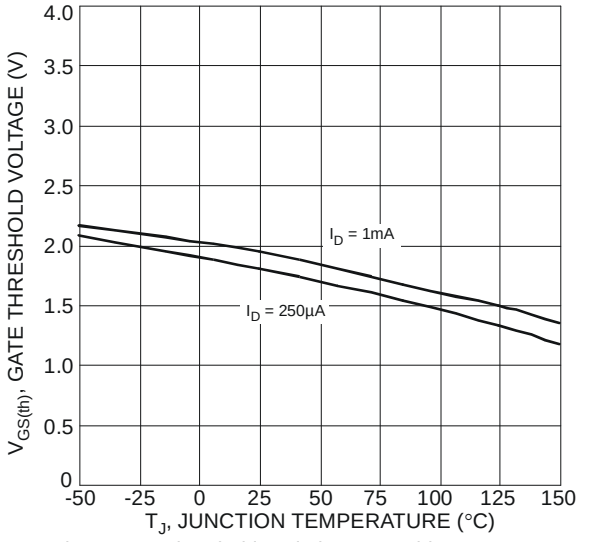


Fig. 8 Gate Threshold Variation vs. Ambient Temperature

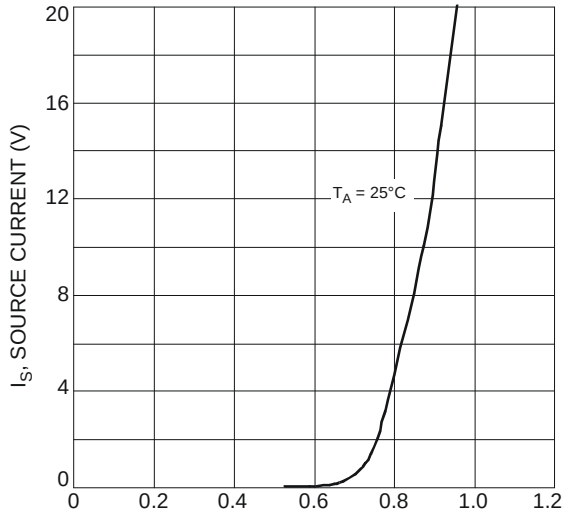


Fig.9 Diode Forward Voltage vs. Current

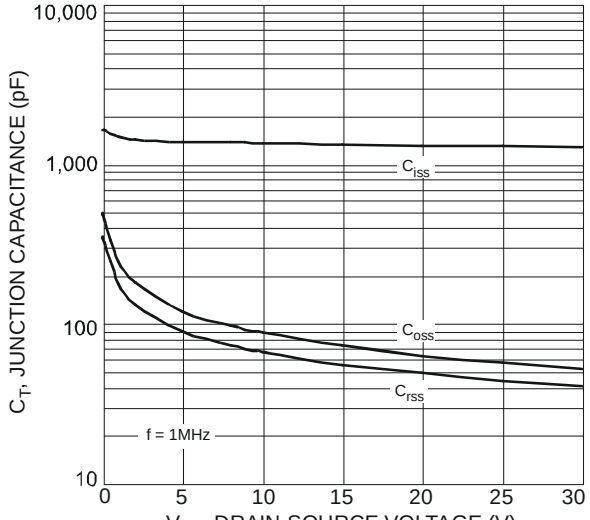


Fig. 10 Typical Junction Capacitance

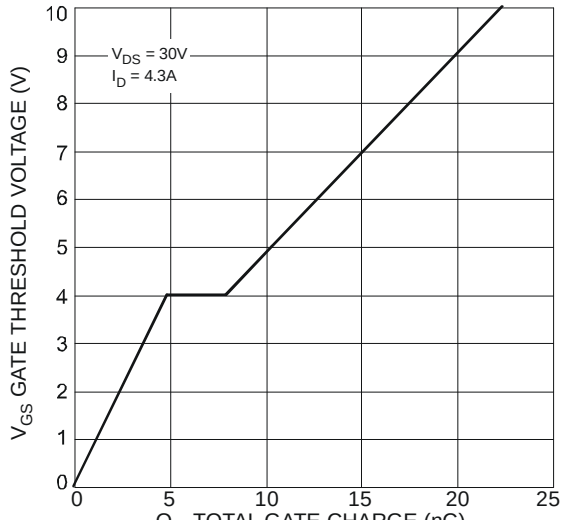


Fig. 11 Gate Charge

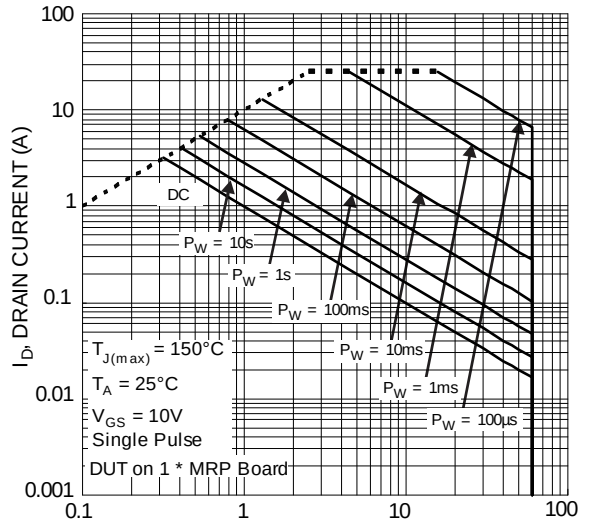


Figure 12 SOA, Safe Operation Area

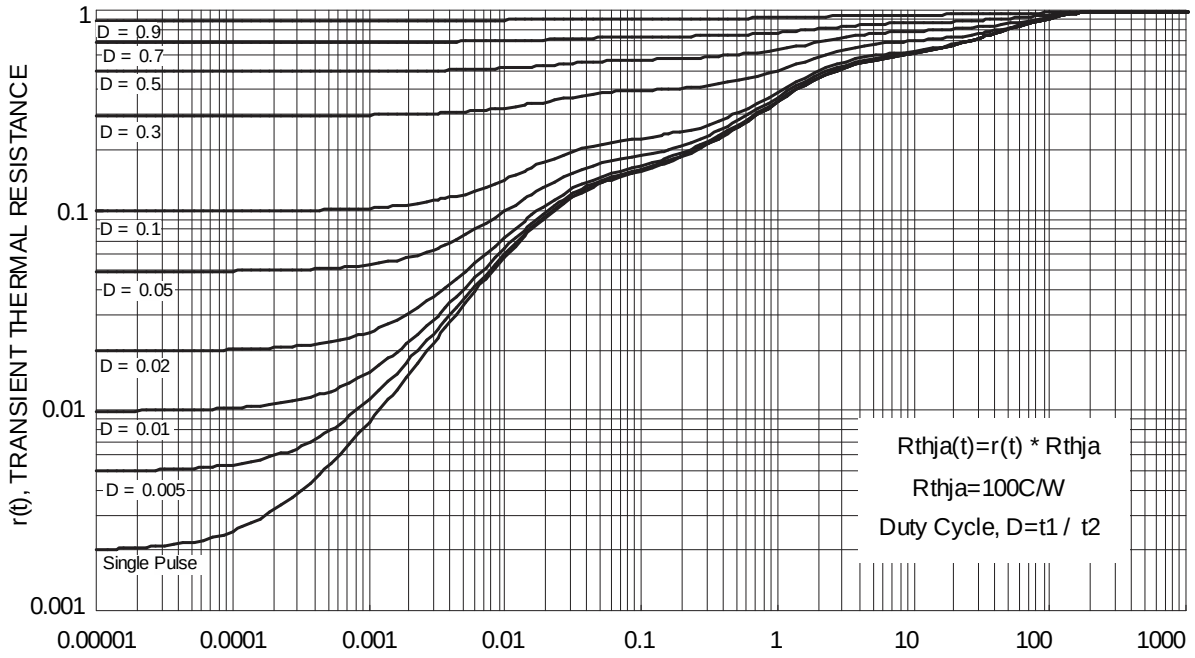
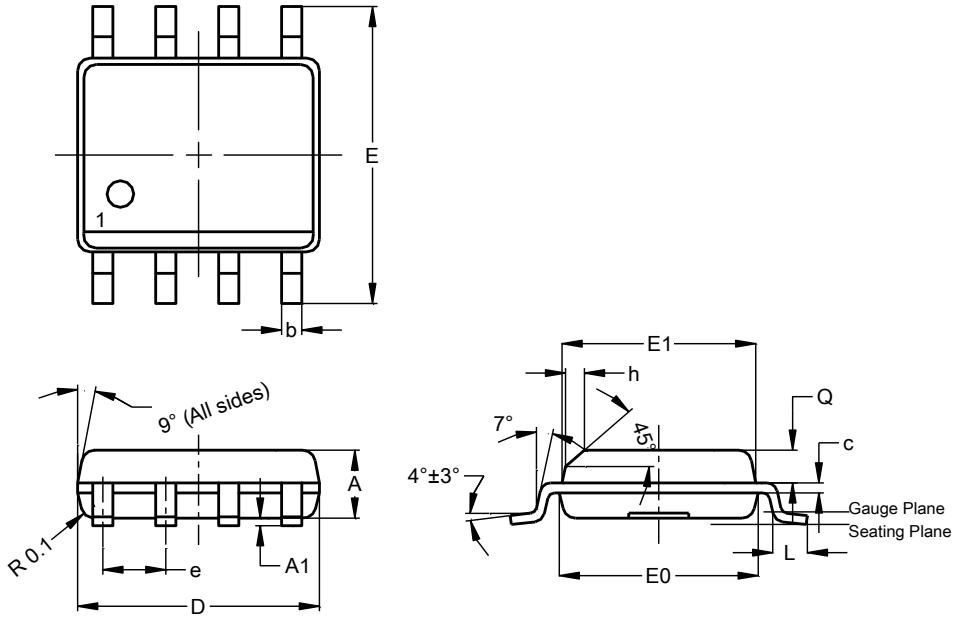


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8

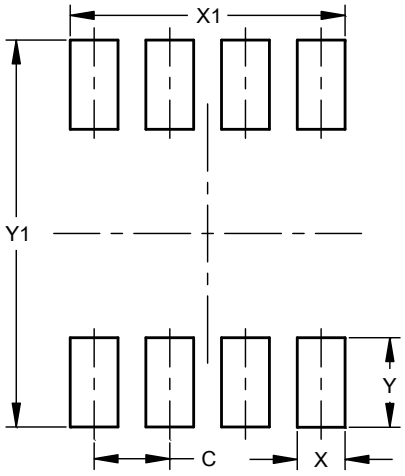


SO-8			
Dim	**Min**	**Max**	**Typ**
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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