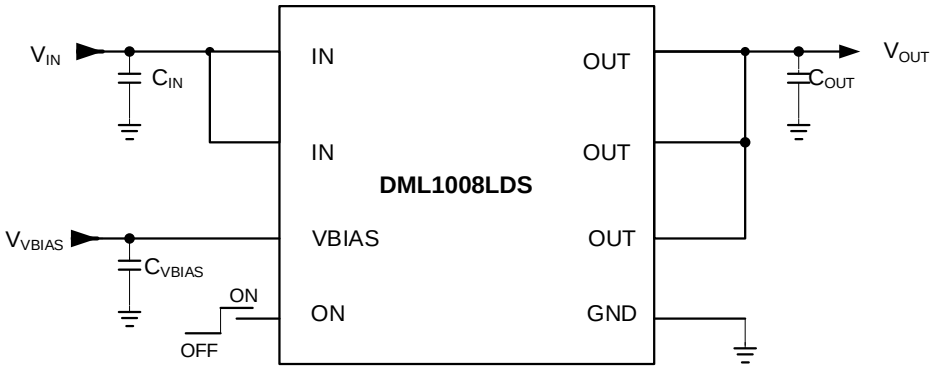
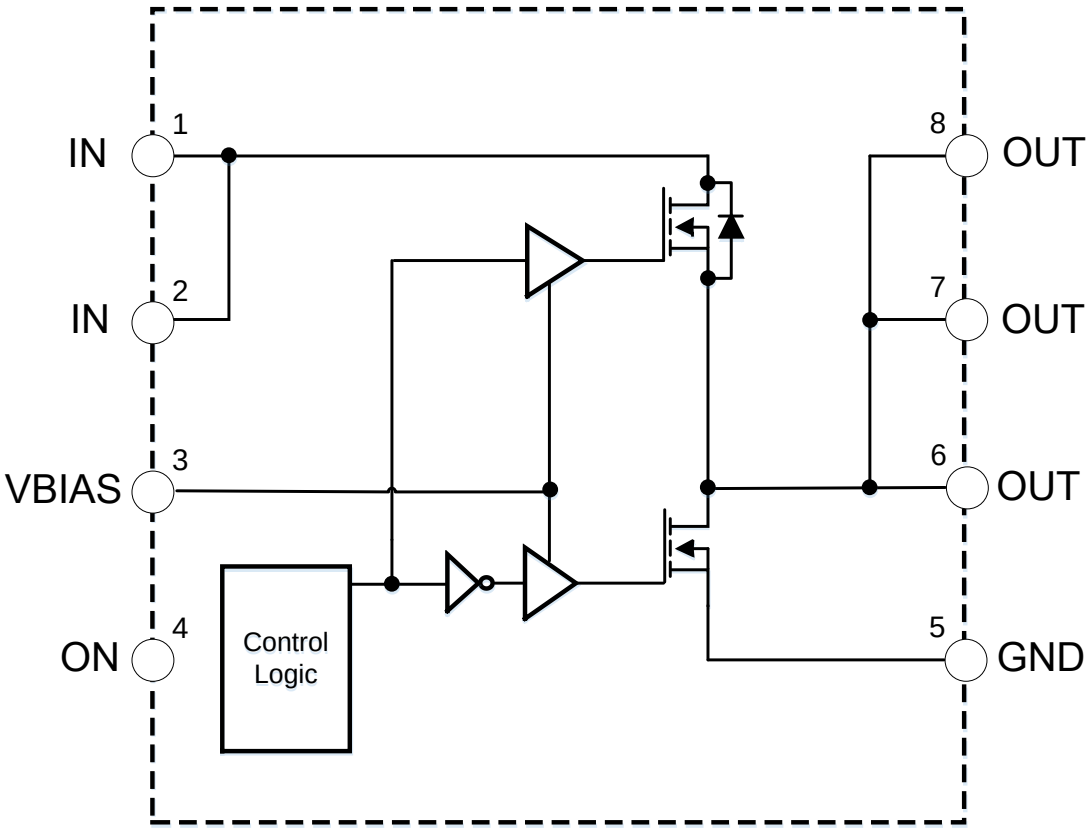
Typical Application Circuit



Pin Description

Pin Number	Pin Name	Pin Function
1, 2, EPAD	IN	Load Switch Input. Bypass capacitor is recommended to minimize input voltage dip.
3	VBIAS	Bias Voltage. Power supply input for the device.
4	ON	Enable Input. Load switch is on when ON is pulled high. Load switch is off when ON is pulled low. Do not leave floating.
5	GND	Ground.
6, 7, 8	OUT	Load switch output.

Functional Block Diagram



Absolute Maximum Rating

Parameter	Rating
IN, ON, VBIAS, OUT to GND Voltage	-0.3V to 6V
Junction Temperature (T_J)	+150°C
I_{MAX}	12A
Storage Temperature (T_S)	-65°C to +150°C
ESD Rating HBM/CDM	2kV/1kV

Recommended Operating Ranges

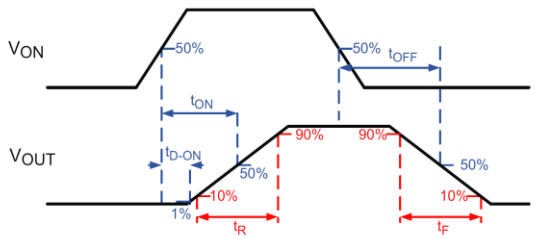
Parameter	Rating
Supply Voltage (V_{VBIAS})	3.2V to 5.5V
Input Voltage (V_{IN})	0.8V to V_{VBIAS}-1.5V
Ambient Temperature (T_A)	-40°C to +85°C
Package Thermal Resistance (θ_{JC})	8°C/W
Package Thermal Resistance (θ_{JA})	60°C/W

Electrical Characteristics (T_A = +25°C, V_{VBIAS}=5V, V_{IN}=1.05V, unless otherwise specified.)

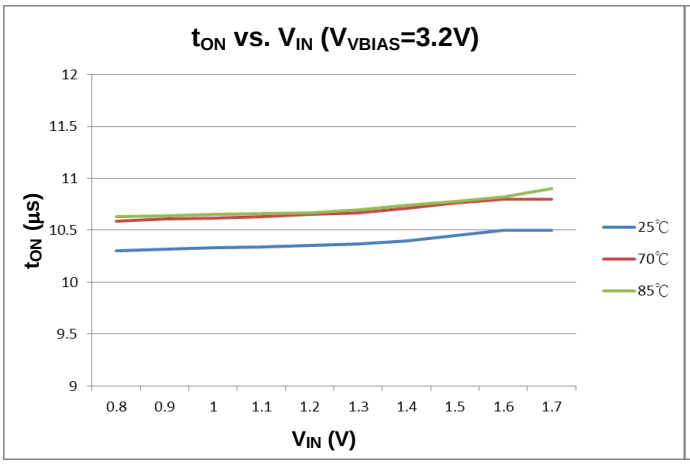
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{IN}	IN Supply Voltage	V_{ON} = 5V	0.8	1.05	V_{VBIAS}-1.5	V
V_{VBIAS}	VBIAS Supply Voltage	—	3.2	5	5.5	V
I_D	Maximum Continuous Current	V_{ON} = 5V	—	6	—	A
I_{PLS}	Maximum Pulsed Switch Current	V_{IN} = V_{ON} = 5V Pulse < 300µs, 2% Duty Cycle	—	9	—	A
I_Q	Quiescent Supply Current of VBIAS	I_{OUT} = 0V, V_{ON} = 5V	—	35	—	µA
I_{OFF}	VBIAS Shutdown Supply Current	V_{ON} = 0V, V_{OUT} = 0V	—	—	2	µA
I_{INOFF}	IN Shutdown Supply Current	V_{ON} = 0V, V_{OUT} = 0V	—	—	2	µA
I_{ON}	ON Leakage Current	V_{ON} = 5V	—	—	1	µA
V_{ONH}	ON High Level Voltage	—	1.2	—	—	V
V_{ONL}	ON Low Level Voltage	—	—	—	0.5	V
Switching ON Resistance						
R_{ON}	Switch ON-State Resistance	I_{OUT} = -200mA, V_{ON} = 5V, V_{VBIAS} = 5V	—	—	8	mΩ
		I_{OUT} = -200mA, V_{ON} = 5V, V_{VBIAS} = 3.3V	—	—	10	mΩ
R_{PD}	Output Pull-Down Resistance	I_{OUT} = 15mA, V_{ON} = 0V	—	—	200	Ω

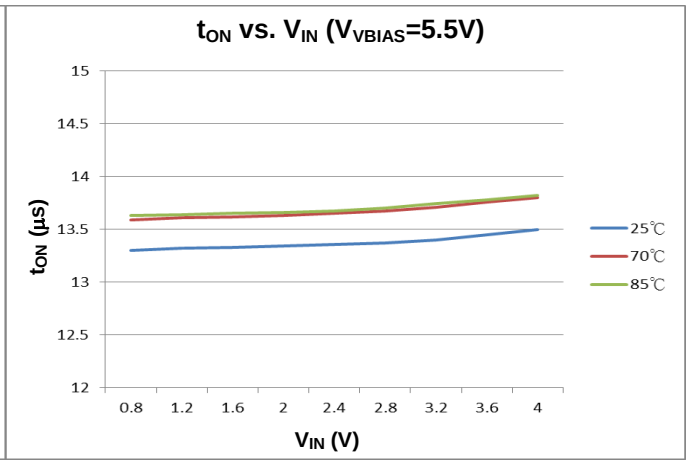
Switching Electrical Characteristics (T_A = +25°C, V_{VBIAS}=V_{ON}=5V, V_{IN}=1.05V, C_{IN}=1µF, C_{OUT}=0.1µF, unless otherwise specified.)

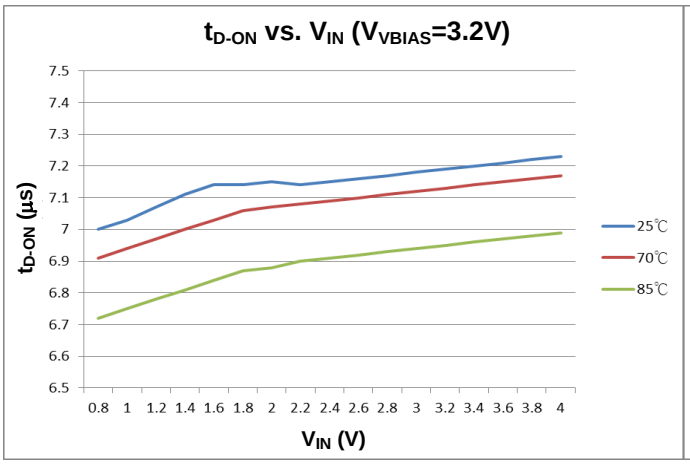
Symbol	Parameter	Min	Typ	Max	Unit
V_{IN} = 1.5V, V_{VBIAS} = V_{ON} = 5V					
t_{ON}	Turn-ON Time	10	—	65	µs
t_{D-ON}	Turn-ON Delay time	7.5	—	45	
t_R	Turn-ON Rise Time	5	—	33	
t_{OFF}	Turn-OFF Time	—	0.2	—	
t_F	Turn-OFF Fall Time	—	0.7	—	
V_{IN} = 1.05V, V_{VBIAS} = V_{ON} = 5V					
t_{ON}	Turn-ON Time	10	—	65	µs
t_{D-ON}	Turn-ON Delay Time	7.5	—	45	
t_R	Turn-ON Rise Time	5	—	33	
t_{OFF}	Turn-OFF Time	—	0.2	—	
t_F	Turn-OFF Fall Time	—	0.7	—	

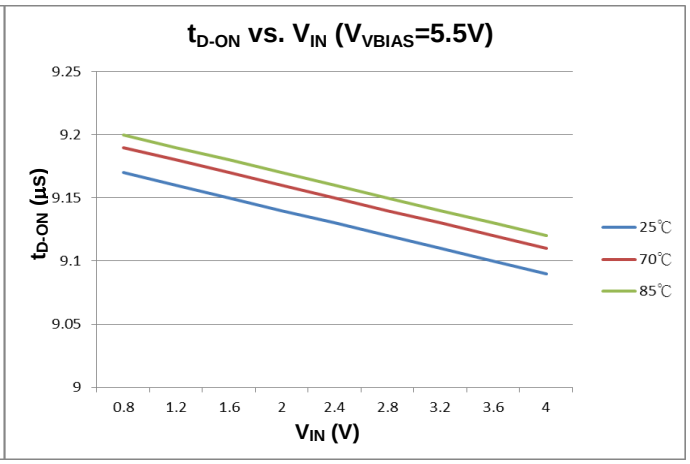


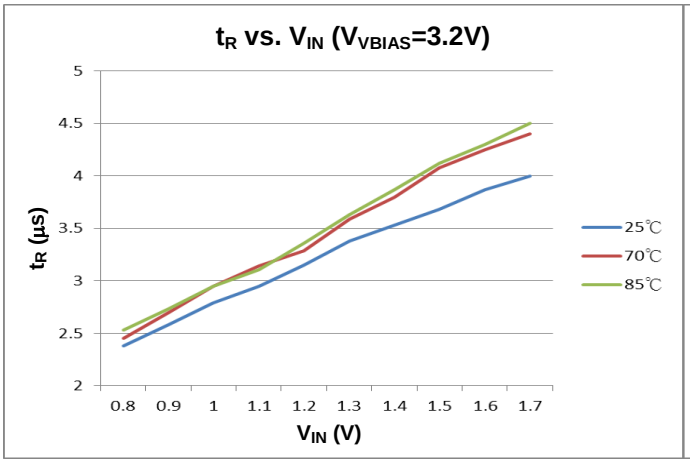
Performance Characteristics (@T_A = +25°C, unless otherwise specified.)

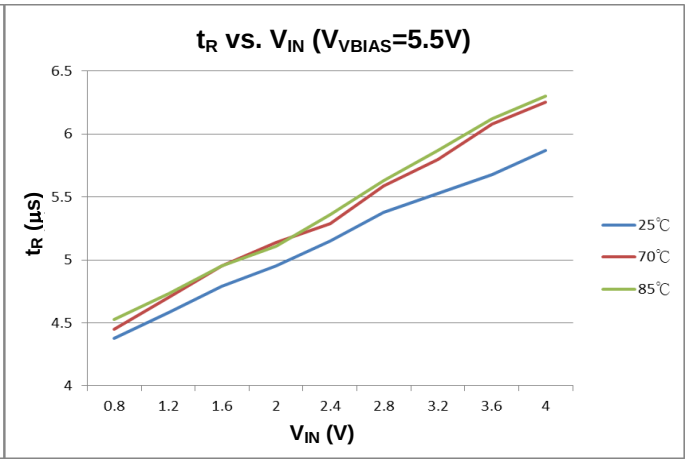




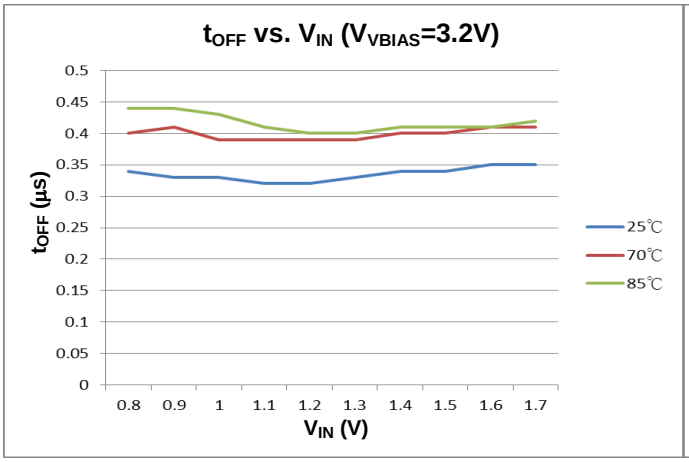


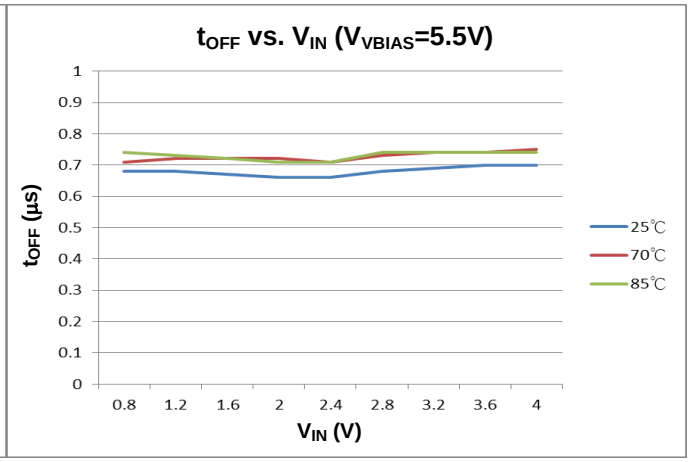


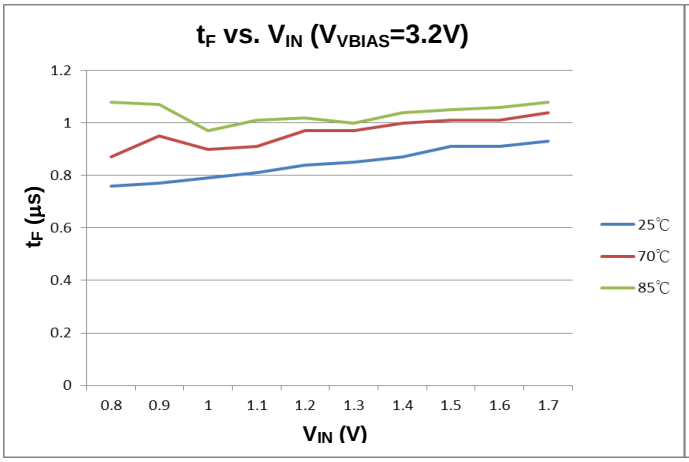


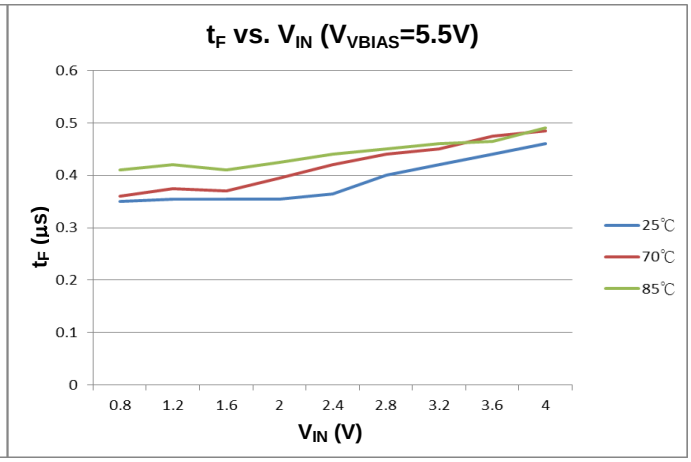


Performance Characteristics (@T_A = +25°C, unless otherwise specified. continued)



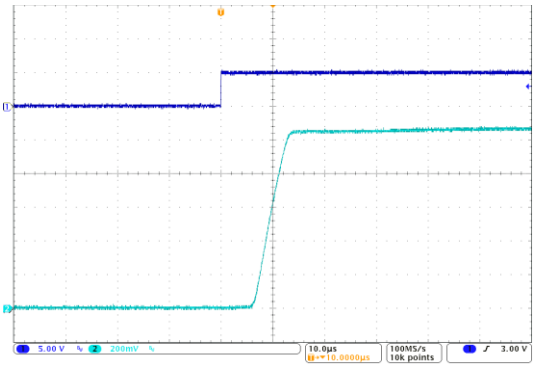




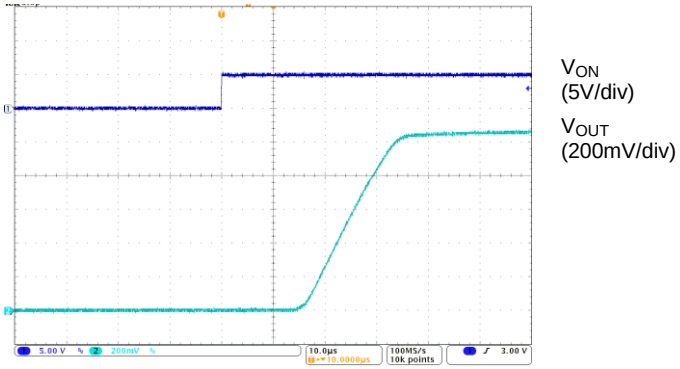


Performance Characteristics (@T_A = +25°C, unless otherwise specified. continued)

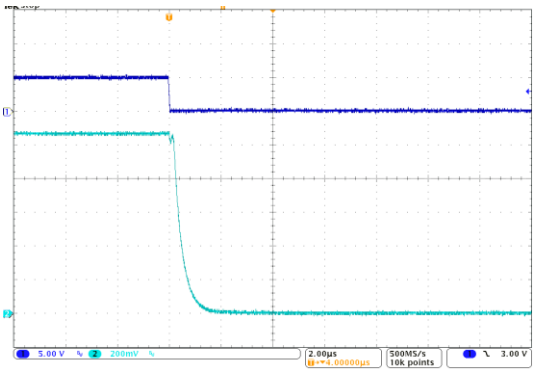
Turn-ON & Turn-ON Rise Times
V_{IN}=1.05V, V_{VBIAS}=5V, C_{IN}=1µF, C_{OUT}=0.1µF, R_{OUT}=10Ω



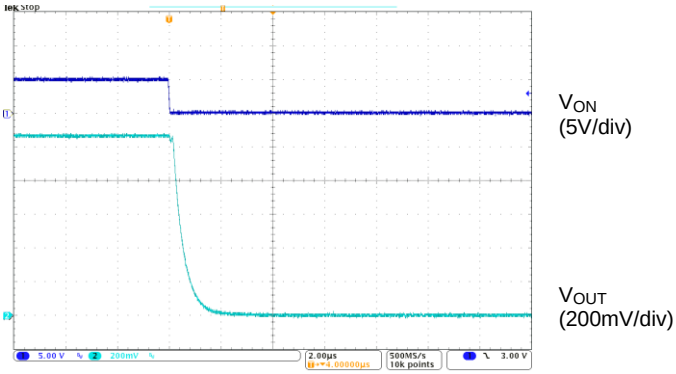
Turn-ON & Turn-ON Rise Times
V_{IN}=1.05V, V_{VBIAS}=3.2V, C_{IN}=1µF, C_{OUT}=0.1µF, R_{OUT}=10Ω



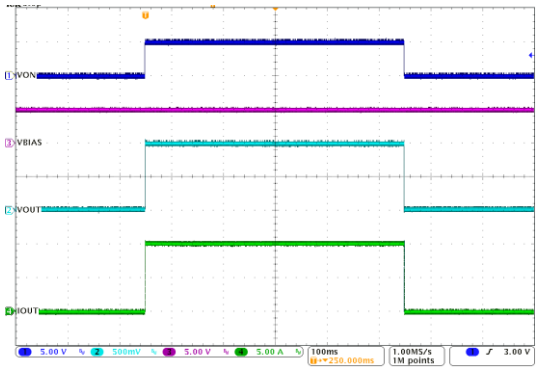
Turn-OFF & Turn-OFF Fall Times
V_{IN}=1.05V, V_{VBIAS}=5V, C_{IN}=1µF, C_{OUT}=0.1µF, R_{OUT}=10Ω



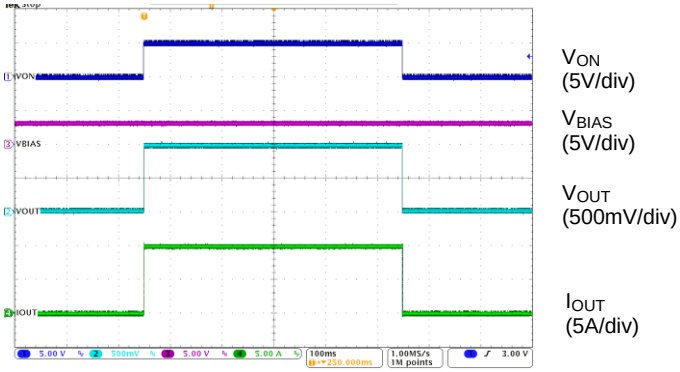
Turn-OFF & Turn-OFF Fall Times
V_{IN}=1.05V, V_{VBIAS}=3.2V, C_{IN}=1µF, C_{OUT}=0.1µF, R_{OUT}=10Ω



Turn-ON & Turn-OFF at I_{OUT}= -10A
V_{IN}=1.05V, V_{VBIAS}=5V, C_{IN}=1µF, C_{OUT}=0.1µF, R_{OUT}=0.1Ω



Turn-ON & Turn-OFF at I_{OUT}= -10A
V_{IN}=1.05V, V_{VBIAS}=3.2V, C_{IN}=1µF, C_{OUT}=0.1µF, R_{OUT}=0.1Ω



Application Information

General Description

The DML1008LDS is a single channel, 6A load switch in an 8-pin V-DFN3030-8 (Type R) package. To reduce the voltage drop in high current rails, the device implements an ultra-low resistance N-channel MOSFET which can be operated input voltage range from 0.8V to 3.5V.

The device has very low leakage current during off state. This prevents downstream circuits from pulling high standby current from the supply. Integrated control logic, driver, power supply and discharge FET eliminates the needs for any external components, which reduce solution size and bill of materials (BOM) count.

Enable Control

The DML1008LDS device allows for enabling the MOSFET in an active-high configuration. When the VBIAS supply pin has an adequate voltage applied and the ON pin is at logic high level, the MOSFET will be enabled. Similarly, when the ON pin is at logic low level, the MOSFET will be disabled. An internal pull down resistor to ground on the ON pin ensures that the MOSFET will be disabled when not being driven.

Power sequencing

The DML1008LDS device will function with any power sequence, but the output turn-on delay performance may vary from what is specified. To archives the specified performance, there are two recommended power sequences:

1.) $V_{VBIAS} \rightarrow V_{IN} \rightarrow V_{ON}$

2.) $V_{IN} \rightarrow V_{VBIAS} \rightarrow V_{ON}$

Input Capacitor

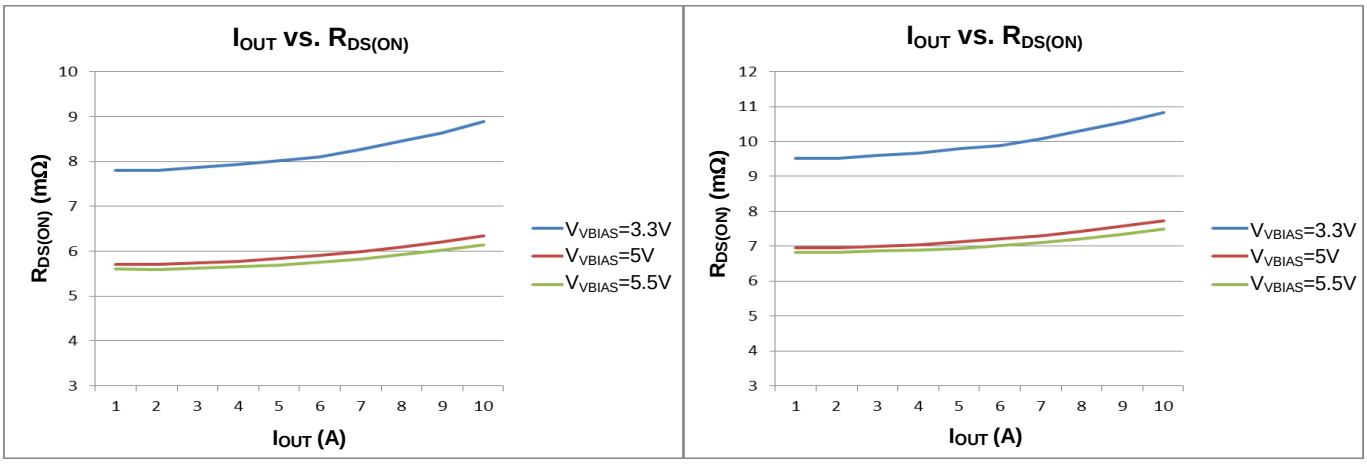
A capacitor of 10µF or higher value is recommended to be placed close to the IN pins of DML1008LDS. This capacitor can reduce the voltage drop caused by the in-rush current during the turn-on transient of the load switch. A higher value capacitor can be used to further reduce the voltage drop during high-current application.

Output Capacitor

A capacitor of 0.1µF or higher value is recommended to be placed between the OUT pins and GND pin. The switching times are affected by the capacitance. A larger capacitor makes the initial turn-on transient smoother. This capacitor must be large enough to supply a fast transient load in order to prevent the output from dropping.

V_{IN} and V_{VBIAS} Voltage Range

For optimal on-resistance of load switch, make sure $V_{IN} \leq 1.5V + V_{VBIAS}$ and V_{VBIAS} is within the voltage range from 3.2V to 5.5V. On-resistance of load switch will be higher if $V_{IN} + 1.5V > V_{VBIAS}$. Resistance curves of a typical sample device at different $V_{VBIAS} = V_{IN}$ at I_{OUT} = -200mA are shown as below.



Application Information (continued)

Thermal Considerations

To ensure proper operation, the maximum junction temperature of the DML1008LDS should not exceed +150°C. Several factors attribute to the junction temperate rise: load current, MOSFET on-resistance, junction-to-ambient thermal resistance, and ambient temperature. The maximum load current can be determined by:

$$I_{LOAD(MAX)} = \sqrt{\frac{T_{J(MAX)} - T_C}{\Theta_{JC} \times R_{DS(ON)}}}$$

Where

$I_{LOAD(MAX)}$ is the maximum allowable current on load (A). (6A for DML1008LDS)

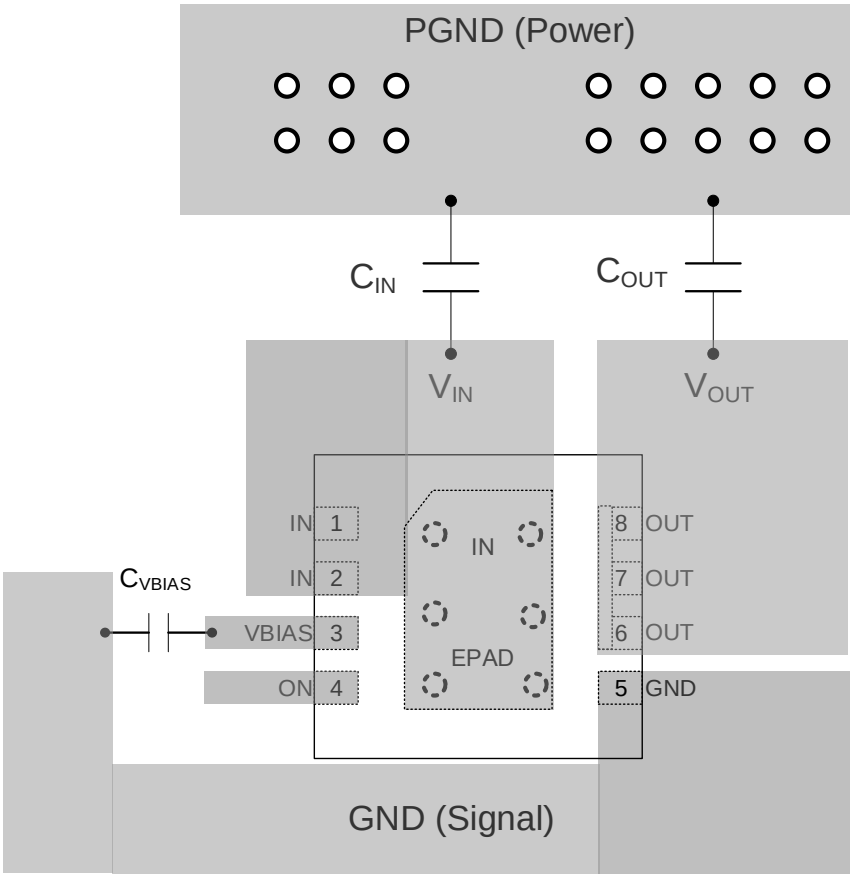
$T_{J(MAX)}$ is the maximum allowable junction temperature.

T_C is the case temperature of the device.

θ_{JC}= junction to case thermal impedance. This parameter is highly dependent upon PCB layout.

PCB Layout Consideration

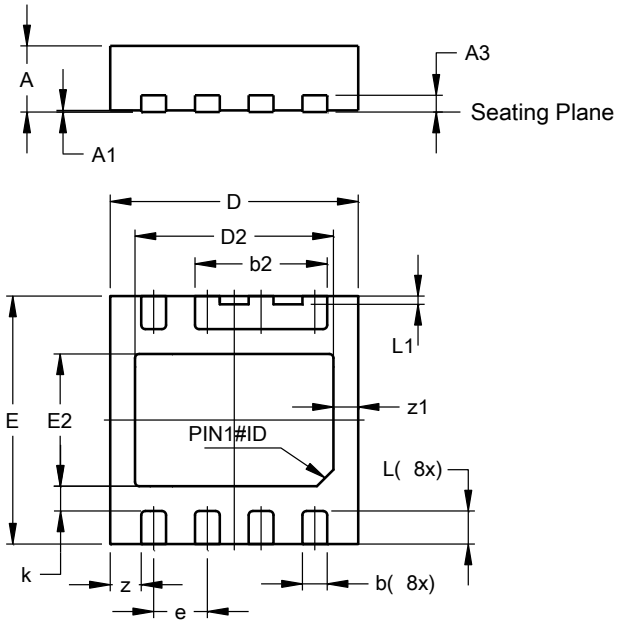
1. Place the input/output capacitors C_{IN} and C_{OUT} as close as possible to the IN and OUT pins.
2. The power traces which are IN trace, OUT trace and GND trace. They should be short, wide and directly for minimize parasitic inductance.
3. Place C_{VBIAS} capacitor near the device pin.
4. Connect the signal ground to the GND pin, and keep a single connection from GND pin to the power ground behind the input or output capacitors.
5. For better power dissipation, holes are recommended to connect to the exposed pad's landing area with a large copper polygon on the other side of the printed circuit board. The copper polygons and exposed pad shall connect to IN pin on the printed circuit board.



Package Outline Dimensions (All dimensions in mm.)

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: V-DFN3030-8 (Type R)

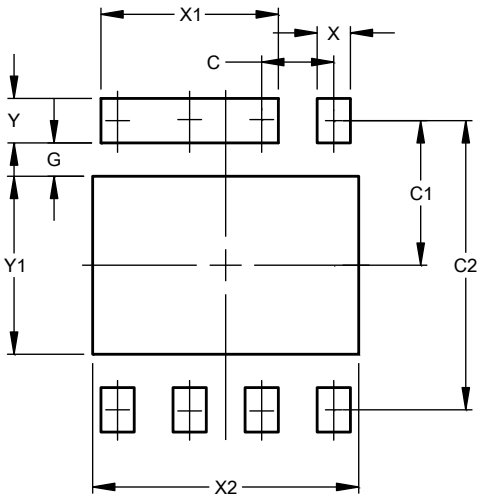


V-DFN3030-8 (Type R)			
Dim	**Min**	**Max**	**Typ**
A	0.77	0.83	0.80
A1	0.00	0.05	0.03
A3	--	--	0.203
b	0.25	0.35	0.30
b2	1.55	1.65	1.60
D	2.95	3.05	3.00
D2	2.30	2.50	2.40
E	2.95	3.05	3.00
E2	1.50	1.70	1.60
e	0.65 BSC		
k	--	--	0.30
L	0.35	0.45	0.40
L1	0.05	0.15	0.10
z	--	--	0.375
z1	--	--	0.30
All Dimensions in mm			

Suggested Pad Layout

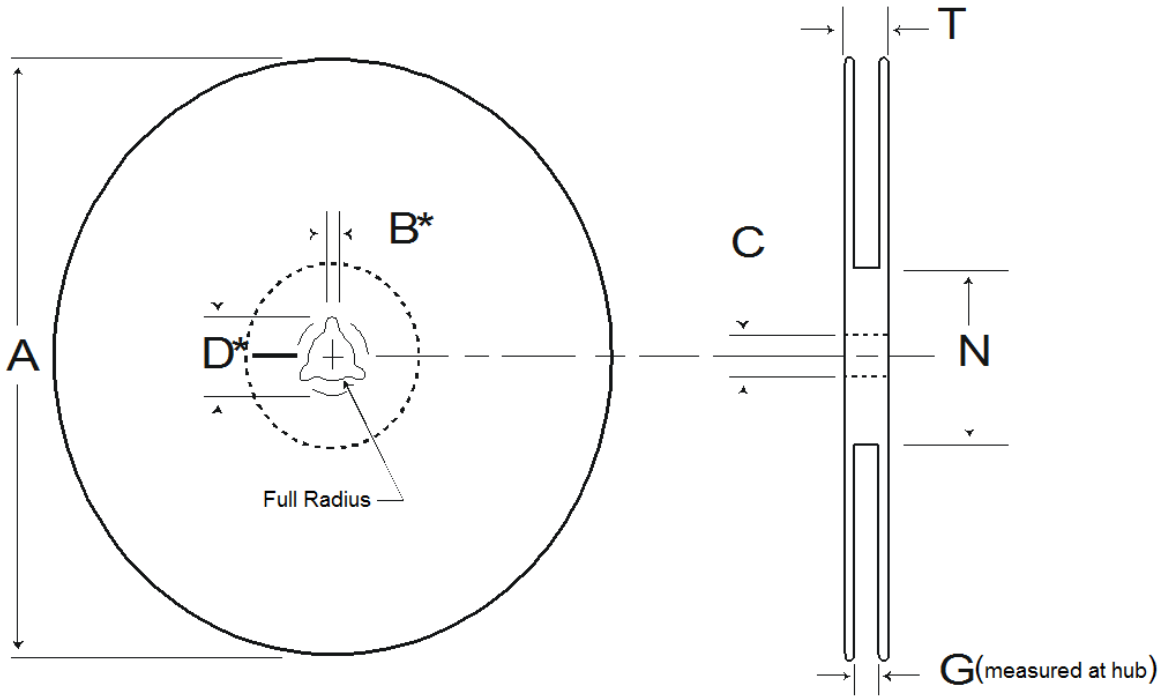
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: V-DFN3030-8 (Type R)



Dimensions	Value (in mm)
C	0.65
C1	1.30
C2	2.60
G	0.30
X	0.30
X1	1.60
X2	2.40
Y	0.40
Y1	1.60

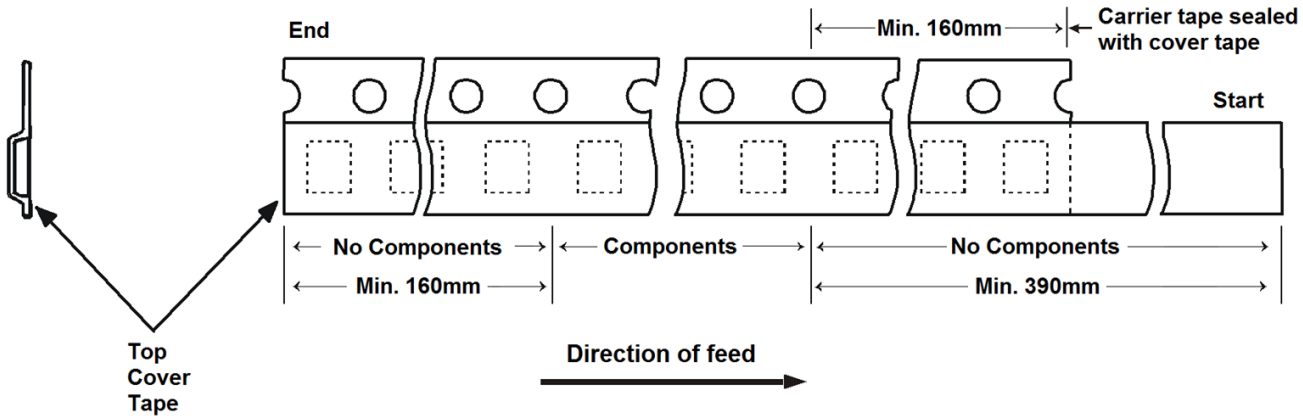
Surface Mount Reel Specifications



* Drive spokes optional. If used, dimensions with asterisks apply

Tape Width	Reel Size	A (mm)	B Max (mm)	C (mm)	D Max (mm)	N Min (mm)	G (mm)	T Max (mm)
8mm	7”	178 ±2	2.0 +0.5 -0	13 +0.5 -0.2	20.5 ±0.2	55 ±5	8.4 +1.5 -0.0	14.4
8mm	13”	330 ±2	2.0 +0.5 -0	13 +0.5 -0.2	20.5 ±0.2	100 ±2	8.4 +1.5 -0.0	14.4

Tape Leader and Trailer Specifications (Notes 5 and 6)



Notes:
5. There shall be a leader of at least 230mm which may consist of carrier tape and/or cover tape or a start tape followed by at least 160mm of empty carrier tape sealed with cover tape.
6. There shall be a trailer of at least 160mm of empty carrier tape sealed with cover tape. The entire carrier tape must release from the reel hub as the last portion of the tape unwinds from the reel without damage to the carrier tape and the remaining components in the cavities.

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B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

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