

NOT RECOMMENDED FOR NEW DESIGN USE DMN2056U

DMG2302U

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 5)	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	4.2 3.4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 6)			I _{DM}	27	Α

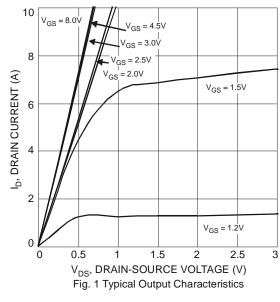
Thermal Characteristics

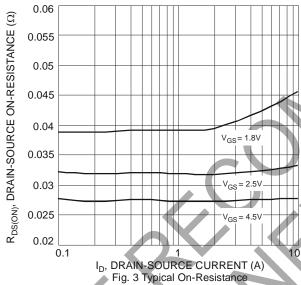
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	8.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	156	°C/W
Total Power Dissipation (Note 6)		P _D	1.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	91	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

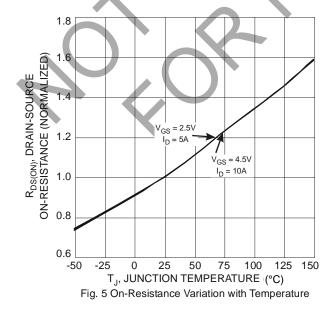
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	20			V	$V_{GS} = 0V, I_{D} = 10\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}		_	100	nA	$V_{DS} = 16V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	0.4		1.0	V	$V_{DS} = V_{GS}$, $I_D = 50\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)} —		90	mΩ	$V_{GS} = 4.5V, I_D = 3.6A$		
Static Brain Godice on Resistance	TUS(ON)			120	11122	$V_{GS} = 2.5V, I_D = 3.1A$	
Forward Transfer Admittance	Y _{fs}		13	_	S	$V_{DS} = 5V, I_{D} = 3.6A$	
Diode Forward Voltage	V _{SD}	ļ	0.75	1.0	V	$V_{GS} = 0V$, $I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)	DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		594.3	_	pF	101/1/	
Output Capacitance	C_{oss}	•	64.5	_	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}		57.7	l	pF	71 – 1.01911 12	
Gate Resistance	R_{g}	_	1.5		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_{g}	1	7.0	-	nC	$V_{GS} = 4.5V$, $V_{DS} = 10V$, $I_{D} = 3.6A$	
Gate-Source Charge	Q _{gs}	l	0.9	l	nC		
Gate-Drain Charge	Q_{gd}	1	1.4	1	nC		
Turn-On Delay Time	t _{D(ON)}	_	7.4	_	ns	V _{DD} = 10V, V _{GS} = 4.5V,	
Turn-On Rise Time	t _R	_	9.8	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	28.1	_	ns	$R_L = 2.78\Omega$, $R_G = 1.0\Omega$	
Turn-Off Fall Time	t _F	_	6.7	_	ns	<u> </u>	

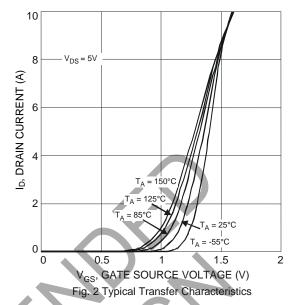
^{5.} Device mounted on FR-4 PCB, with minimum recommended pad layout.
6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. copper, single sided.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.





vs. Drain Current and Gate Voltage





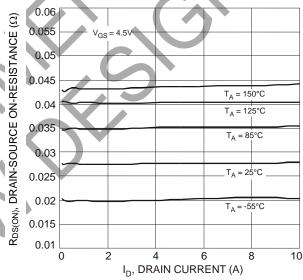
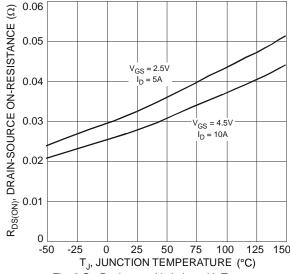
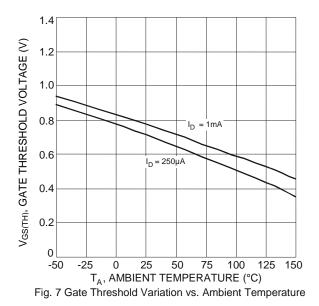
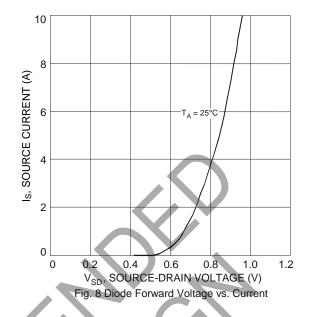


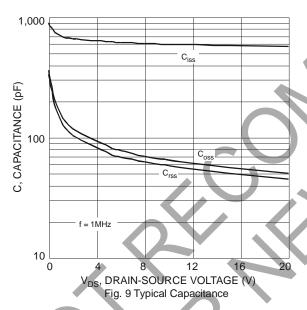
Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

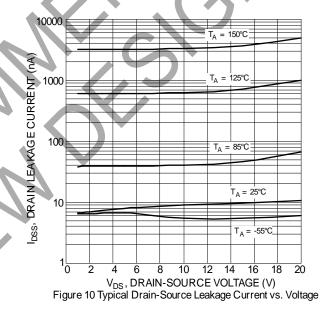












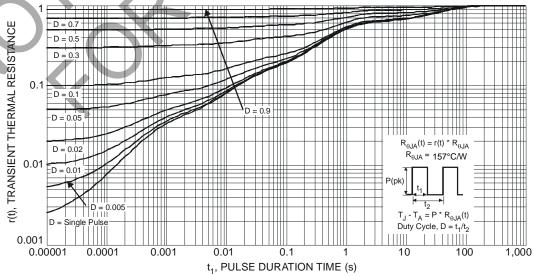


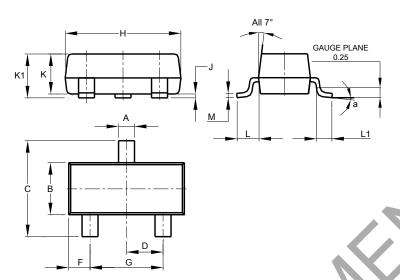
Fig. 11 Transient Thermal Response

DMG2302U

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

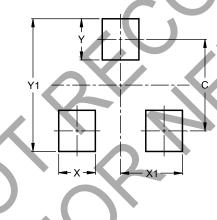


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
U	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)				
С	2.0				
Х	0.8				
X1	1.35				
Υ	0.9				
Y1	29				



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