

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±6	V
Continuous Drain Current (Note 7)	Steady State	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	I _D	0.63 0.45	А
Pulsed Drain Current			I _{DM}	3	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 7)	P_{D}	0.28	W
Thermal Resistance, Junction to Ambient (Note 7)	$R_{ hetaJA}$	452	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

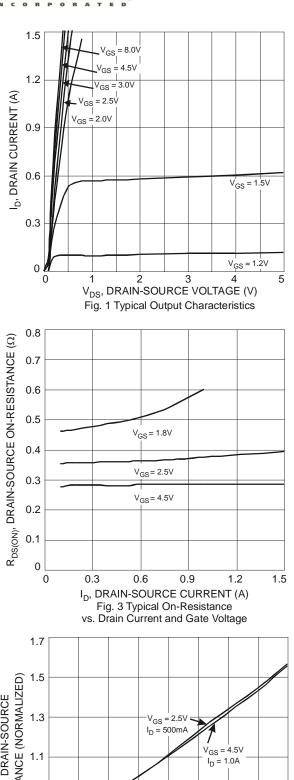
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

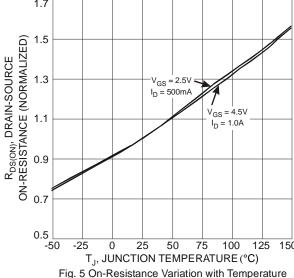
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	100	nA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±1.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
	R _{DS(ON)}	_	0.3	0.4	Ω	$V_{GS} = 4.5V, I_D = 600mA$	
Static Drain-Source On-Resistance			0.4	0.5		$V_{GS} = 2.5V, I_D = 500mA$	
			0.5	0.7		$V_{GS} = 1.8V, I_D = 350mA$	
Forward Transfer Admittance	Y _{fs}	_	1.4		S	$V_{DS} = 10V, I_D = 400mA$	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 150mA$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	60.67	_	pF	V _{DS} = 16V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	9.68	_	pF		
Reverse Transfer Capacitance	C_{rss}		5.37	-	pF		
Total Gate Charge	Qg	1	736.6	l	рС	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250mA$	
Gate-Source Charge	Q_{gs}	1	93.6	l	рС		
Gate-Drain Charge	Q_{gd}	1	116.6	l	рС		
Turn-On Delay Time	t _{D(ON)}	_	5.1	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$ $R_{L} = 47\Omega, R_{G} = 10\Omega,$ $I_{D} = 200 \text{mA}$	
Turn-On Rise Time	t _R	_	7.4	_	ns		
Turn-Off Delay Time	t _{D(OFF)}		26.7	_	ns		
Turn-Off Fall Time	t _F	_	12.3	_	ns		

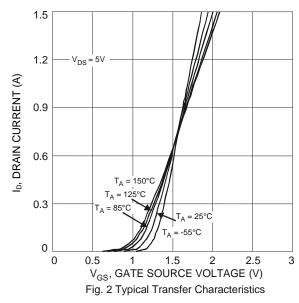
Notes:

- 7. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.









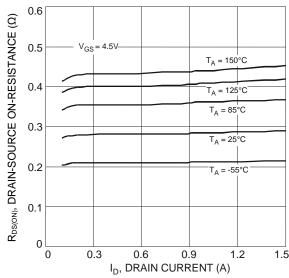


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

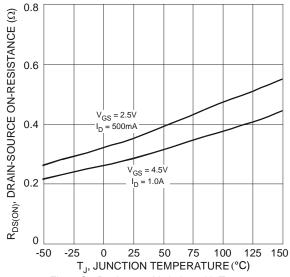


Fig. 6 On-Resistance Variation with Temperature



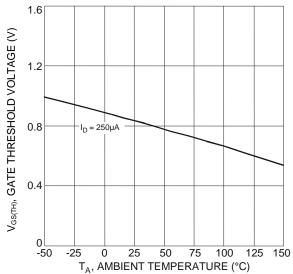
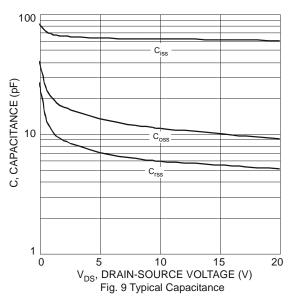
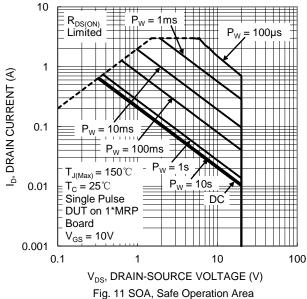
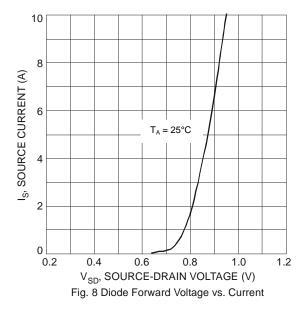
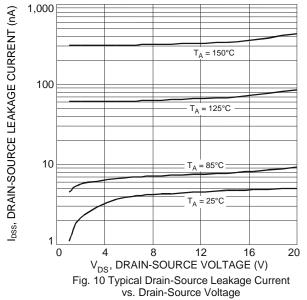


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

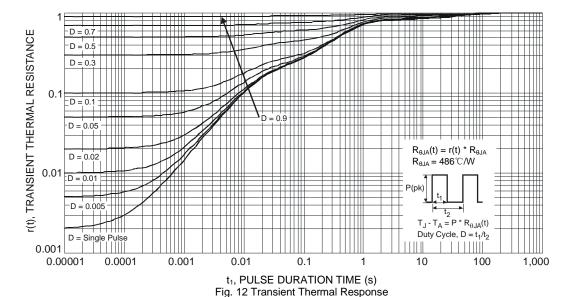












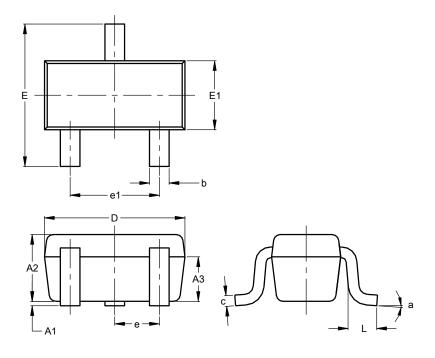
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523

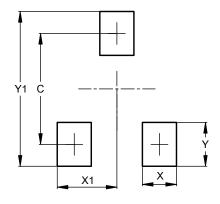


SOT523					
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.60	0.80	0.75		
A3	0.45	0.65	0.50		
b	0.15	0.30	0.22		
С	0.10	0.20	0.12		
D	1.50	1.70	1.60		
Е	1.45	1.75	1.60		
E1	0.75	0.85	0.80		
е	0.50 BSC				
e1	0.90	1.10	1.00		
L	0.20	0.40	0.33		
а	0°		8°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523



Dimensions	Value (in mm)			
С	1.29			
Х	0.40			
X1	0.70			
Y	0.51			
V1	1 80			



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