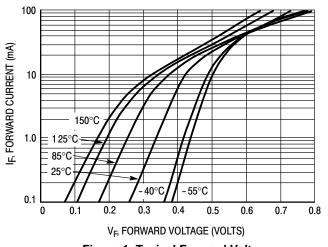
BAS40-04LT1G, SBAS40-04LT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit |
|---|--------------------|-----|-----|------|
| Reverse Breakdown Voltage (I _R = 10 μA) | V _{(BR)R} | 40 | _ | V |
| Total Capacitance (V _R = 1.0 V, f = 1.0 MHz) | Ст | _ | 5.0 | pF |
| Reverse Leakage (V _R = 25 V) | I _R | _ | 1.0 | μΑ |
| Forward Voltage (I _F = 1.0 mA) | V _F | _ | 380 | mV |
| Forward Voltage (I _F = 10 mA) | V _F | - | 500 | mV |
| Forward Voltage (I _F = 40 mA) | V _F | _ | 1.0 | V |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



T_A = 150°C IR, REVERSE CURRENT (uA) 125°C 10 85°C 1.0 0.1 25°C 0.01 0.001 5.0 20 25 10 15 V_R, REVERSE VOLTAGE (VOLTS)

Figure 1. Typical Forward Voltage

Figure 2. Reverse Current versus Reverse Voltage

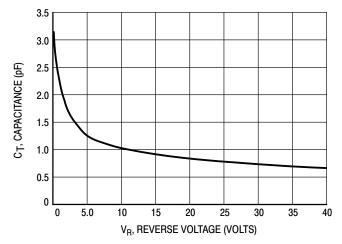


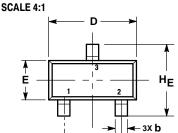
Figure 3. Typical Capacitance





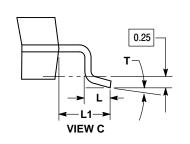
SOT-23 (TO-236) CASE 318-08 **ISSUE AS**

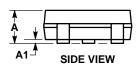
DATE 30 JAN 2018

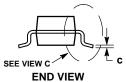


e

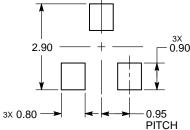
TOP VIEW







RECOMMENDED SOLDERING FOOTPRINT



DIMENSIONS: MILLIMETERS

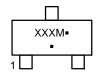
NOTES:

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
 MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,

PROTRUSIONS, OR GATE BURRS.

| | MILLIMETERS | | | INCHES | | |
|-----|-------------|------|------|--------|-------|-------|
| DIM | MIN | NOM | MAX | MIN | NOM | MAX |
| Α | 0.89 | 1.00 | 1.11 | 0.035 | 0.039 | 0.044 |
| A1 | 0.01 | 0.06 | 0.10 | 0.000 | 0.002 | 0.004 |
| b | 0.37 | 0.44 | 0.50 | 0.015 | 0.017 | 0.020 |
| С | 0.08 | 0.14 | 0.20 | 0.003 | 0.006 | 0.008 |
| D | 2.80 | 2.90 | 3.04 | 0.110 | 0.114 | 0.120 |
| E | 1.20 | 1.30 | 1.40 | 0.047 | 0.051 | 0.055 |
| е | 1.78 | 1.90 | 2.04 | 0.070 | 0.075 | 0.080 |
| L | 0.30 | 0.43 | 0.55 | 0.012 | 0.017 | 0.022 |
| L1 | 0.35 | 0.54 | 0.69 | 0.014 | 0.021 | 0.027 |
| HE | 2.10 | 2.40 | 2.64 | 0.083 | 0.094 | 0.104 |
| Т | 0° | | 10° | 0° | | 10° |

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

| _ | | ┚╽ | L | | |
|-----------|---|----|---|--------|--|
| 3X 0 80 - | _ | | | - 0.95 | |

| STYLE 1 THRU 5: | STYLE 6: | STYLE 7: | STYLE 8: |
|-----------------|-----------------------------|-----------------------------|---------------------------------|
| CANCELLED | PIN 1. BASE | PIN 1. EMITTER | PIN 1. ANODE |
| | EMITTER | 2. BASE | NO CONNECTION |
| | COLLECTOR | COLLECTOR | CATHODE |

STYLE 9: STYLE 10: PIN 1. DRAIN STYLE 11: PIN 1. ANODE STYLE 12: PIN 1. CATHODE STYLE 14: PIN 1. CATHODE STYLE 13: PIN 1. SOURCE PIN 1. ANODE 2. ANODE 2. SOURCE 2. CATHODE 2. CATHODE 2. DRAIN 2. GATE 3. ANODE 3. GATE 3. CATHODE-ANODE 3. ANODE 3. GATE

STYLE 15: STYLE 16: STYLE 17: STYLE 18: STYLE 19: STYLE 20: PIN 1. CATHODE 2. ANODE PIN 1. GATE 2. CATHODE PIN 1. ANODE 2. CATHODE PIN 1. NO CONNECTION PIN 1. NO CONNECTION 2. CATHODE PIN 1. CATHODE 2. ANODE 2. ANODE 3. ANODE 3. CATHODE 3. CATHODE-ANODE

STYLE 22: STYLE 25: STYLE 23: STYLE 26: STYLE 21: STYLE 24: PIN 1. GATE 2. DRAIN 3. SOURCE PIN 1. ANODE 2. CATHODE 3. GATE PIN 1. GATE PIN 1. RETURN PIN 1. ANODE PIN 1. CATHODE 2. ANODE 2. SOURCE 2. OUTPUT ANODE
 CATHODE 3. NO CONNECTION

DRAIN 3. INPUT STYLE 27: PIN 1. CATHODE 2. CATHODE STYLE 28: PIN 1. ANODE 2. ANODE 3. CATHODE 3. ANODE

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| ISSUE | REVISION | DATE |
|-------|--|-------------|
| AJ | ADDED STYLE 27. REQ. BY P. LEM. | 07 JUL 2004 |
| AK | OBSOLETED -09 VERSION. REQ. BY D. TRUHITTE. | 14 SEP 2004 |
| AL | ADDED NOMINAL VALUES AND UPDATED GENERIC MARKING DIAGRAM. REQ. BY HONG XIAO. | 27 MAY 2005 |
| AM | REDREW LEAD SIDE VIEW. REQ BY DARRELL TRUHITTE. | 26 AUG 2005 |
| AN | REINTRODUCED LABELS FOR DIMENSION C. REQ. BY D. TRUHITTE. | 14 OCT 2005 |
| AP | ADDED THETA DEGREE VALUES TO DIMENSION TABLE. REQ. BY D. TRUHITTE. | 17 NOV 2009 |
| AR | MODIFIED DIMENSIONS C AND L. REQ. BY M. YOU. | 10 OCT 2016 |
| AS | ADDED STYLE 28. REQ. BY E. ESTILLER. | 30 JAN 2018 |
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