

## **Maximum Ratings** $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	$V_{RRM}$		
Working Peak Reverse Voltage	$V_{RWM}$	40	V
DC Blocking Voltage $@ I_R = 0.1 \text{mA}$	$V_R$		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Rectified Output Current @ T <sub>T</sub> = 115°C	Io	1.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	45	А
Non-Repetitive Peak Forward Surge Current 5μs Single Half Sine-Wave	I <sub>FSM</sub>	430	А

### **Thermal Characteristics**

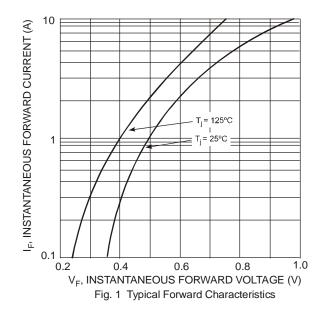
Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Terminal (Note 4)	$R_{ hetaJT}$	36	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

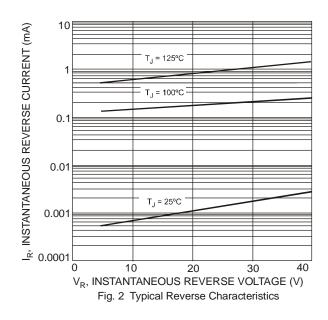
# **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	V <sub>F</sub>	-	-	0.53	V	$I_F = 1.0A, T_A = 25^{\circ}C$
		-	-	0.49		$I_F = 1.0A, T_A = 125$ °C
		-	-	0.70		$I_F = 2.0A, T_A = 25^{\circ}C$
		-	-	0.64		I <sub>F</sub> = 2.0A, T <sub>A</sub> = 125°C
Leakage Current (Note 5)	I <sub>R</sub>	-	-	0.1	m A	$V_R = 40V, T_A = 25^{\circ}C$
		-	-	4.0		$V_R = 40V, T_A = 100^{\circ}C$
Total Capacitance	C <sub>T</sub>	-	-	80	рF	$V_R = 5V$ , $f = 1MHz$

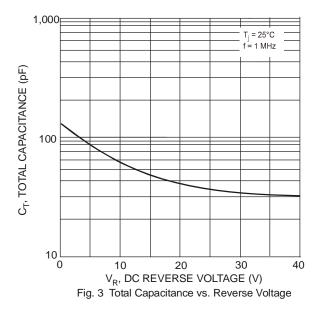
Notes:

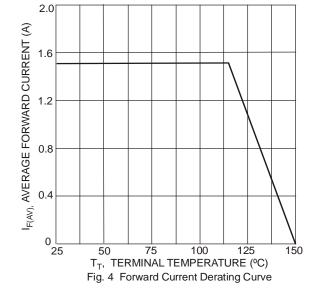
- Thermal Resistance: Junction to terminal, unit mounted on PC board with 5.0 mm² (0.013 mm thick) copper pads as heat sink.
- Short duration pulse test used to minimize self-heating effect.

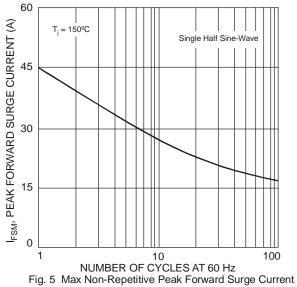




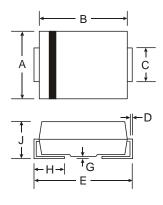








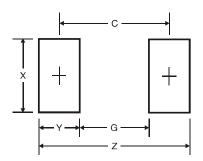
# **Package Outline Dimensions**



SMB		
Dim	Min	Max
Α	3.30	3.94
В	4.06	4.57
С	1.96	2.21
D	0.15	0.31
Е	5.00	5.59
G	0.05	0.20
Н	0.76	1.52
J	2.00	2.50
All Dimensions in mm		



### **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	6.7
G	1.8
Х	2.3
Υ	2.5
С	4.3

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