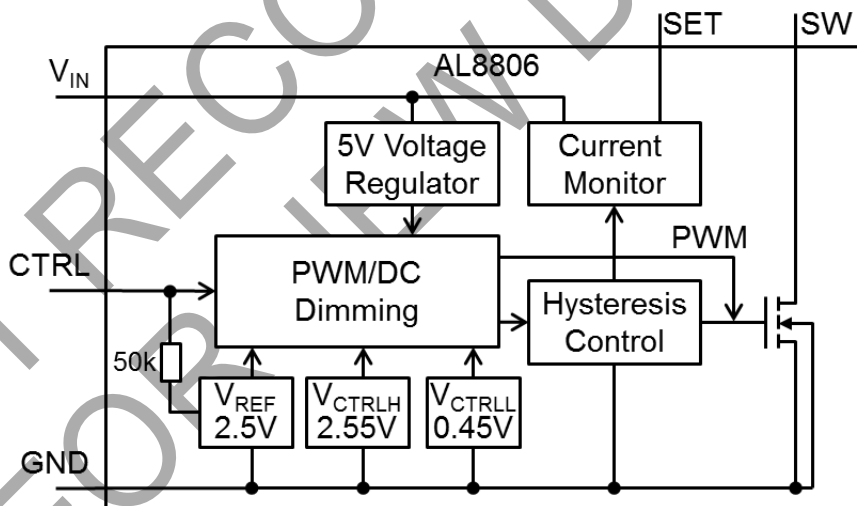


## Pin Descriptions

Pin Name	Pin Number MSOP-8EP	Function
SW	5, 6	Switch Pin. Connect inductor/freewheeling diode here, minimizing track length at this pin to reduce EMI.
GND	2, 3	GND Pin
CTRL	4	Dimming and On/Off Control Input. <ul style="list-style-type: none"> <li>Leave floating for normal operation.</li> <li>(<math>V_{CTRL} = V_{REF} = 2.5V</math> giving nominal average output current <math>I_{OUTnom} = 0.1/R_S</math>)</li> <li>Drive to voltage below 0.4V to turn off output current</li> <li>Drive with DC voltage (<math>0.5V &lt; V_{CTRL} &lt; 2.5V</math>) to adjust output current from 20% to 100% of <math>I_{OUTnom}</math></li> <li>A PWM signal (low level <math>\leq 0.4V</math> and high level <math>&gt; 2.6V</math>; transition times less than 1<math>\mu s</math>) allows the output current to be adjusted below the level set by the resistor connected to SET input pin.</li> </ul>
SET	1	Set Nominal Output Current Pin. Configure the output current of the device.
$V_{IN}$	8	Input Supply Pin. Must be locally decoupled to GND with $\geq 2.2\mu F$ X7R ceramic capacitor – see applications section for more information.
EP	EP	Exposed pad: Connect to GND and thermal mass for enhanced thermal impedance. It should not be used as electrical ground conduction path.
N/C	7	no connection

## Functional Block Diagram



**Absolute Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Ratings	Unit
ESD HBM	Human Body Model ESD Protection	2.5	kV
ESD MM	Machine Model ESD Protection	200	V
V <sub>IN</sub>	Continuous V <sub>IN</sub> pin voltage relative to GND	-0.3 to +40	V
V <sub>SW</sub>	SW voltage relative to GND	-0.3 to +40	V
V <sub>CTRL</sub>	CTRL pin input voltage	-0.3 to +6	V
I <sub>SW-RMS</sub>	DC or RMS switch current	1.65	A
I <sub>SW-PK</sub>	Peak switch current (<10%)	3	A
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>LEAD</sub>	Lead Temperature Soldering	300	°C
T <sub>ST</sub>	Storage Temperature Range	-65 to +150	°C

Caution: Stresses greater than the 'Absolute Maximum Ratings' specified above, may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time. Semiconductor devices are ESD sensitive and may be damaged by exposure to ESD events. Suitable ESD precautions should be taken when handling and transporting these devices.

**Recommended Operating Conditions** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

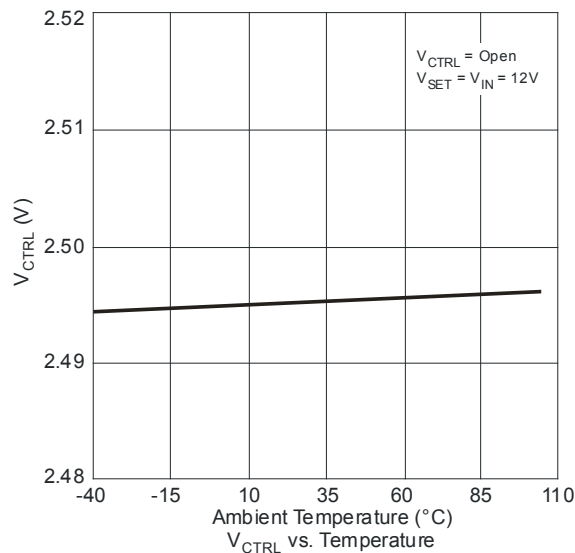
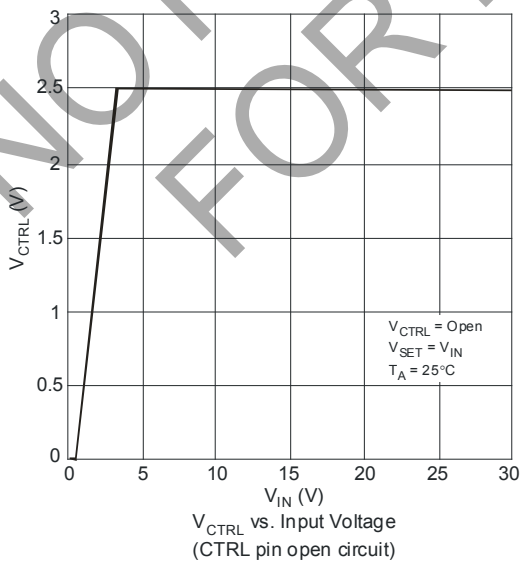
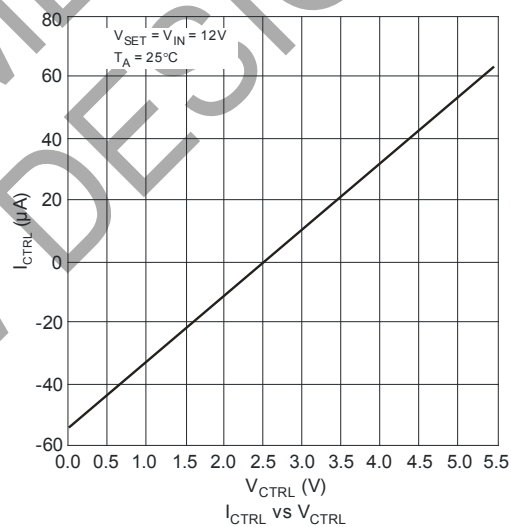
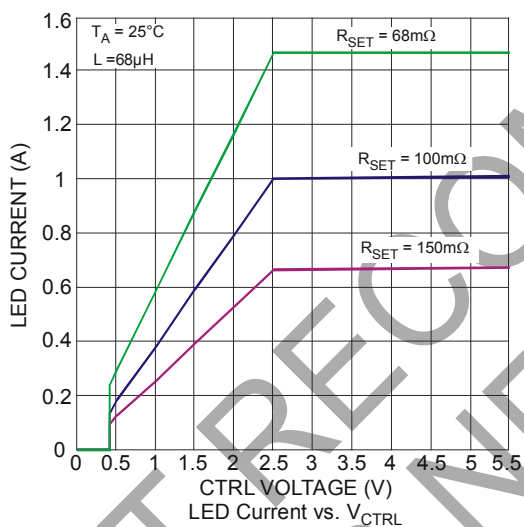
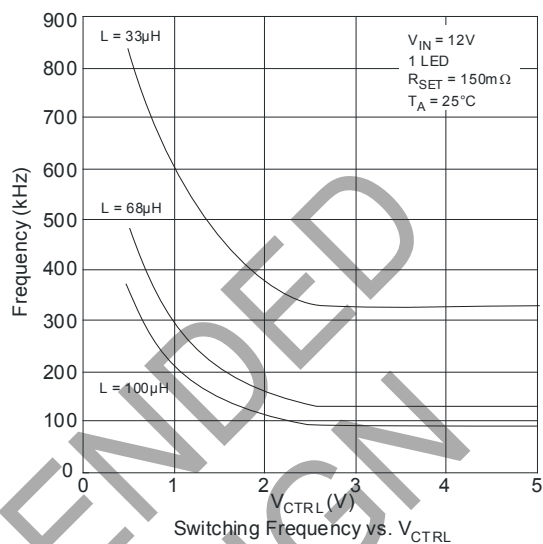
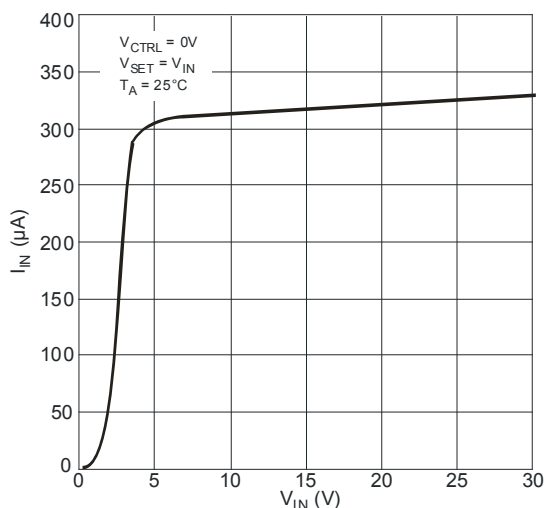
Symbol	Parameter	Min	Max	Unit
V <sub>IN</sub>	Operating Input Voltage relative to GND	6.0	36	V
V <sub>CTRLH</sub>	Voltage High for PWM dimming relative to GND	2.6	5.5	V
V <sub>CTRLDC</sub>	Voltage range for 20% to 100% DC dimming relative to GND	0.5	2.5	V
V <sub>CTRLLL</sub>	Voltage Low for PWM dimming relative to GND	0	0.4	V
I <sub>SW</sub>	DC or RMS switch current		1.5	A
f <sub>OSC</sub>	Switching Frequency		1	MHz
T <sub>J</sub>	Junction Temperature Range	-40	+125	°C

**Electrical Characteristics** (@V<sub>IN</sub> = 12V, T<sub>A</sub> = +25°C, unless otherwise specified.)

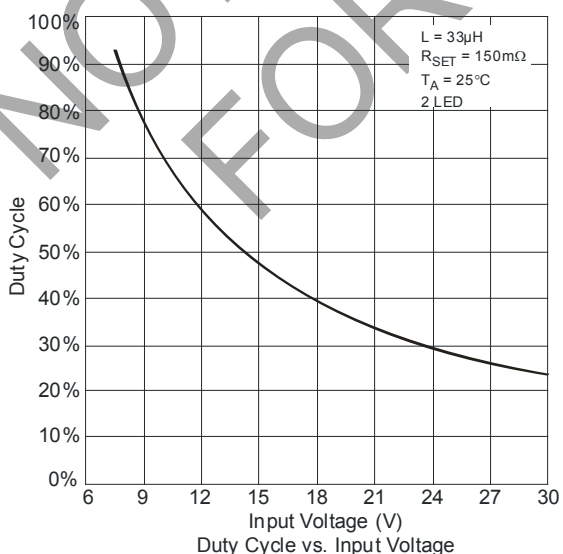
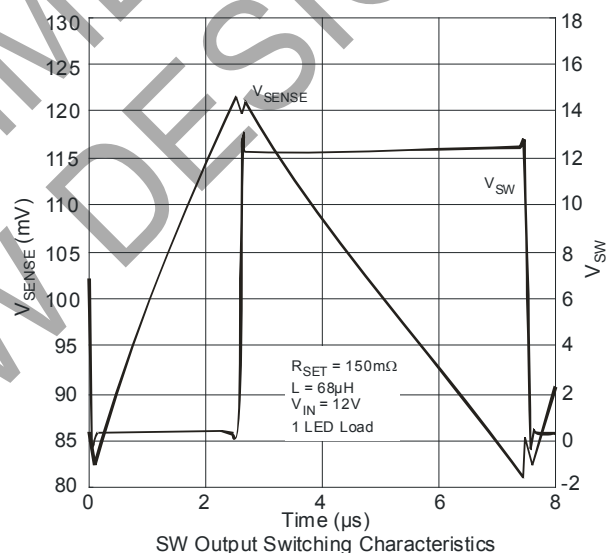
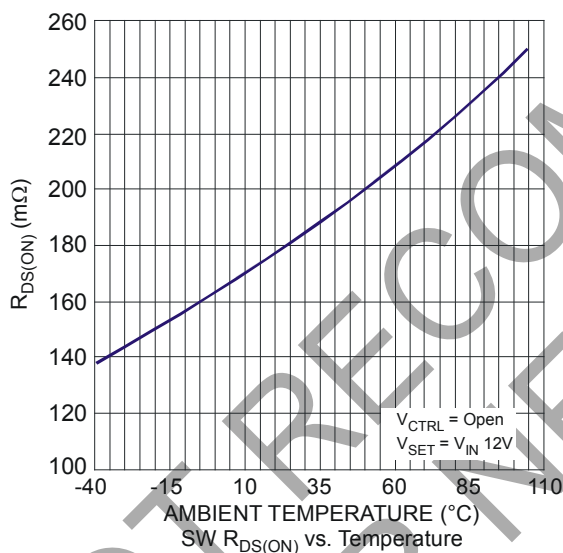
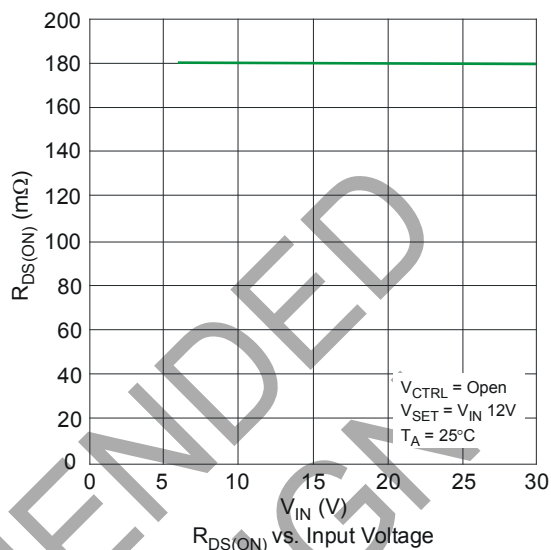
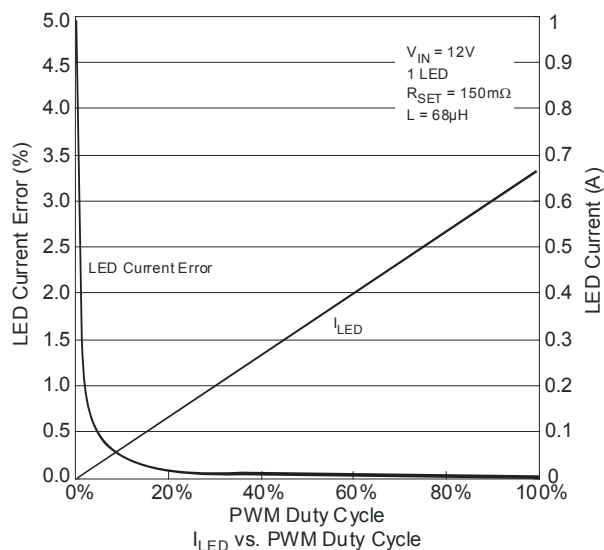
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>INSU</sub>	Internal regulator start up threshold	V <sub>IN</sub> rising			5.9	V
V <sub>INSH</sub>	Internal regulator hysteresis threshold	V <sub>IN</sub> falling	100		300	mV
I <sub>Q</sub>	Quiescent current	Output not switching (Note 4)			350	μA
I <sub>S</sub>	Input supply Current	CTRL pin floating f = 250kHz		1.8	5	mA
V <sub>TH</sub>	Set current Threshold Voltage	V <sub>CTRL</sub> ≥ 2.6V or floating.	95	100	105	mV
V <sub>TH-H</sub>	Set threshold hysteresis			±20		mV
I <sub>SET</sub>	SET pin input current	V <sub>SET</sub> = V <sub>IN</sub> -0.1		16	22	μA
R <sub>CTRL</sub>	CTRL pin input resistance	Referred to internal reference		50		kΩ
V <sub>REF</sub>	Internal Reference Voltage			2.5		V
R <sub>DS(on)</sub>	On Resistance of SW MOSFET	I <sub>SW</sub> = 1A		0.18	0.35	Ω
I <sub>SW_Leakage</sub>	Switch leakage current	V <sub>IN</sub> = 36V			0.5	μA
θ <sub>JA</sub>	Thermal Resistance Junction-to-Ambient (Note 5)	MSOP-8EP (Note 6)		69		°C/W
θ <sub>JC</sub>	Thermal Resistance Junction-to-Case (Note 5)	MSOP-8EP (Note 6)		4.3		

- Notes:
- AL8806 does not have a low power standby mode but current consumption is reduced when output switch is inhibited: V<sub>SENSE</sub> = 0V. Parameter is tested with V<sub>CTRL</sub> ≤ 2.5V
  - Dominant conduction path via exposed pad. Refer to figure 5 for the device derating curve.
  - Measured on an FR4 51x51mm PCB with 2oz copper standing in still air with minimum recommended pad layout on top layer and thermal vias to bottom layer maximum area ground plane. For better thermal performance, larger copper pad for heat-sink is needed.

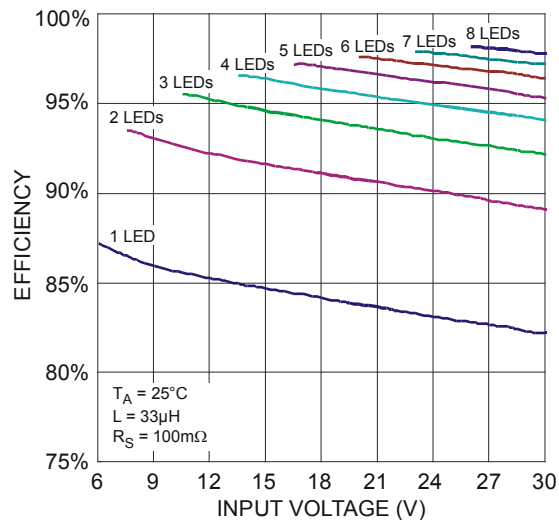
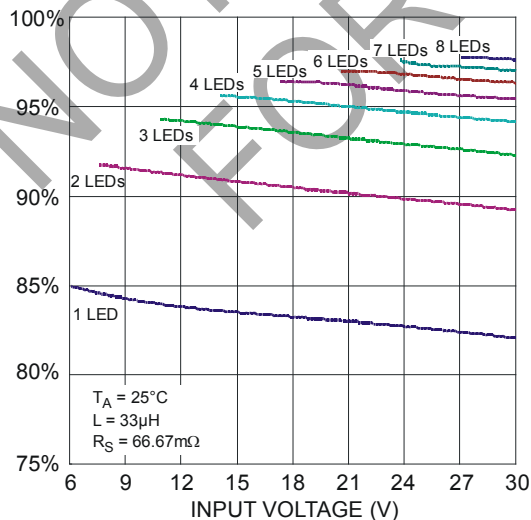
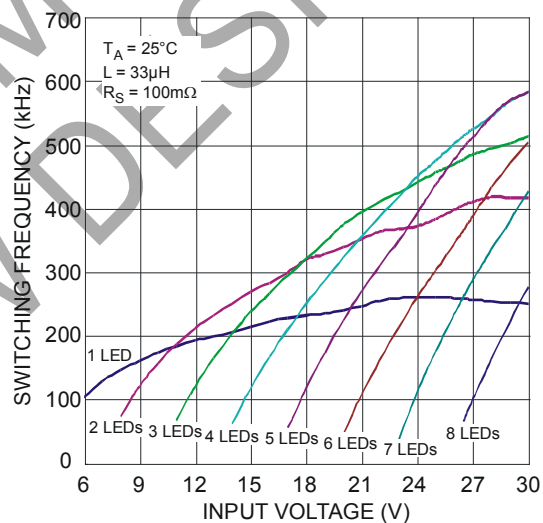
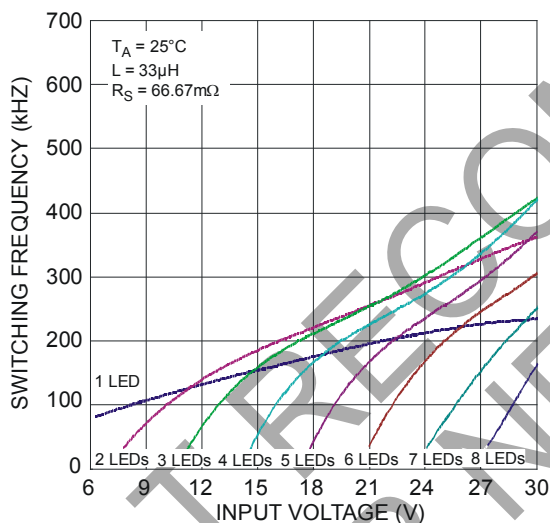
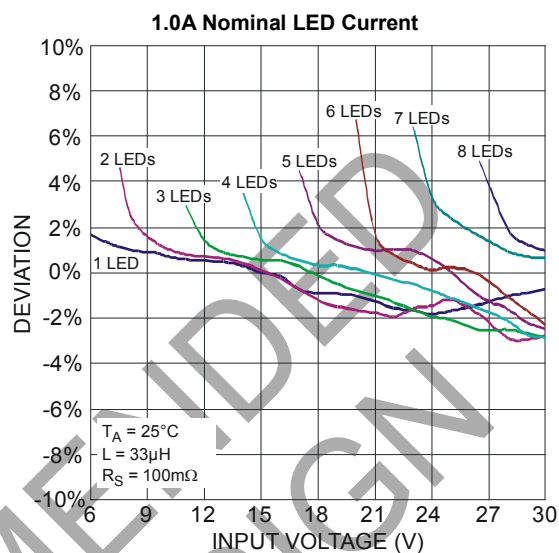
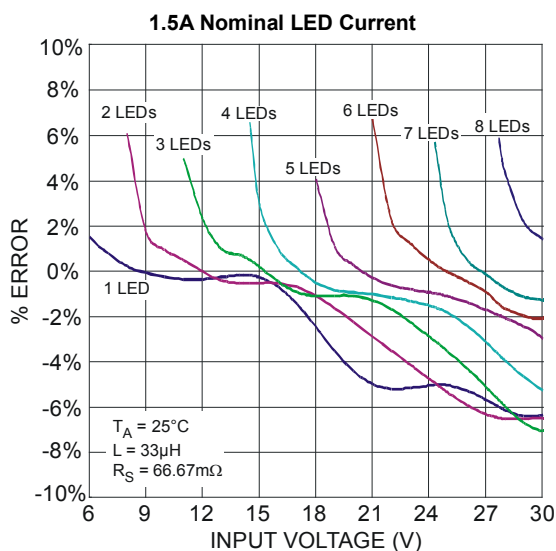
## Typical Performance Characteristics



# Typical Performance Characteristics (cont.)



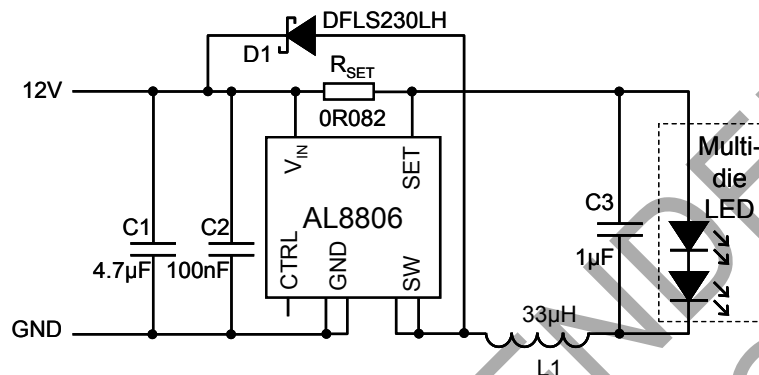
# Typical Operating Performance Characteristics (cont.)



## Application Information

### AL8806 Operation

In normal operation, when voltage is applied at  $V_{IN}$ , the AL8806 internal switch is turned on. Current starts to flow through sense resistor  $R_{SET}$ , inductor  $L1$ , and the LEDs. The current ramps up linearly, and the ramp rate is determined by the input voltage  $V_{IN}$  and the inductor  $L1$ .



**Figure 1 Typical Application Circuit**

This rising current produces a voltage ramp across  $R_{SET}$ . The internal circuit of the AL8806 senses the voltage across  $R_{SET}$  and applies a proportional voltage to the input of the internal comparator.

When this voltage reaches an internally set upper threshold, the internal switch is turned off. The inductor current continues to flow through  $R_{SET}$ ,  $L1$ , the LEDs and the schottky diode  $D1$ , and back to the supply rail, but it decays, with the rate of decay determined by the forward voltage drop of the LEDs and the schottky diode.

This decaying current produces a falling voltage at  $R_1$ , which is sensed by the AL8806. A voltage proportional to the sense voltage across  $R_{SET}$  is applied at the input of the internal comparator. When this voltage falls to the internally set lower threshold, the internal switch is turned on again.

This switch-on-and-off cycle continues to provide the average LED current set by the sense resistor  $R_{SET}$ .

### LED Current Control

The LED current is controlled by the resistor  $R_{SET}$  in Figure 1.

Connected between  $V_{IN}$  and SET the nominal average output current in the LED(s) is defined as:

$$I_{LED} = \frac{V_{THD}}{R_{SET}}$$

If the CTRL pin is driven by an external voltage (higher than 0.4V and lower than 2.5V), the average LED current is:

$$I_{LED} = \frac{V_{CTRL}}{V_{REF}} \frac{V_{THD}}{R_{SET}}$$

For example for a desired LED current of 1.33A and a default voltage  $V_{CTRL}=2.5V$  the resulting resistor is:

$$R_{SET} = \frac{V_{THD}}{I_{LED}} \frac{V_{CTRL}}{V_{REF}} = \frac{0.1}{1.33} \frac{2.5}{2.5} \approx 75m\Omega$$

### DC Dimming

The CTRL pin can be driven by an external DC voltage ( $V_{CTRL}$ ), to adjust the output current to a value below the nominal average value defined by  $R_{SET}$ . The LED current decreases linearly with the CTRL voltage when  $0.5V \leq V_{CTRL} \leq 2.5V$ , as shown on page 4 for 4 different current levels.

Note that 100% brightness setting corresponds to  $V_{CTRL} = V_{REF}$ , nominally 2.5V. For any voltage applied on the CTRL pin that is higher than  $V_{REF}$ , the device will not overdrive the LED current and will still set the current according to the equation  $V_{CTRL} = V_{REF}$ .

When the CTRL voltage falls below the threshold, 0.4V, the output switch is turned off which allows PWM dimming.

## Application Information (cont.)

### PWM Dimming

LED current can be adjusted digitally, by applying a low frequency Pulse Width Modulated (PWM) logic signal to the CTRL pin to turn the device on and off. This will produce an average output current proportional to the duty cycle of the control signal. In particular, a PWM signal with a max resolution of 10bit can be applied to the CTRL pin to change the output current to a value below the nominal average value set by resistor  $R_{SET}$ . To achieve this resolution the PWM frequency has to be lower than 500Hz, however higher dimming frequencies can be used - at the expense of dimming dynamic range and accuracy.

Typically, for a PWM frequency of 500Hz the accuracy is better than 1% for PWM ranging from 1% to 100%.

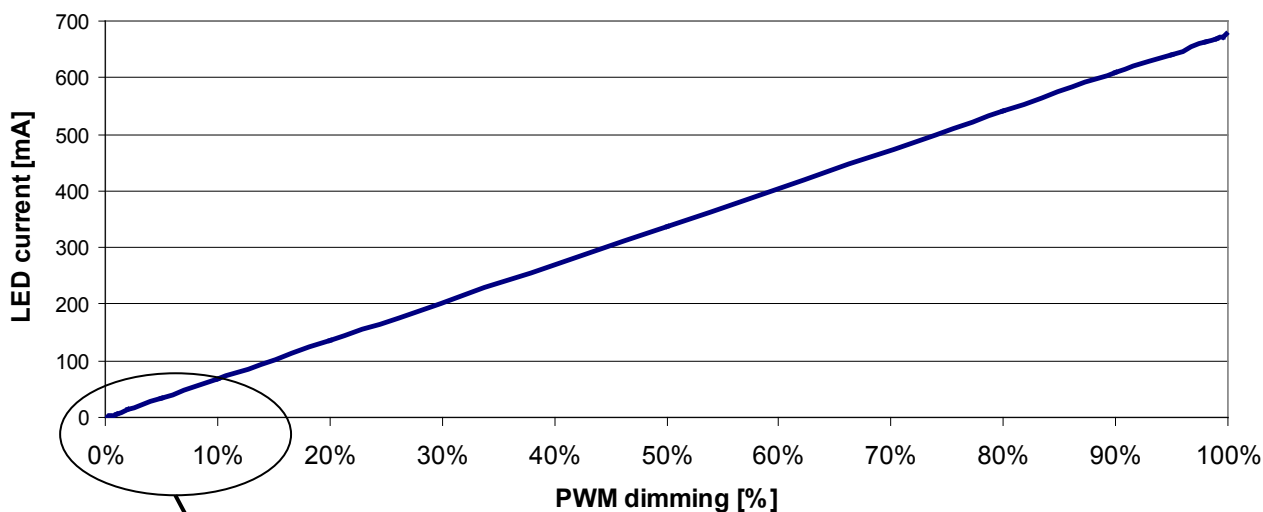


Figure 2 PWM Dimming at 500Hz

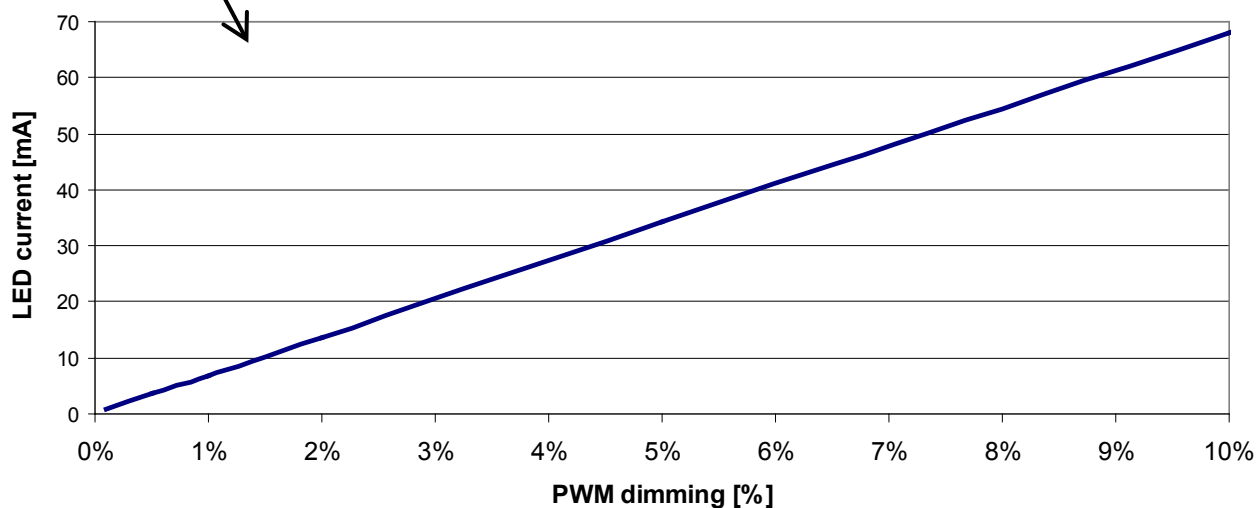


Figure 3 Low Duty Cycle PWM Dimming at 500Hz

The PWM pin is designed to be driven by both 3.3V and 5V logic levels directly from a logic output with either an open drain output or push-pull output stage.



## Application Information (cont.)

### Soft Start

The AL8806 does not have in-built soft-start action – this provides very fast turn off of the output the stage improving PWM dimming accuracy; nonetheless, adding an external capacitor from the CTRL pin to ground will provide a soft-start delay. This is achieved by increasing the time taken for the CTRL voltage to rise to the turn-on threshold and by slowing down the rate of rise of the control voltage at the input of the comparator. Adding a capacitor increases the time taken for the output to reach 90% of its final value, this delay is 0.1ms/nF, but will impact on the PWM dimming accuracy depending on the delay introduced.

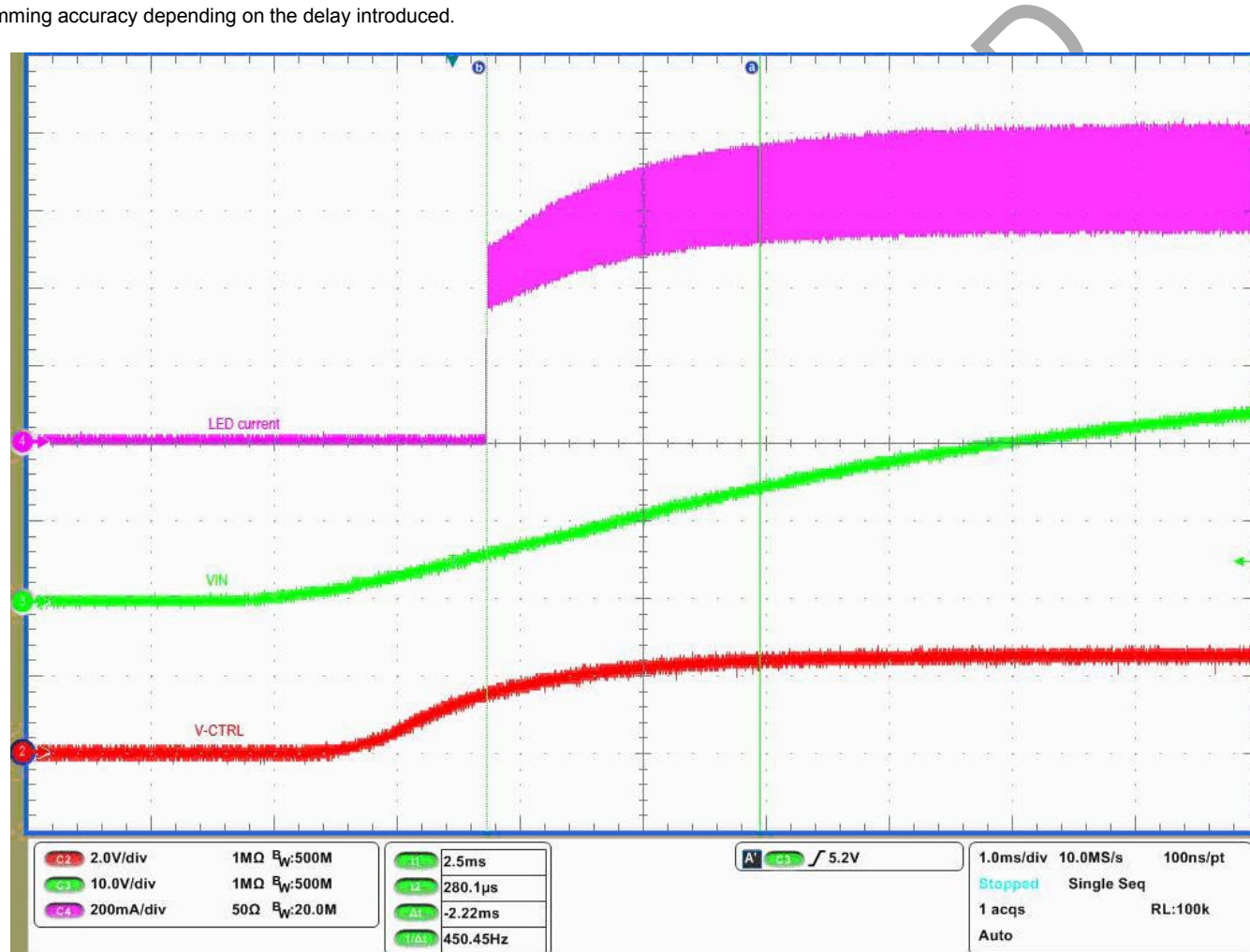


Figure 4 Soft Start with 22nF Capacitor on CTRL Pin ( $V_{IN} = 36V$ ,  $I_{LED} = 667mA$ , 1 LED)

### Reducing Output Ripple

Peak to peak ripple current in the LED(s) can be reduced, if required, by shunting a capacitor C2 across the LED(s) as shown already in the circuit schematic.

A value of 1 $\mu$ F will reduce the supply ripple current by a factor three (approx.). Proportionally lower ripple can be achieved with higher capacitor values. Note that the capacitor will not affect operating frequency or efficiency, but it will increase start-up delay, by reducing the rate of rise of LED voltage. By adding this capacitor the current waveform through the LED(s) changes from a triangular ramp to a more sinusoidal version without altering the mean current value.



## Application Information (cont.)

### Capacitor Selection

The small size of ceramic capacitors makes them ideal for AL8806 applications. X5R and X7R types are recommended because they retain their capacitance over wider voltage and temperature ranges than other types such as Z5U.

A 2.2μF input capacitor is sufficient for most intended applications of AL8806; however a 4.7μF input capacitor is suggested for input voltages approaching 30V.

### Diode Selection

For maximum efficiency and performance, the rectifier (D1) should be a fast low capacitance Schottky diode with low reverse leakage at the maximum operating voltage and temperature. The Schottky diode also provides better efficiency than silicon PN diodes, due to a combination of lower forward voltage and reduced recovery time.

It is important to select parts with a peak current rating above the peak coil current and a continuous current rating higher than the maximum output load current. In particular, it is recommended to have a diode voltage rating at least 15% higher than the operating voltage to ensure safe operation during the switching and a current rating at least 10% higher than the average diode current. The power rating is verified by calculating the power loss through the diode.

Schottky diodes, e.g. B240 or B140, with their low forward voltage drop and fast reverse recovery, are the ideal choice for AL8806 applications.

### Thermal and Layout Considerations

For continuous conduction mode of operation, the absolute maximum junction temperature must not be exceeded. The maximum power dissipation depends on several factors: the thermal resistance of the IC package  $\theta_{JA}$ , PCB layout, airflow surrounding the IC, and difference between junction and ambient temperature.

The maximum power dissipation can be calculated using the following formula:

where  $T_{J(MAX)}$  is the maximum operating junction temperature,  
 $T_A$  is the ambient temperature, and  
 $\theta_{JA}$  is the junction to ambient thermal resistance.

The recommended maximum operating junction temperature,  $T_J$ , is 125°C and so maximum ambient temperature is determined by the AL8806's junction to ambient thermal resistance,  $\theta_{JA}$ . To support high LED drive at higher ambient temperatures the AL8806 has been packaged in thermally enhanced MSOP-8EP package.

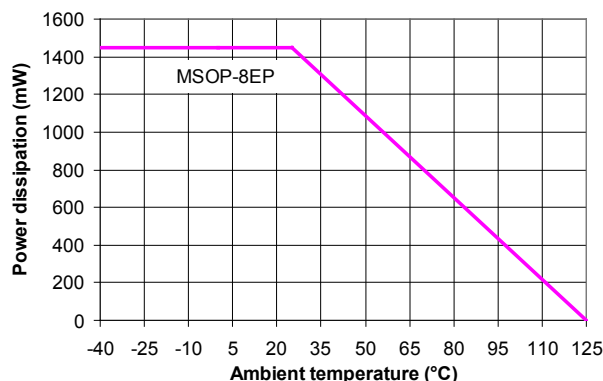
$\theta_{JA}$ , is layout dependent and the AL8806's  $\theta_{JA}$  in MSOP-8EP on a 51 x 51mm double layer PCB with 2oz copper standing in still air is approximately 69°C/W.

Therefore the maximum power dissipation at  $T_A = +25^\circ\text{C}$  is:

$$P_{D(MAX)} = \frac{(125^\circ\text{C} - 25^\circ\text{C})}{69^\circ\text{C/W}} = 1.45\text{W}$$

Figure 5, shows the power derating of the AL8806 on an FR4 51x51mm PCB with 2oz copper standing in still air.

As the ambient temperature increases and/or the PCB area reduces the maximum allowable power dissipated by the AL8806 will decrease.



**Figure 5 Derating Curve**

Downloaded from [Arrow.com](http://Arrow.com).

## Application Information (cont.)

### Application Example (cont.)

The output of the 12V transformer is fed into the bridge rectifier, comprising of D2, D3, D4 and D5. C10 offers an optional EMI filtering at the input. (see figure 9)

Capacitor,  $C_{BULK}$ , (capacitors C1 and C2 on the PCB and BOM are multiple components to reduce size) forms the bulk reservoir capacitance - used to sustain operation of the device during the low part of the rectified AC wave. In the case of a system driving two LEDs at 1.1A, one 470 $\mu$ F plus 47 $\mu$ F capacitors are required.

Capacitors C3 and C9 provide local decoupling for the AL8806 and for the schottky freewheeling diode, D1. It is important that C3 is as close as possible to the AL8806's  $V_{IN}$  pin and that C9 is as close to the  $V_{IN}$  and schottky diode as possible, as reflected in the layout shown below in figure 8.

The LED current is determined by the total resistance between the  $V_{IN}$  and SET pins (R1 and R2 in figures 7 (schematic) and 8 (top PCB) and BOM).

C4 decouples the LED connections, again to minimize EMI, as well as smoothing the current.

D1 operates as the freewheeling diode, providing a current path for the LED current when the power switch at SW pin is off.

Capacitors C5 to C9, 1.5nF have been added to improve the overall EMI performance. C5 is connected across the Supply pins to reduce radiation via the wiring. C6 and C7 also assist the reduction of EMI via the wiring. C8 snubs high frequency radiation from the harmonics in the switching regulator.

A large number of vias have been used to improve the grounding effect throughout the PCB to improve the evaluation board's EMI performance.



Figure 8 AL8806EV6 evaluation board (Top)

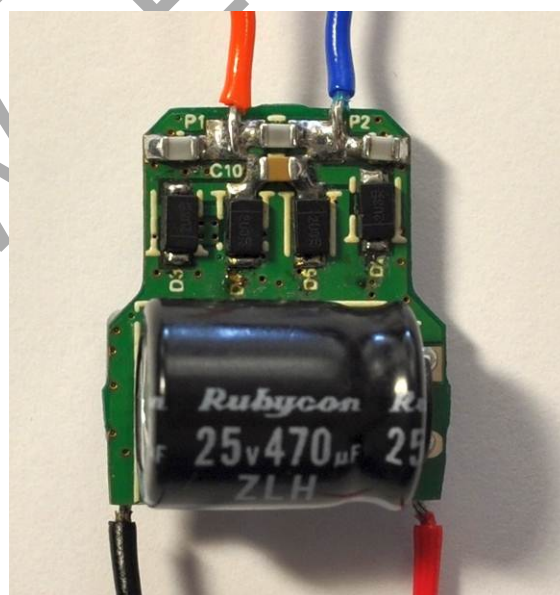


Figure 9 AL8806EV6 evaluation board (Bottom)

## Application Information (cont.)

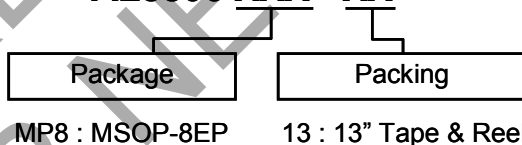
### AL8806EV6 BOM

Count	Designator	Description	Package	Manufacturer	Part Number
1	C1	Capacitor, 470uF, 25V Aluminium Electrolytic	Radial, 10 x 12.5mm	Rubycon	25ZLH470M
1	C2	Capacitor, 4.7µF 10% 50V X7R	1210	Kemet	C1210X475K5RAC
3	C3, C9, C10	Capacitor, 100nF 10% 50V X7R	0805	Kemet NIC	C1210X475K5RAC NMC0805X7R104K50TRPF
1	C4	Capacitor, 100nF 10% 50V X7R	1206	Kemet NIC	C1206104K5RAC7800 NMC1206X7R104K50TRPF
4	C5 to C8	Capacitor, 1.5nF 10% 50V or 100V, COG	0805	generic	
1	L1	Inductor, shielded, 15µH 20%	6.6 x 6.6mm	Coilcraft NIC Würth	MSS7341-153ML NPIS64D150MTRF 744777115
1	L2	Inductor, ferrite bead, 6A, 30ohms @100MHz	0805	TDK	MPZ2012S300A
1	R1	Resistor, 0.15ohms 1% 250ppm 125mW	0805	generic	
1	R2	Resistor, 0.2ohms 1% 250ppm 125mW	0805	generic	
1	D1	Diode, schottky, 36V 2A DFSL230 (alternatives: DFSL230L or LH)	PowerDi123	Diodes Inc	DFSL230-7
4	D2 to D5	Diode, Super Barrier Rectifier, 36V 2A SBR2U30	PowerDi123	Diodes Inc	SBR2U30P1-7
1	U1	IC, LED Driver, AL8806	MSOP-8EP	Diodes Inc	AL8806MP8-13

The AL8806 guarantees high level of performance both with 12V<sub>AC</sub> and 12V<sub>DC</sub> power supply.

## Ordering Information

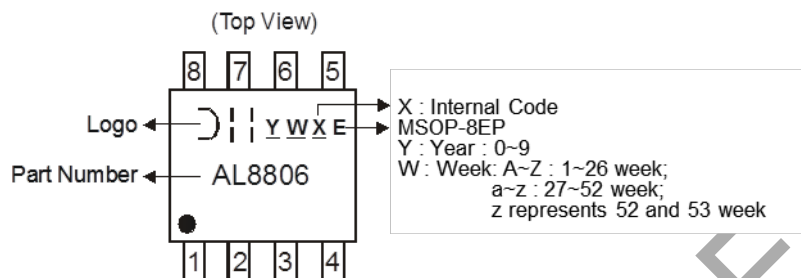
### AL8806 XXX - XX



Part Number	Package Code	Packaging	Tape and Reel	
			Quantity	Part Number Suffix
AL8806MP8-13	MP8	MSOP-8EP	2500/Tape & Reel	-13

## Marking Information

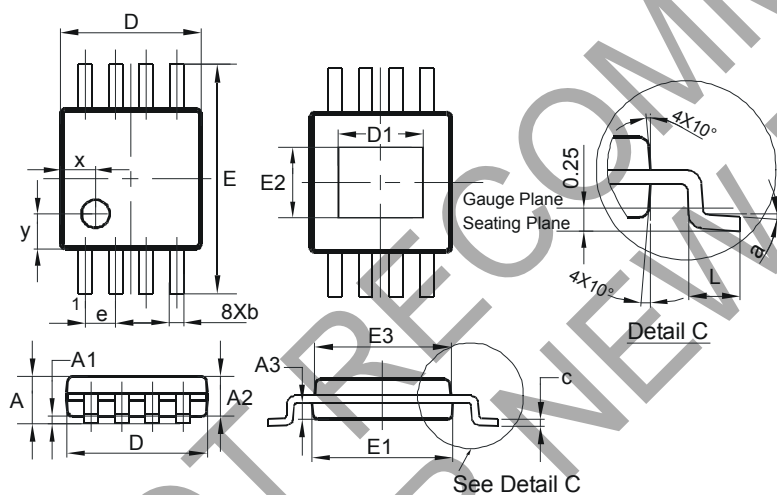
### MSOP-8EP



Part Number	Package
AL8806MP8-13	MSOP-8EP

## Package Outline Dimensions (All dimensions in mm.)

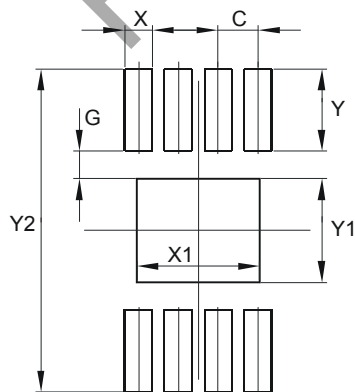
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



MSOP-8EP			
Dim	Min	Max	Typ
A	-	1.10	-
A1	0.05	0.15	0.10
A2	0.75	0.95	0.86
A3	0.29	0.49	0.39
b	0.22	0.38	0.30
c	0.08	0.23	0.15
D	2.90	3.10	3.00
D1	1.60	2.00	1.80
E	4.70	5.10	4.90
E1	2.90	3.10	3.00
E2	1.30	1.70	1.50
E3	2.85	3.05	2.95
e	-	-	0.65
L	0.40	0.80	0.60
a	0°	8°	4°
x	-	-	0.750
y	-	-	0.750
All Dimensions in mm			

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.650
G	0.450
X	0.450
X1	2.000
Y	1.350
Y1	1.700
Y2	5.300

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