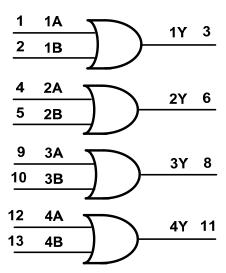


### **Pin Descriptions**

Pin Number	Pin Name	Description
1	1A	Data Input
2	1B	Data Input
3	1Y	Data Output
4	2A	Data Input
5	2B	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	3B	Data Input
11	4Y	Data Output
12	4A	Data Input
13	4B	Data Input
14	Vcc	Supply Voltage

## **Logic Diagram**



## **Function Table**

Inputs		Output
Α	В	Υ
L	L	L
Н	X	Н
X	Н	Н

### Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
$V_{CC}$	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 4)	-0.5 to +7.0	V
lık	Input Clamp Current V <sub>1</sub> < 0V	-20	mA
lok	Output Clamp Current V <sub>0</sub> < -0V	-50	mA
Io	Continuous Output Current -0.5V < V <sub>O</sub> V <sub>CC</sub> +0.5V	±25	mA
Icc	Continuous Current Through V <sub>cc</sub>	50	mA
I <sub>GND</sub>	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
P <sub>TOT</sub>	Total Power Dissipation	500	mW

Note:

<sup>4.</sup> Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



# Recommended Operating Conditions (Note 5) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
Vcc	Supply Voltage	_	2.0	5.5	V
VI	Input Voltage	_	0	5.5	V
Vo	Output Voltage	_	0	V <sub>CC</sub>	V
		2.0V	_	-50	mA
	Lligh Loyal Output Current	2.3V to 2.7V	_	-2	μA
Іон	High-Level Output Current	3.0V to 3.6V	_	-6	mA
		4.5V to 5.5V	_	-12	mA
		2.0V	_	50	μA
	Law Lavel Output Current	2.3V to 2.7V	_	2	mA
loL	Low-Level Output Current	3.0V to 3.6V	_	6	mA
		4.5V to 5.5V	_	12	mA
	land Transition Discost Fall	2.3V to 2.7V	_	200	
Δt/ΔV	Input Transition Rise or Fall Rate	3.0V to 3.6V	_	100	ns/V
	Nate	4.5V to 5.5V		20	
T <sub>A</sub>	Operating Free-Air Temperature	_	-40	+125	°C

Note: 5. Unused inputs should be held at Vcc or Ground.

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

				T. = -40°C	C to +85°C	T. = -40°C	to +125°C	
Symbol	Parameter	Test Conditions	V <sub>CC</sub>	Min	Max	Min	Max	Unit
		_	2.0V	1.5	_	1.5	_	
	High-Level Input	_	2.3V to 2.7V	V <sub>CC</sub> X 0.7	_	V <sub>CC</sub> X 0.7	_	V
ViH	Voltage	_	3.0V to 3.6V	V <sub>CC</sub> X 0.7	_	V <sub>CC</sub> X 0.7	_	
		_	4.5V to 5.5V	V <sub>CC</sub> X 0.7	_	V <sub>CC</sub> X 0.7	_	
		_	2.0V	_	0.5	_	0.5	
.,	Low-Level Input	_	2.3V to 2.7V	_	V <sub>CC</sub> X 0.3	_	V <sub>CC</sub> X 0.3	V
V <sub>IL</sub>	Voltage	_	3.0V to 3.6V	_	V <sub>CC</sub> X 0.3	_	V <sub>CC</sub> X 0.3	
		_	4.5V to 5.5V	_	V <sub>CC</sub> X 0.3	_	V <sub>CC</sub> X 0.3	
		I <sub>OH</sub> = -50μA	2.0V to 5.5V	V <sub>CC</sub> -0.1	_	V <sub>CC</sub> -0.1	_	
.,	High-Level	I <sub>OH</sub> = -2mA	2.3V	2.0	_	2.0	_	V
V <sub>OH</sub>	Output Voltage	I <sub>OH</sub> = -6mA	3.0V	2.48	_	2.48	_	V
		I <sub>OH</sub> = -12mA	4.5V	3.8	_	3.8	_	
		I <sub>OL</sub> = 50μA	2.0V to 5.5V	_	0.1	_	0.1	
.,	Low-Level	I <sub>OL</sub> = 2mA	2.3V	_	0.4	_	0.4	V
V <sub>OL</sub>	Output Voltage	I <sub>OL</sub> = 6mA	3.0V	_	0.44	_	0.44	V
		I <sub>OL</sub> = 12mA	4.5V	_	0.55	_	0.55	
I <sub>OFF</sub>	Power Down Leakage Current	$V_{I}$ or $V_{O} = 0$ to 5.5V	0V	_	5		5	μΑ
Iı	Input Current	V <sub>I</sub> = GND or 5.5V	0 to 5.5V	_	±1	_	±1	μΑ
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}$ $I_O = 0$	5.5V	_	20	_	20	μΑ



## **Switching Characteristics**

Symbol Parameter		Test	T <sub>A</sub> = +25°C		-40°C to +85°C		-40°C to +125°C		Unit		
Symbol	Farameter	Conditions	V <sub>cc</sub>	Min	Тур	Max	Min	Max	Min	Max	Oilit
		Figure 1	2.5V ± 0.2V	_	7.1	12.8	1	15	1	16	
		Figure 1 $C_L = 15pF$	$3.3V \pm 0.3V$	_	5	7.9	1	9.5	1	9.5	ns
	Propagation	CL = 15pF	5.0V ± 0.5V	_	3.6	5.5	1	6.5	1	6.5	
t <sub>PD</sub>	Delay A <sub>N</sub> to Y <sub>N</sub>	Figure 1	2.5V ± 0.2V	_	9.6	16.2	1	19	1	20	
		Figure 1  C <sub>L</sub> = 50pF	$3.3V \pm 0.3V$	_	6.9	11.4	1	13	1	13	ns
			5.0V ± 0.5V	_	4.9	7.5	1	8.5	1	8.5	

# **Operating Characteristics**

T<sub>A</sub> = +25°C

	Parameter	Test Conditions	V <sub>cc</sub>	Тур	Unit
0	Power Dissipation	f = 10MHz	3.3V	9.5	pF
$C_{pd}$	Capacitance per Gate	$C_L = 50pF$	5.0V	11.5	PΓ

### **Noise Characteristics**

 $V_{CC}$  = 3V,  $C_L$  = 50pF,  $T_A$  = +25°C

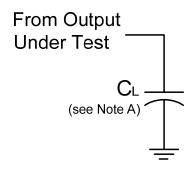
Symbol	Parameter	Min	Тур	Max	Unit
$V_{OL(p)}$	Quiet output, maximum dynamic V <sub>OL</sub>	_	0.2	0.8	V
$V_{OL(V)}$	Quiet output, minimum dynamic V <sub>OL</sub>	-	-0.1	-0.8	V
$V_{OH(V)}$	Quiet output, minimum dynamic V <sub>OH</sub>	_	3.1	-	V
$V_{IH(D)}$	High Level dynamic input voltage	2.31	-	-	V
$V_{IL(D)}$	Low Level dynamic input voltage	_	-	0.99	V

## **Package Characteristics**

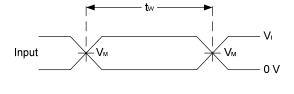
Symbol	Parameter	Test Conditions	V <sub>cc</sub>	Min	Тур	Max	Unit
C <sub>i</sub>	Input Capacitance	$V_i = V_{CC} - \text{or GND}$	2.0 to 5.5V	_	3.3	10	pF



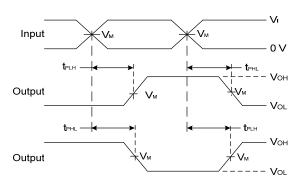
### **Parameter Measurement Information**



V	Inputs		V	
V <sub>CC</sub>	Vı	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	C <sub>L</sub>
2.0V to 5.5V	V <sub>CC</sub>	<3ns	V <sub>CC</sub> /2	15pF or 50pF



**Voltage Waveform Pulse Duration** 



Voltage Waveform Propagation Delay Times **Inverting and Non Inverting Outputs** 

Notes: A. Includes test lead and test apparatus capacitance.

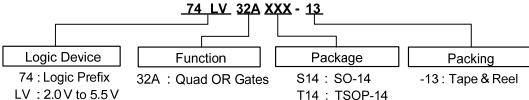
B. All pulses are supplied at pulse repetition rate ≤ 10MHz.

- C. Inputs are measured separately one transition per measurement.
- D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{PD}$ .

Figure 1 Load Circuit and Voltage Waveforms



### **Ordering Information**



Family

T14: TSOP-14

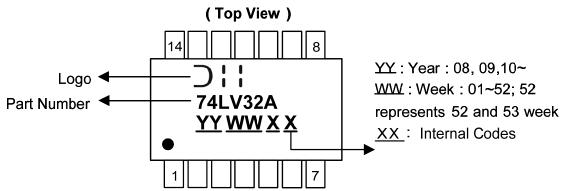
Device	Bookaga Coda	Packaging	13" Tape	and Reel
Device	Package Code	(Note 6)	Quantity	Part Number Suffix
74LV32AS14-13	S14	SO-14	2500/Tape & Reel	-13
74LV32AT14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Note:

6. The taping orientation and tape details can be found at http://www.diodes.com/datasheets/ap02007.pdf

### **Marking Information**

#### (1) SO14, TSSOP14



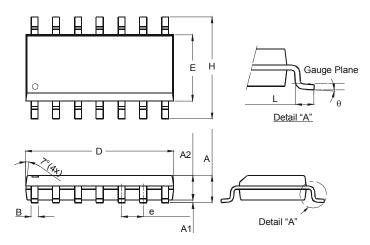
Part Number	Package
74LV32AS14	SO-14
74I \/32AT14	TSSOP-14



### Package Outline Dimensions (All Dimensions in mm)

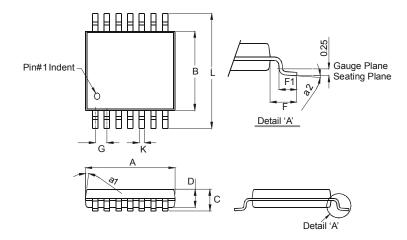
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### Package Type: SO-14



	SO-14	
Dim	Min	Max
Α	1.47	1.73
A1	0.10	0.25
A2	1.45 Typ	
В	0.33	0.51
D	8.53	8.74
Е	3.80	3.99
е	1.27 Typ	
Н	5.80	6.20
Ĺ	0.38	1.27
θ	0°	8°
All Dimensions in mm		

### Package Type: TSSOP-14



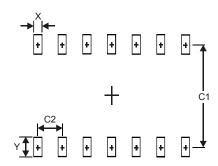
TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
Α	4.9	5.10
В	4.30	4.50
С	_	1.2
D	0.8	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
L	6.40	Тур
All Dimensions in mm		



### **Suggested Pad Layout**

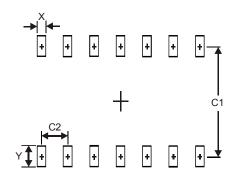
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Υ	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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