

VS-62CTQ030PbF, VS-62CTQ030-N3

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	30 A	T _{.1} = 25 °C	0.46	0.5	V	
		60 A	1j=25 C	0.56	0.6		
		30 A	T 105 00	0.39	0.44		
		60 A	T _J = 125 °C	0.54	0.59		
Maximum instantaneous reverse current	I _{RM}	T _J = 25 °C	Rated DC voltage	0.4	2.5	mA	
		T _J = 125 °C	hated DC voltage	180	350	IIIA	
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		3000		pF	
Typical series inductance	L _S	Measured from top of terminal to mounting plane		8.0		nH	
Maximum voltage rate of change	dV/dt	Rated V _R			10 000		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYME	SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum junction temperature ra	nge T _J			- 65 to 150	°C		
Maximum storage temperature rar	nge T _{Ste}	g		- 65 to 175	.0		
Maximum thermal resistance, junction to case per leg	R _{thJ}	IC	DC operation 1.2		°C/W		
Typical thermal resistance, case to heatsink	R _{thC}	cs	Mounting surface, smooth and greased	0.50			
Accompliance				2	g		
Approximate weight				0.07	OZ.		
	inimum		Non-lubricated threads	6 (5)	kgf · cm		
Mounting torque ma	aximum		Non-lubricated tirreads	12 (10)	$(lbf \cdot in)$		
Marking device			Case style TO-220AB	62CT	Q030		

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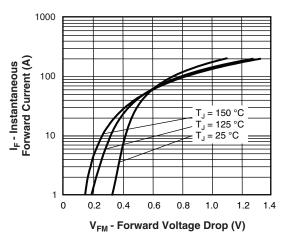


Fig. 1 - Maximum Forward Voltage Drop Characteristics

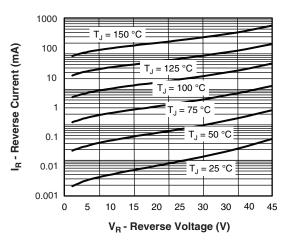


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

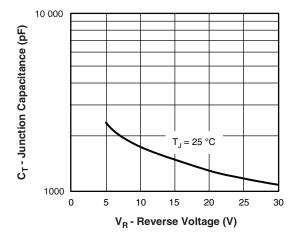


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

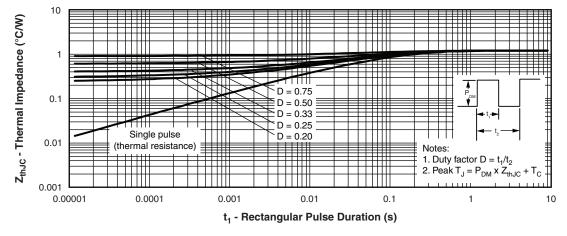


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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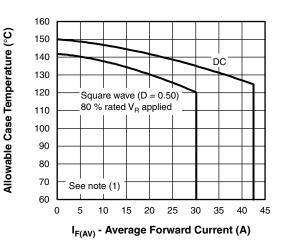


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

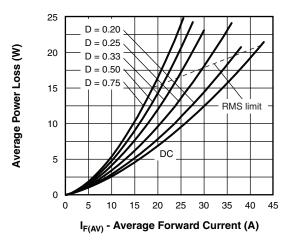


Fig. 6 - Forward Power Loss Characteristics

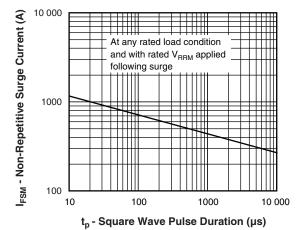


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

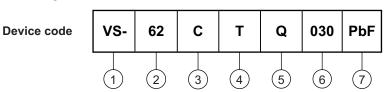
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \text{ (1 - D); } I_R \text{ (2 - D); } I_R \text{ (3 - D); } I_R \text{ (3 - D); } I_R \text{ (4 - D$

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ORDERING INFORMATION TABLE



1 - Vishay Semiconductors product

2 - Current rating (60 = 60 A)

3 - Circuit configuration

C = Common cathode

4 - Package

T = TO-220

5 - Schottky "Q" series

Voltage rating (030 = 30 V)

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-62CTQ030PbF	50	1000	Antistatic plastic tube			
VS-62CTQ030-N3	50	1000	Antistatic plastic tube			

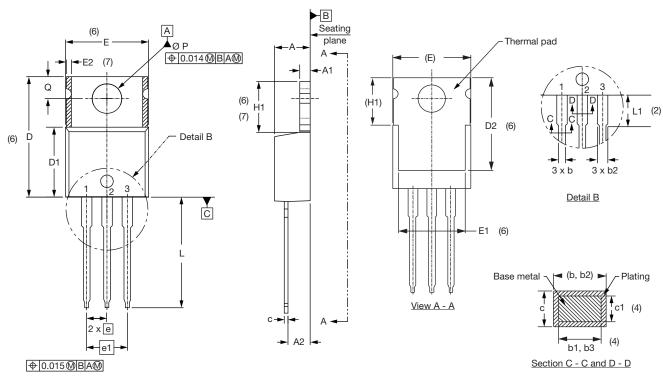
LINKS TO RELATED DOCUMENTS				
Dimensions		www.vishay.com/doc?95222		
Part marking information	TO-220AB PbF	www.vishay.com/doc?95225		
	TO-220AB -N3	www.vishay.com/doc?95028		
SPICE model		www.vishay.com/doc?95185		



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TO-220AB

DIMENSIONS in millimeters and inches



Lead assignments

<u>Diodes</u>

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIMETERS		INCHES		NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIMETERS		INC	NOTES	
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° to 93°		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip

Legal Disclaimer Notice



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