30BQ040PbF

Vishay High Power Products Schottky Rectifier, 3.0 A



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	3 A	T _J = 25 °C	0.53	V		
		6 A		0.68			
		3 A	T _J = 125 °C	0.43			
		6 A		0.57			
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.5	- mA		
		T _J = 125 °C		30			
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		230	pF		
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		3.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C		
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	12	°C/W		
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46			
Approximate weight			0.24	g		
			0.008	OZ.		
Marking device		Case style SMC (similar to DO-214AB)	V3F			

Notes

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⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB



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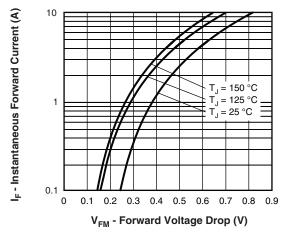


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

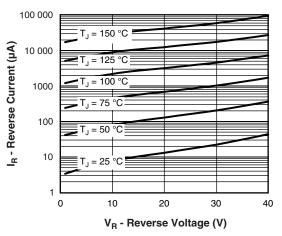


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

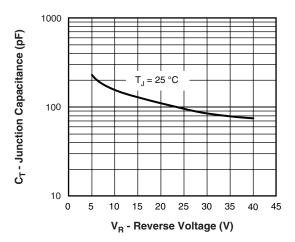


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

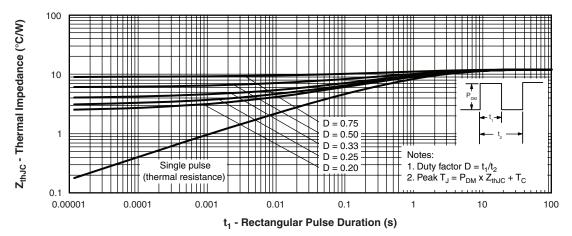


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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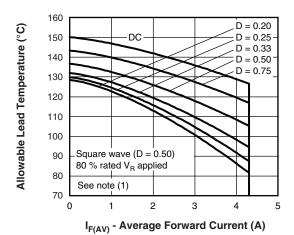


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

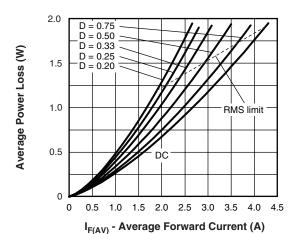


Fig. 6 - Maximum Average Forward Dissipation vs.
Average Forward Current

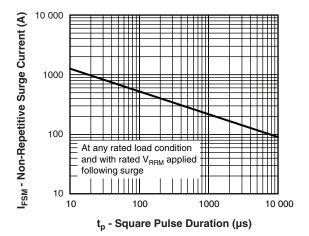


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J \text{-} (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \ x \ I_R \ (1 \text{-} D); \ I_R \ at \ V_{R1} = 80 \ \% \ rated \ V_R \\ \end{array}$

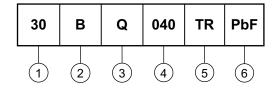
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ORDERING INFORMATION TABLE

Device code



1 - Current rating

2 - B = Single lead diode

3 - Q = Schottky "Q" series

4 - Voltage rating (040 = 40 V)

• None = Box (1000 pieces)

• TR = Tape and reel (3000 pieces)

6 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95023			
Part marking information	http://www.vishay.com/doc?95029			
Packaging information	http://www.vishay.com/doc?95034			
SPICE model	http://www.vishay.com/doc?95324			

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