ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Forward voltage drop See fig. 1	V_{FM} (1)	19 A	T_J = 25 °C	-	0.41	V
		40 A		-	0.52	
		19 A	T_J = 125 °C	0.25	0.33	
		40 A		0.37	0.50	
Reverse leakage current See fig. 2	I_{RM} (1)	T_J = 25 °C	V_R = Rated V_R	-	10	mA
		T_J = 100 °C		-	600	
Threshold voltage	$V_{F(TO)}$	T_J = T_J maximum		0.182		V
Forward slope resistance	r_t			7.6		mΩ
Maximum junction capacitance	C_T	V_R = 5 V_{DC}, (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF
Typical series inductance	L_S	Measured lead to lead 5 mm from package body		8	-	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000		V/µs

Note

(1) Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T_J		- 55 to 125	°C
Maximum storage temperature range	T_{Stg}		- 55 to 150	
Maximum thermal resistance, junction to case	R_{thJC}	DC operation See fig. 4	1.5	°C/W
Typical thermal resistance, case to heatsink	R_{thCS}	Mounting surface, smooth and greased (For TO-220)	0.50	
Maximum thermal resistance, junction to ambient	R_{thJA}	DC operation	40	
Approximate weight			2	g
			0.07	oz.
Mounting torque minimum		Non-lubricated threads	6 (5)	kgf · cm (lbf · in)
Mounting torque maximum			12 (10)	
Marking device		Case style D^2PAK	20L15TS	

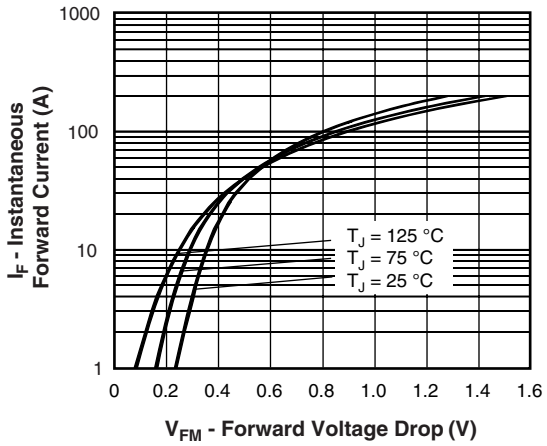

Fig. 1 - Maximum Forward Voltage Drop Characteristics

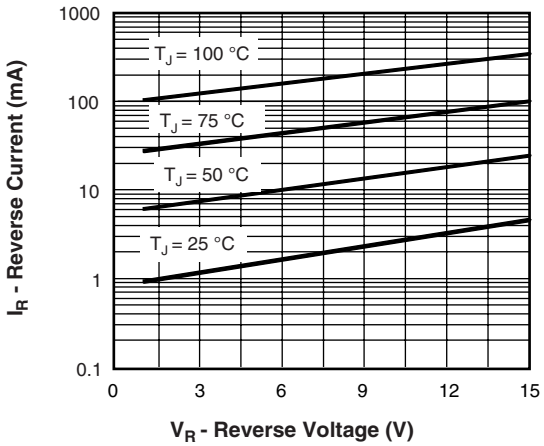

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

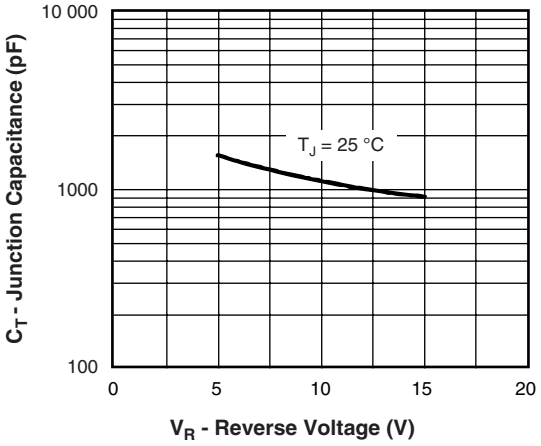

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

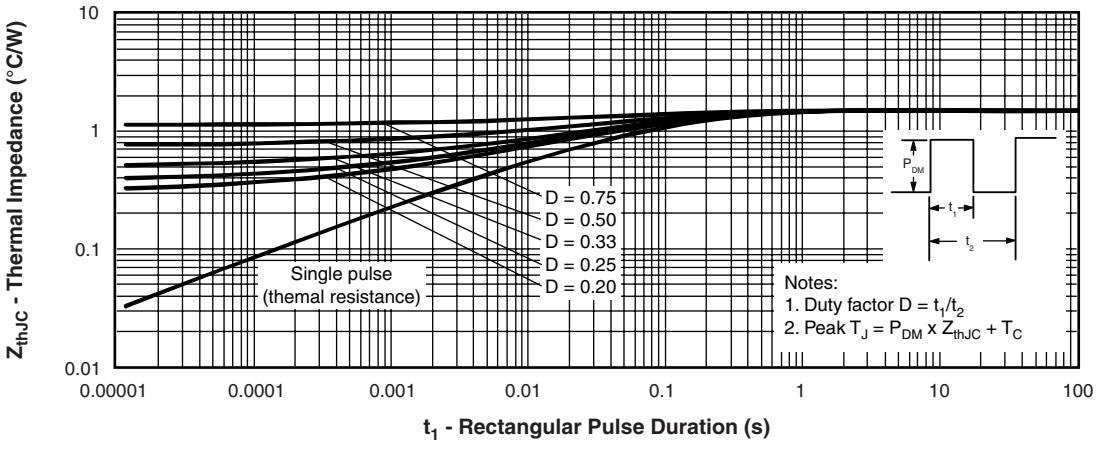

Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

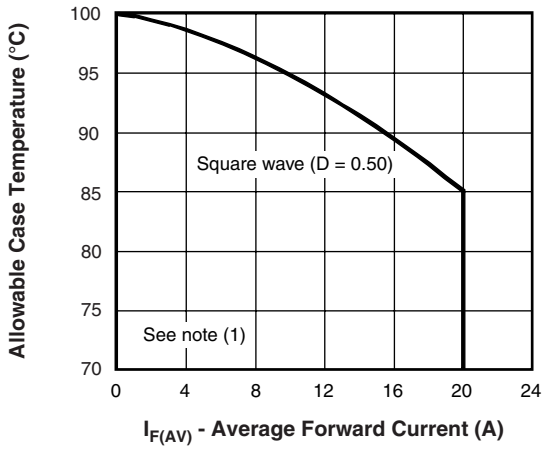

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

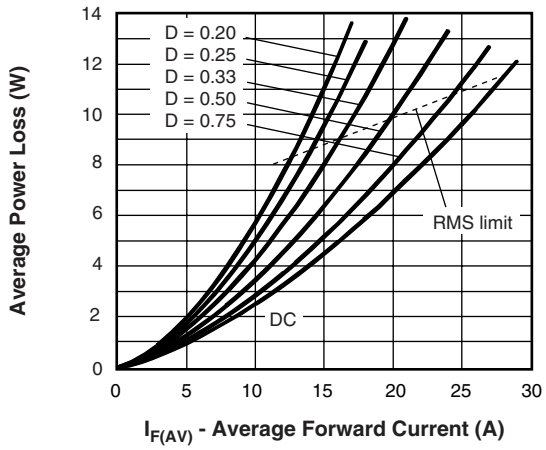

Fig. 6 - Forward Power Loss Characteristics

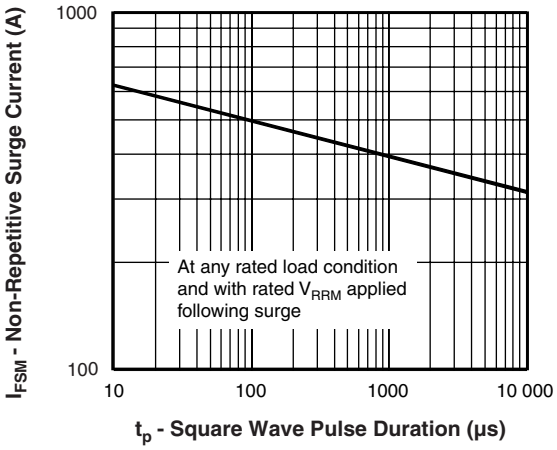

Fig. 7 - Maximum Non-Repetitive Surge Current

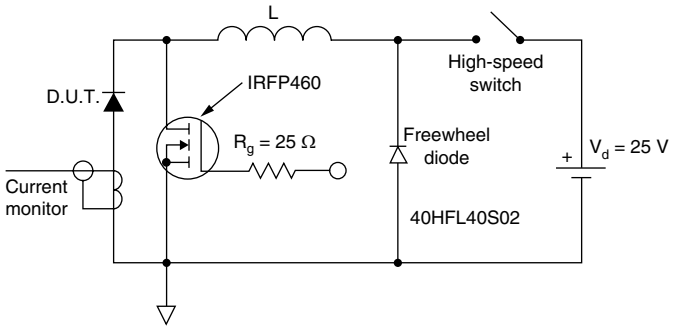

Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}$/D) (see fig. 6);
Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

Device code

20	L	15	T	S	TRL	PbF
1	2	3	4	5	6	7

1 - Current rating (20 A)

2 - L = Low V_F

3 - Voltage rating (15 = 15 V)

4 - T = Schottky series

5 - S = D^2PAK

6 - • None = Tube (50 pieces)
• TRL = Tape and reel (left oriented)
• TRR = Tape and reel (right oriented)

7 - • None = Standard production
• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95014
Part marking information	www.vishay.com/doc?95008
Packaging information	www.vishay.com/doc?95032

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