1 Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings (AMR)

Symbol	Parameter	Value	Unit
I_k	Reverse breakdown current	20	mA
I_f	Forward current	10	mA
P_d	Power dissipation (1) SOT23-3	360	mW
T_{stg}	Storage temperature	-65 to +150	°C
ESD	Human body model (HBM)(2)	2	kV
	Machine model (MM)(3)	200	V
T_{lead}	Lead temperature (soldering, 10 seconds)	260	°C

1. P_d is calculated with T_{amb} = 25°C and T_j = 150°C and R_{thja} = 340°C/W for the SOT23-3L package.
2. Human body model: 100pF discharged through a 1.5kΩ resistor between two pins of the device, done for all couples of pin combinations with other pins floating.
3. Machine model: a 200pF cap is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5Ω), done for all couples of pin combinations with other pins floating.

Table 2. Operating conditions

Symbol	Parameter	Value	Unit
I_{min}	Minimum operating current	65	µA
I_{max}	Maximum operating current	12	mA
T_{oper}	Operating free air temperature range	-40 to +85	°C

2 Electrical characteristics

Table 3. TS4041E (2% precision) T_{amb} = 25°C (1) (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_k	Reverse breakdown voltage	I_k = 100µA	1.200	1.225	1.250	V
	Reverse breakdown voltage tolerance	I_k = 100µA -40°C < T_{amb} < +85°C	-25 -36		+25 +36	mV
$I_{k\text{-}min}$	Minimum operating current	T_{amb} = 25°C		40	65	µA
		-40°C < T_{amb} < +85°C			70	
$\Delta V_{ref}/\Delta T$	Average temperature coefficient	I_k = 100µA			150	ppm/°C
$\Delta V_k/\Delta I_k$	Reverse breakdown voltage change with operating current range	$I_{k\text{-}min}$ < I_k < 1mA -40°C < T_{amb} < +85°C		0.3	2 2.5	mV
		1mA < I_k < 12mA -40°C < T_{amb} < +85°C		2.5	8 10	
R_{ka}	Static impedance	ΔI_k = 45µA to 1mA		0.25	0.5	Ω
K_{vh}	Long term stability	I_k = 100µA, t = 1000hrs		120		ppm
En	Wide band noise	I_k = 100µA, 10Hz < f < 10kHz		200		nV/√Hz

1. Limits are 100% production tested at 25°C. Behavior at the temperature range limits is guaranteed through correlation and by design.

Table 4. TS4041D (1% precision) T_{amb} = 25°C (1) (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_k	Reverse breakdown voltage	I_k = 100µA	1.213	1.225	1.237	V
	Reverse breakdown voltage tolerance	I_k = 100µA -40°C < T_{amb} < +85°C	-12 -25		+12 +25	mV
$I_{k\text{-}min}$	Minimum operating current	T_{amb} = 25°C		40	65	µA
		-40°C < T_{amb} < +85°C			70	
$\Delta V_{ref}/\Delta T$	Average temperature coefficient	I_k = 100µA			150	ppm/°C
$\Delta V_k/\Delta I_k$	Reverse breakdown voltage change with operating current range	$I_{k\text{-}min}$ < I_k < 1mA -40°C < T_{amb} < +85°C		0.3	2 2.5	mV
		1mA < I_k < 12mA -40°C < T_{amb} < +85°C		2.5	8 10	
R_{ka}	Static impedance	ΔI_k = 45µA to 1mA		0.25	0.5	Ω
K_{vh}	Long term stability	I_k = 100µA, t = 1000hrs		120		ppm
En	Wide band noise	I_k = 100µA, 10Hz < f < 10kHz		200		nV/√Hz

1. Limits are 100% production tested at 25°C. Behavior at the temperature range limits is guaranteed through correlation and by design.

Table 5. TS4041C (0.5% precision) (1) T_{amb} = 25°C (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_k	Reverse breakdown voltage	I_k = 100µA	1.219	1.225	1.231	V
	Reverse breakdown voltage tolerance	I_k = 100µA -40°C < T_{amb} < +85°C	-6 -16		+6 +16	mV
$I_{k\text{-}min}$	Minimum operating current	T_{amb} = 25°C		40	60	µA
		-40°C < T_{amb} < +85°C			65	
$\Delta V_{ref}/\Delta T$	Average temperature coefficient	I_k = 100µA			120	ppm/°C
$\Delta V_k/\Delta I_k$	Reverse breakdown voltage change with operating current range	$I_{k\text{-}min}$ < I_k < 1mA -40°C < T_{amb} < +85°C		0.3	1.52	mV
		1mA < I_k < 12mA -40°C < T_{amb} < +85°C		2.5	68	
R_{ka}	Static impedance	ΔI_k = 45µA to 1mA		0.25	0.5	Ω
K_{vh}	Long term stability	I_k = 100µA, t = 1000hrs		120		ppm
En	Wide band noise	I_k = 100µA, 10Hz < f < 10kHz		200		nV/√Hz

1. Limits are 100% production tested at 25°C. Behavior at the temperature range limits is guaranteed through correlation and by design.

Figure 1. Reference voltage versus cathode current

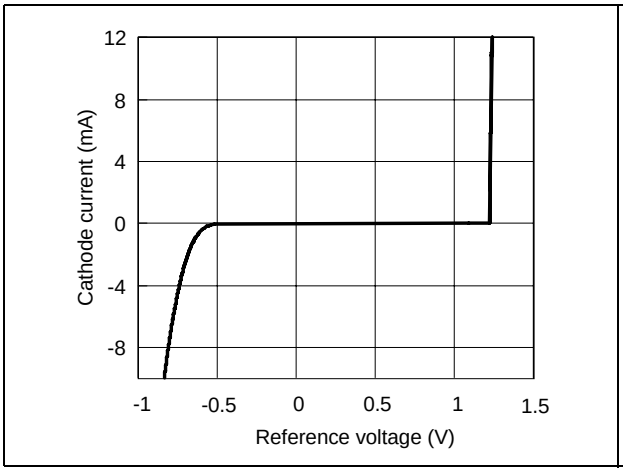


Figure 2. Reference voltage versus cathode current

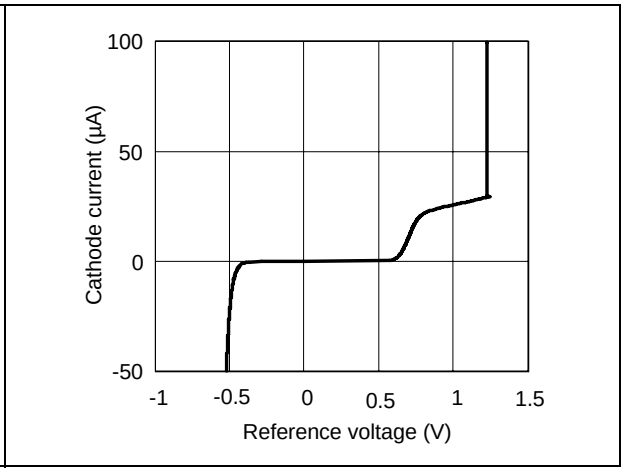


Figure 3. Test circuit

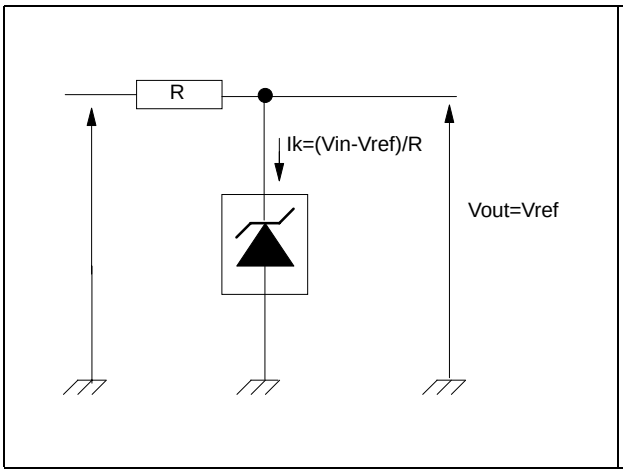


Figure 4. Reference voltage versus temperature

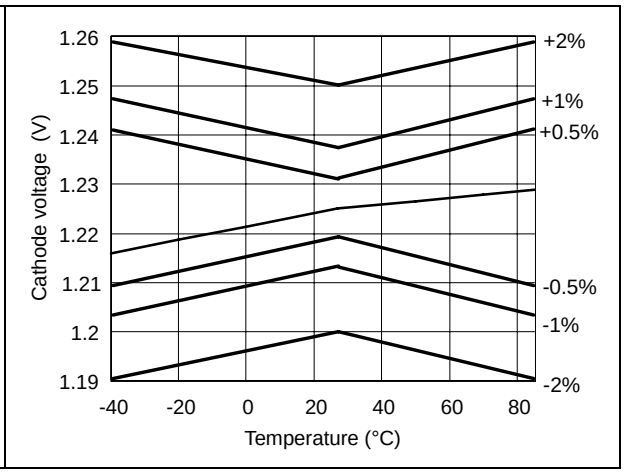


Figure 5. Static impedance versus temperature

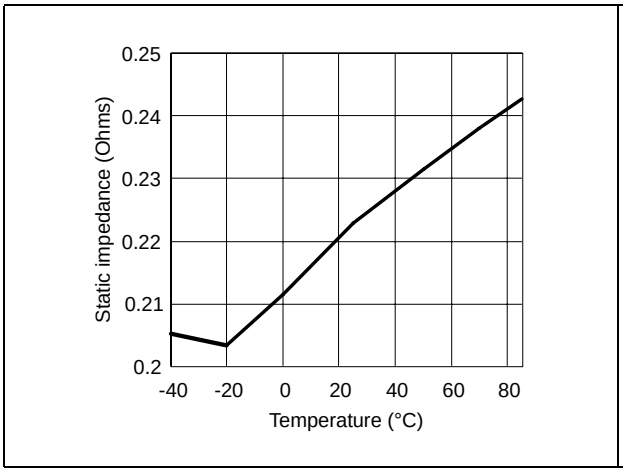


Figure 6. Noise voltage versus frequency

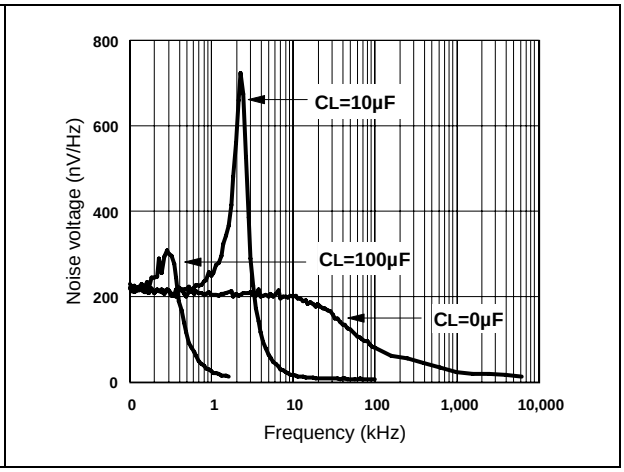


Figure 7. Pulse response for I_k=100µA

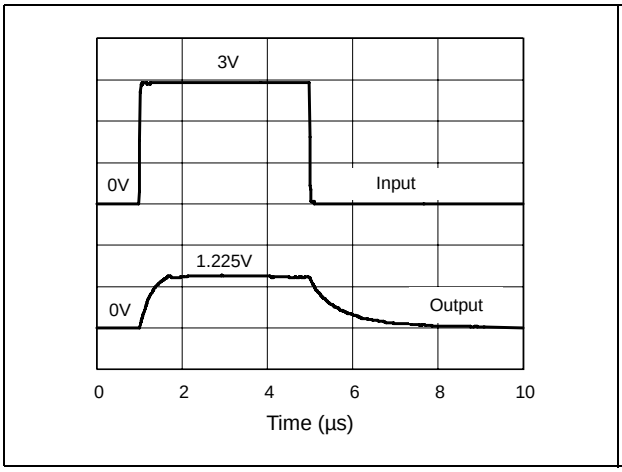


Figure 8. Test circuit for pulse response at I_k=100µA

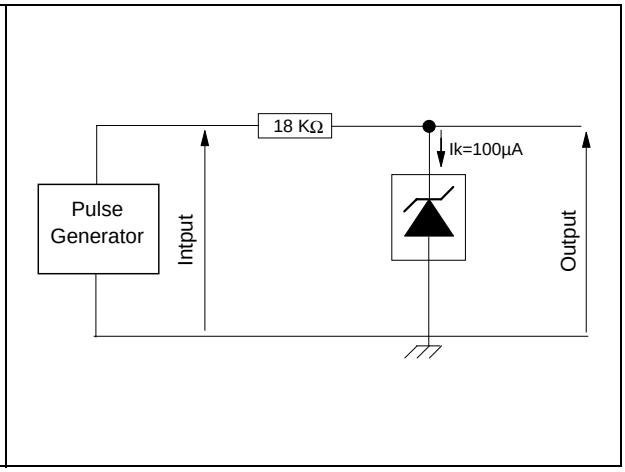


Figure 9. Pulse response for I_k=1mA

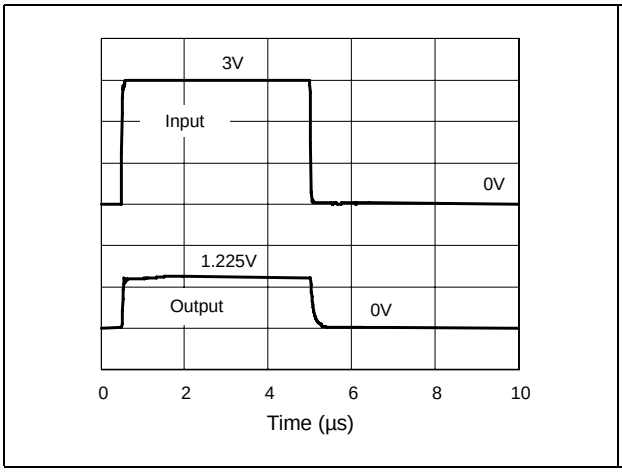
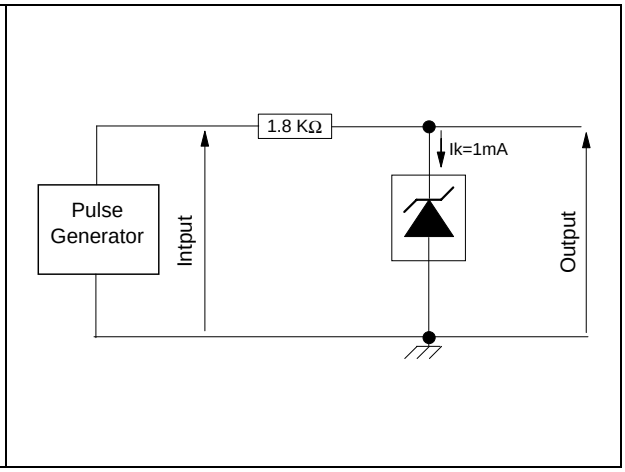


Figure 10. Test circuit for pulse response at I_k=1mA

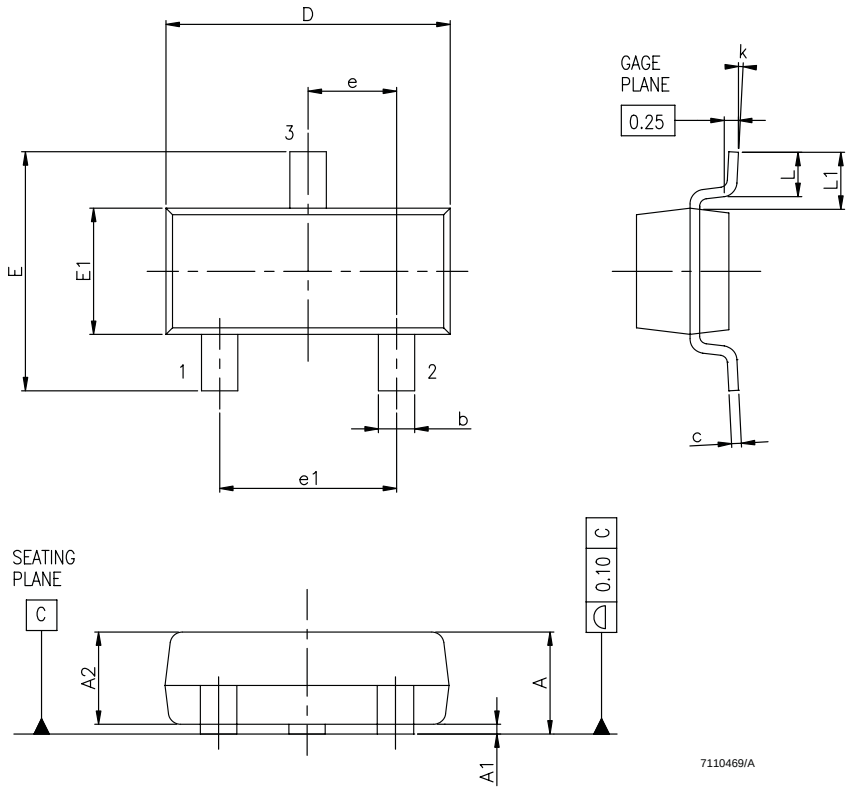


3 Package information

In order to meet environmental requirements, STMicroelectronics offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an STMicroelectronics trademark. ECOPACK specifications are available at: www.st.com.

Figure 11. SOT23-3 package mechanical data

Ref.	Dimensions					
	Millimeters			Mils		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.890		1.120	35.05		44.12
A1	0.010		0.100	0.39		3.94
A2	0.880	0.950	1.020	34.65	37.41	40.17
b	0.300		0.500	11.81		19.69
C	0.080		0.200	3.15		7.88
D	2.800	2.900	3.040	110.26	114.17	119.72
E	2.100		2.64	82.70		103.96
E1	1.200	1.300	1.400	47.26	51.19	55.13
e		0.950			37.41	
e1		1.900			74.82	
L	0.400		0.600	15.75		23.63
L1		0.540			21.27	
k	0°		8°	0°		8°



4 Ordering information

Table 6. Order codes

Part number	Precision	Temperature range	Package	Packing	Marking
TS4041EILT-1.2	2%	-40°C to +85°C	SOT23-3	Tape & reel	L233
TS4041DILT-1.2	1%				L232
TS4041CILT-1.2	0.5%				L231

5 Revision history

Table 7. Document revision history

Date	Revision	Changes
21-Mar-2002	1	Initial release.
20-Aug-2007	2	Removed TO-92 package information. Format update.

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