

TC1269

1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings*

Input Voltage6.5V
Output Voltage..... ($V_{SS} - 0.3$) to ($V_{IN} + 0.3V$)
Maximum Voltage on Any Pin $V_{IN} + 0.3V$ to $-0.3V$
Power Dissipation.....Internally Limited (**Note 6**)
Operating Temperature $-40^{\circ}C < T_J < +125^{\circ}C$
Storage Temperature..... $-65^{\circ}C$ to $+150^{\circ}C$

*Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

TC1269 ELECTRICAL SPECIFICATIONS

Electrical Characteristics: $V_{IN} = V_{OUT} + 1V$, $I_L = 0.1\mu A$, $C_L = 3.3\mu F$, $\overline{SHDN} > V_{IH}$, $T_A = 25^{\circ}C$, unless otherwise noted. Boldface type specifications apply for junction temperatures of $-40^{\circ}C$ to $+125^{\circ}C$.						
Symbol	Parameter	Min	Typ	Max	Units	Test Conditions
V_{IN}	Input Operating Voltage	—	—	6.0	V	
I_{OUTMAX}	Maximum Output Current	300	—	—	mA	
V_{OUT}	Output Voltage	— $V_R - 2.5\%$	$V_R \pm 0.5\%$ —	— $V_R + 2.5\%$	V	Note 1
$\Delta V_{OUT}/\Delta T$	V_{OUT} Temperature Coefficient	—	40	—	ppm/ $^{\circ}C$	Note 2
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	—	0.05	0.35	%	$(V_R + 1V) \leq V_{IN} \leq 6V$
$\Delta V_{OUT}/I_{OUT}$	Load Regulation	—	0.5	2.0	%	$I_L = 0.1mA$ to I_{OUTMAX}
$V_{IN}-V_{OUT}$	Dropout Voltage	— — —	20 80 240	30 160 480	mV	$I_L = 0.1mA$ $I_L = 100mA$ $I_L = 300mA$ (Note 4)
I_{SS1}	Supply Current	—	50	90	μA	$\overline{SHDN} = V_{IH}$
I_{SS2}	Shutdown Supply Current	—	0.05	0.5	μA	$\overline{SHDN} = 0V$
PSRR	Power Supply Rejection Ratio	—	50	—	dB	$F_{RE} \leq 120Hz$
I_{OUTSC}	Output Short Circuit Current	—	550	650	mA	$V_{OUT} = 0V$
$\Delta V_{OUT}/\Delta P_D$	Thermal Regulation	—	0.04	—	V/W	Note 5
eN	Output Noise	—	260	—	nV/ $\sqrt{Hz}$	$F = 1kHz$, $C_{OUT} = 1\mu F$, $R_{LOAD} = 50\Omega$
SHDN Input						
V_{IH}	SHDN Input High Threshold	45	—	—	% V_{IN}	
V_{IL}	SHDN Input Low Threshold	—	—	15	% V_{IN}	

Note 1: V_R is the regulator output voltage setting.

$$2: T_C V_{OUT} = \frac{(V_{OUTMAX} - V_{OUTMIN}) \times 10^6}{V_{OUT} \times \Delta T}$$

- 3: Regulation is measured at a constant junction temperature using low duty cycle pulse testing. Load regulation is tested over a load range from 0.1mA to the maximum specified output current. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 4: Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at a 1V differential.
- 5: Thermal Regulation is defined as the change in output voltage at a time T after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a current pulse equal to I_{LMAX} at $V_{IN} = 6V$ for $T = 10$ msec.
- 6: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction-to-air (i.e., T_A , T_J , θ_{JA}). Exceeding the maximum allowable power dissipation causes the device to initiate thermal shutdown. Please see Section 4.0 Thermal Considerations for more details.

2.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 2-1.

TABLE 2-1: PIN FUNCTION TABLE

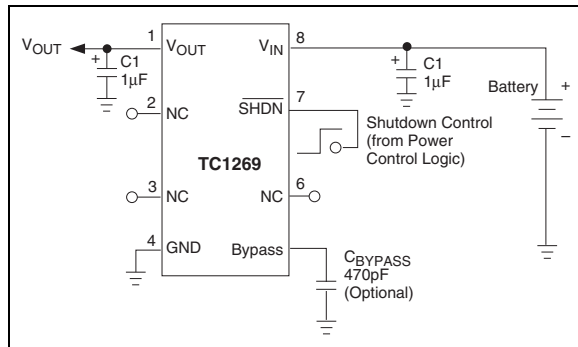
Pin No. (8-Pin SOIC)	Symbol	Description
1	V_{OUT}	Regulated voltage output.
2	NC	No connect.
3	NC	No connect.
4	GND	Ground terminal.
5	Bypass	Reference bypass input. Connecting a 470pF to this input further reduces output noise.
6	NC	No connect.
7	$\overline{SHDN}$	Shutdown control input. The regulator is fully enabled when a logic high is applied to this input. The regulator enters shutdown when a logic low is applied to this input. During shutdown, output voltage falls to zero and supply current is reduced to 0.05 μ A (typical).
8	V_{IN}	Unregulated supply input.

3.0 DETAILED DESCRIPTION

The TC1269 is a precision regulator available in fixed voltages. Unlike the bipolar regulators, the TC1269 supply current does not increase with load current. In addition, V_{OUT} remains stable and within regulation over the entire 0mA to I_{OUTMAX} operating load current range, (an important consideration in RTC and CMOS RAM battery backup applications).

Figure 3-1 shows a typical application circuit. The regulator is enabled any time the shutdown input (SHDN) is at or above V_{IH} , and shutdown (disabled) when SHDN is at or below V_{IL} . SHDN may be controlled by a CMOS logic gate, or I/O port of a microcontroller. If the SHDN input is not required, it should be connected directly to the input supply. While in shutdown, supply current decreases to 0.05 μ A (typical), V_{OUT} falls to zero.

FIGURE 3-1: TYPICAL APPLICATION CIRCUIT



3.1 Bypass Input

A 470pF capacitor connected from the Bypass input to ground reduces noise present on the internal reference, which in turn significantly reduces output noise. If output noise is not a concern, this input may be left unconnected. Larger capacitor values may be used, but results in a longer time period to rated output voltage when power is initially applied.

3.2 Output Capacitor

A 1 μ F (min) capacitor from V_{OUT} to ground is recommended. The output capacitor should have an effective series resistance greater than 0.1 Ω and less than 5.0 Ω , and a resonant frequency above 1MHz. A 1 μ F capacitor should be connected from V_{IN} to GND if there is more than 10 inches of wire between the regulator and the AC filter capacitor, or if a battery is used as the power source. Aluminum electrolytic or tantalum capacitor types can be used. (Since many aluminum electrolytic capacitors freeze at approximately -30°C, solid tantalums are recommended for applications operating below -25°C.) When operating from sources other than batteries, supply-noise rejection and transient response can be improved by increasing the value of the input and output capacitors and employing passive filtering techniques.

4.0 THERMAL CONSIDERATIONS

4.1 Thermal Shutdown

Integrated thermal protection circuitry shuts the regulator off when die temperature exceeds 150°C. The regulator remains off until the die temperature drops to approximately 140°C.

4.2 Power Dissipation

The amount of power the regulator dissipates is primarily a function of input and output voltage, and output current. The following equation is used to calculate worst case actual power dissipation:

EQUATION 4-1:

$$P_D \approx (V_{INMAX} - V_{OUTMIN}) I_{LOADMAX}$$

Where:

P_D = Worst case actual power dissipation
 V_{INMAX} = Maximum voltage on V_{IN}
 V_{OUTMIN} = Minimum regulator output voltage
 $I_{LOADMAX}$ = Maximum output (load) current

The maximum allowable power dissipation (Equation 4-2) is a function of the maximum ambient temperature (T_{AMAX}), the maximum allowable die temperature (T_{JMAX}) and the thermal resistance from junction-to-air (θ_{JA}).

EQUATION 4-2:

$$P_{DMAX} = \frac{(T_{JMAX} - T_{AMAX})}{\theta_{JA}}$$

Where all terms are previously defined.

Equation 4-1 can be used in conjunction with Equation 4-2 to ensure regulator thermal operation is within limits. For example:

Given:

$$V_{INMAX} = 3.0V \pm 10\%$$

$$V_{OUTMIN} = 2.7V - 2.5\%$$

$$I_{LOAD} = 250mA$$

$$T_{AMAX} = 55^\circ C$$

Find: 1. Actual power dissipation
 2. Maximum allowable dissipation

Actual power dissipation:

$$\begin{aligned} P_D &\approx (V_{INMAX} - V_{OUTMIN}) I_{LOADMAX} \\ &= [(3.0 \times 1.1) - (2.7 \times .975)] 250 \times 10^{-3} \\ &= 167mW \end{aligned}$$

Maximum allowable power dissipation:

$$\begin{aligned} P_{DMAX} &= \frac{(T_{JMAX} - T_{AMAX})}{\theta_{JA}} \\ &= \frac{(125 - 55)}{200} \\ &= 350mW \end{aligned}$$

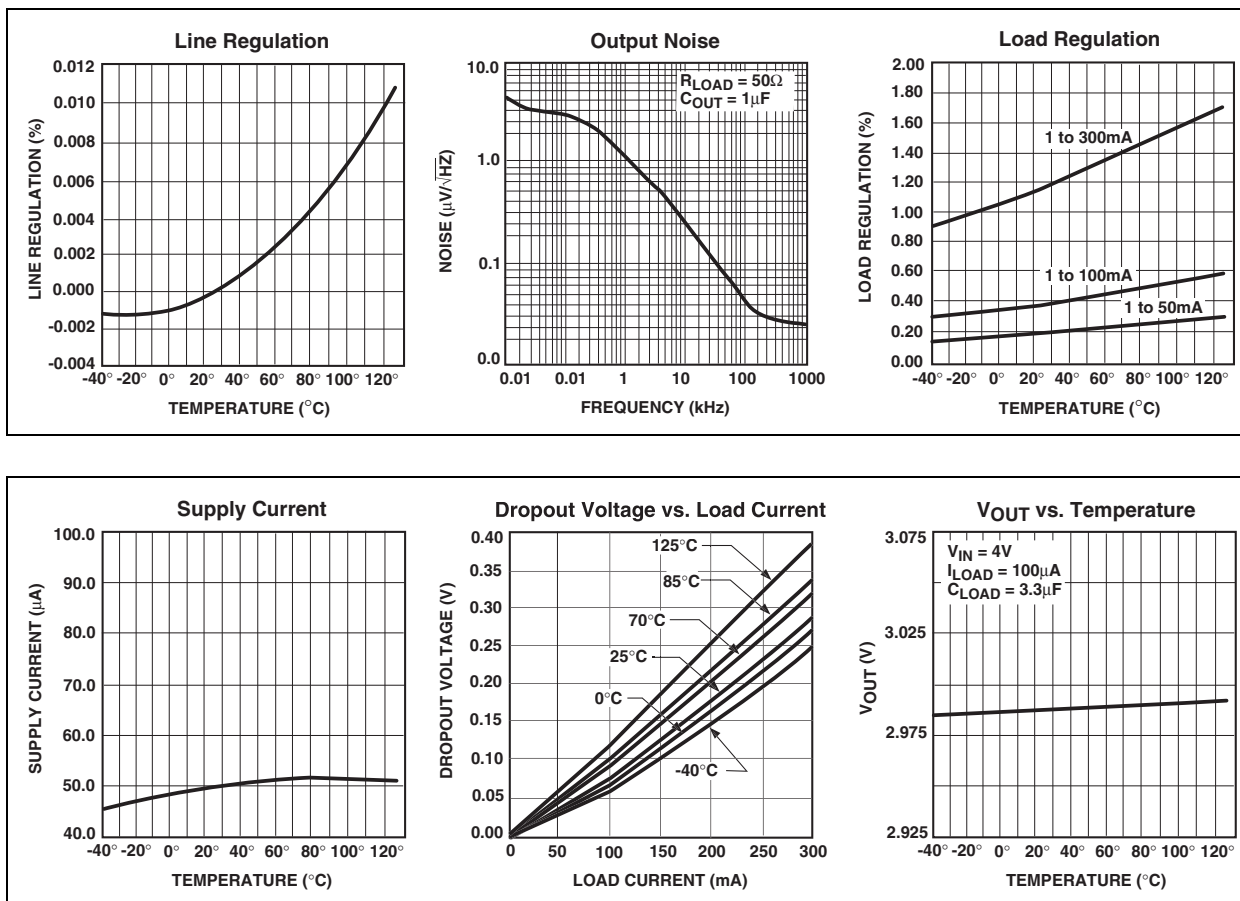
In this example, the TC1269 dissipates a maximum of 167mW; below the allowable limit of 350mW. In a similar manner, Equation 4-1 and Equation 4-2 can be used to calculate maximum current and/or input voltage limits.

4.3 Layout Considerations

The primary path of heat conduction out of the package is via the package leads. Therefore, layouts having a ground plane, wide traces at the pads, and wide power supply bus lines combine to lower θ_{JA} and, therefore, increase the maximum allowable power dissipation limit.

5.0 TYPICAL CHARACTERISTICS

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

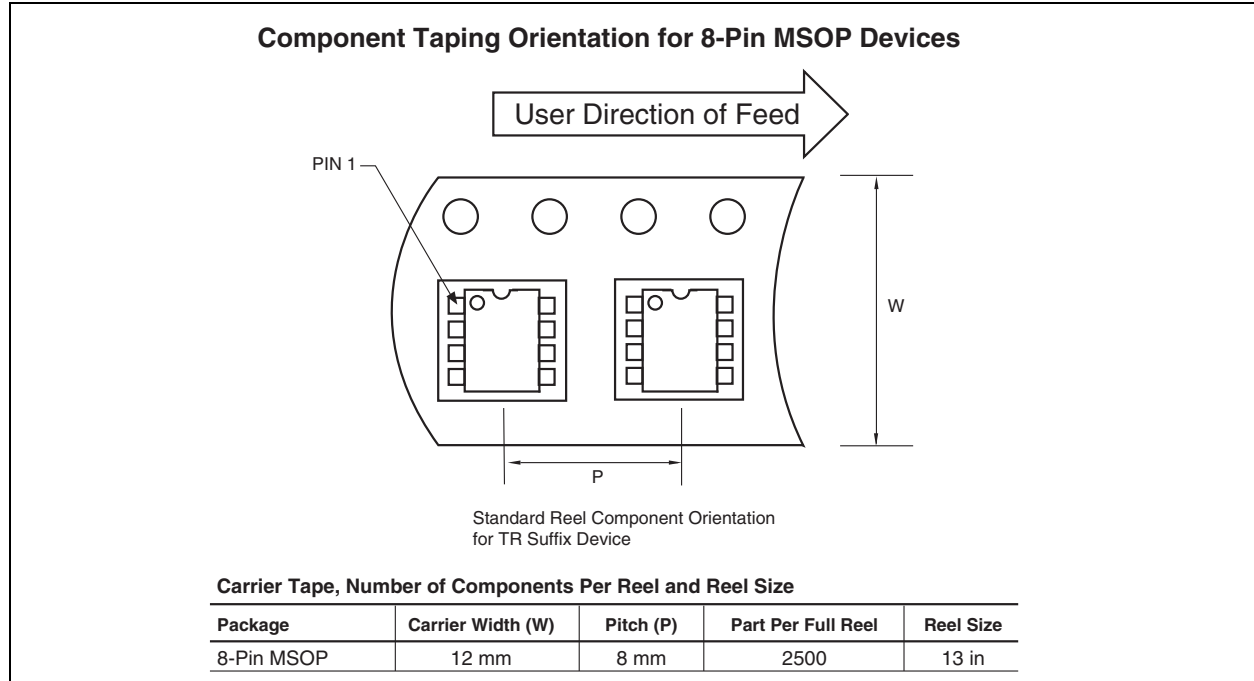


6.0 PACKAGING INFORMATION

6.1 Package Marking Information

Package marking data not available at this time.

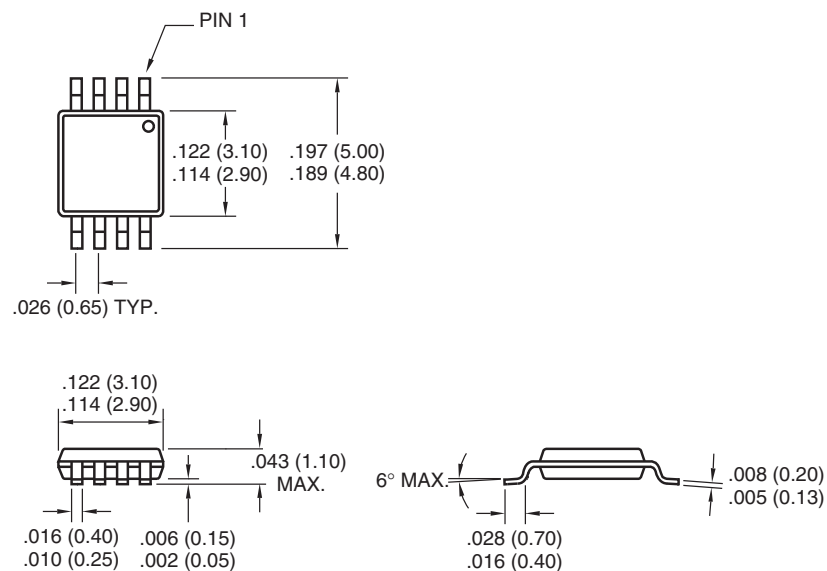
6.2 Taping Form



6.3 Package Dimensions

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>

8-Pin MSOP



7.0 REVISION HISTORY

Revision C (November 2012)

Added a note to each package outline drawing.

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TC1269

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