

# SY100EP32V

## 1.0 ELECTRICAL CHARACTERISTICS

### Absolute Maximum Ratings †

PECL Power Supply Voltage ( $V_{CC}$ ) (Note 1)	+6V
NECL Power Supply Voltage ( $V_{EE}$ ) (Note 2)	-6V
PECL Mode Input Voltage ( $V_{IN}$ ) (Note 3)	+6V
NECL Mode Input Voltage ( $V_{IN}$ ) (Note 4)	-6V
Continuous Output Current ( $I_{OUT}$ )	50 mA
Surge Output Current ( $I_{OUT}$ )	100 mA
VBB Sink/Source Current ( $I_{BB}$ ) (Note 5)	$\pm 0.5$ mA

† **Notice:** Stresses above those listed under “Absolute Maximum ratings” may cause permanent damage to the device. Exposure to maximum rating conditions for extended periods may affect device reliability.

**Note 1:**  $V_{CC} = 0V$ .

**2:**  $V_{EE} = 0V$ .

**3:**  $V_{EE} = 0V$ ,  $V_{IN} \leq V_{CC}$ .

**4:**  $V_{CC} = 0V$ ,  $V_{IN} \geq V_{EE}$ .

**5:** Due to the limited drive capability, the VBB reference should only be used for the input of the same package device (i.e., do not use for other devices).

### DC ELECTRICAL CHARACTERISTICS (Note 1)

**Electrical Characteristics PECL:**  $V_{CC} = 3.0V$  to  $5.5V$ ;  $V_{EE} = 0V$ ;  $T_A = -40^\circ C$  to  $+85^\circ C$ , unless otherwise stated.

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Power Supply Current	$I_{EE}$	—	—	37	mA	$T_A = -40^\circ C$
		—	30	37		$T_A = +25^\circ C$
		—	—	42		$T_A = +85^\circ C$
Output High Voltage (Note 2)	$V_{OH}$	$V_{CC} - 1.145$	$V_{CC} - 1.020$	$V_{CC} - 0.895$	V	—
Output Low Voltage (Note 2)	$V_{OL}$	$V_{CC} - 1.945$	$V_{CC} - 1.820$	$V_{CC} - 1.695$	V	—
Input High Voltage	$V_{IH}$	$V_{CC} - 1.225$	—	$V_{CC} - 0.880$	V	—
Input Low Voltage	$V_{IL}$	$V_{CC} - 1.945$	—	$V_{CC} - 1.625$	V	—
Common Mode Range (Note 3)	$V_{IHCMR}$	2.0	—	$V_{CC}$	V	—
Output Voltage	$V_{BB}$	$V_{CC} - 1.525$	$V_{CC} - 1.425$	$V_{CC} - 1.325$	V	—
Input High Current	$I_{IH}$	—	—	150	$\mu A$	—
Input Low Current CLK	$I_{IL}$	0.5	—	—	$\mu A$	$V_{IN} = V_{IL} \text{ (Min)}$
Input Low Current /CLK		-150	—	—		

**Note 1:** Devices are designed to meet the DC specifications shown in the above table after thermal equilibration has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse airflow greater than 500 lfpm is maintained.

**2:** Outputs are terminated through a  $50\Omega$  resistor to  $V_{CC} - 2.0V$ .

**3:** The CMR range is referenced to the most positive side of the differential input voltage. Normal operation is obtained if the high level falls within the specified range and the peak-to-peak voltage lies between 150 mV and 1.2V.

**DC ELECTRICAL CHARACTERISTICS (Note 1)**

**Electrical Characteristics NECL:**  $V_{EE} = -5.5V$  to  $-3.0V$ ;  $V_{CC} = 0V$ ;  $T_A = -40^{\circ}C$  to  $+85^{\circ}C$ , unless otherwise stated.

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Power Supply Current	$I_{EE}$	—	—	37	mA	$T_A = -40^{\circ}C$
		—	30	37		$T_A = +25^{\circ}C$
		—	—	42		$T_A = +85^{\circ}C$
Output High Voltage (Note 2)	$V_{OH}$	-1.145	-1.020	-0.895	V	—
Output Low Voltage (Note 2)	$V_{OL}$	-1.945	-1.820	-1.695	V	—
Input High Voltage	$V_{IH}$	-1.225	—	-0.880	V	—
Input Low Voltage	$V_{IL}$	-1.945	—	-1.625	V	—
Common Mode Range (Note 3)	$V_{IHCMR}$	$V_{EE} + 2.0$	—	0	V	—
Output Voltage	$V_{BB}$	-1.525	-1.425	-1.325	V	—
Input High Current	$I_{IH}$	—	—	150	$\mu A$	—
Input Low Current CLK	$I_{IL}$	0.5	—	—	$\mu A$	$V_{IN} = V_{IL} \text{ (Min)}$
Input Low Current /CLK		-150	—	—		

- Note 1:** Devices are designed to meet the DC specifications shown in the above table after thermal equilibration has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse airflow greater than 500 lfpm is maintained.
- 2:** Outputs are terminated through a  $50\Omega$  resistor to  $V_{CC} - 2.0V$ .
- 3:** The CMR range is referenced to the most positive side of the differential input voltage. Normal operation is obtained if the high level falls within the specified range and the peak-to-peak voltage lies between 150 mV and 1.2V.

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## AC ELECTRICAL CHARACTERISTICS (Note 1)

**Electrical Characteristics:**  $V_{CC} = 3.0V$  to  $5.5V$ ;  $V_{EE} = 0V$  or  $V_{EE} = -5.5V$  to  $-3.0V$ ;  $V_{CC} = 0V$ ;  
 $T_A = -40^{\circ}C$  to  $+85^{\circ}C$ , unless otherwise stated. All loading with a  $50\Omega$  resistor to  $V_{CC} - 2.0V$ .

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Maximum Toggle Frequency (Note 3)	$f_{MAX}$	4	—	—	GHz	—
Propagation Delay to Differential Output RESET, CLK $\rightarrow$ Q, /Q	$t_{PLH}, t_{PHL}$	250	330	420	ps	$T_A = -40^{\circ}C$
		260	330	430		$T_A = +25^{\circ}C$
		280	400	440		$T_A = +85^{\circ}C$
Reset Recovery Time	$t_{RR}$	200	100	—	ps	$T_A = +25^{\circ}C$
		200	—	—		$T_A = -40^{\circ}C, +85^{\circ}C$
Minimum Pulse Width Reset	$t_{PW}$	550	200	—	ps	$T_A = +25^{\circ}C$
		550	—	—		$T_A = -40^{\circ}C, +85^{\circ}C$
Cycle-to-Cycle RMS Jitter	$t_{JITTER}$	—	0.2	<1	ps (rms)	—
Input Voltage Swing (Differential Configuration, Note 4)	$V_{PP}$	150	800	1200	mV	—
Output Rise/Fall Time Q (20% to 80%)	$t_r/t_f$	50	100	150	ps	$T_A = -40^{\circ}C$
		50	100	160		$T_A = +25^{\circ}C, +85^{\circ}C$

**Note 1:** Measured with 750 mV input signal, 50% duty cycle clock source.

**2:** Specifications for packaged product only.

**3:**  $f_{MAX}$  guaranteed for functionality only.  $V_{OL}$  and  $V_{OH}$  levels are guaranteed at DC only.

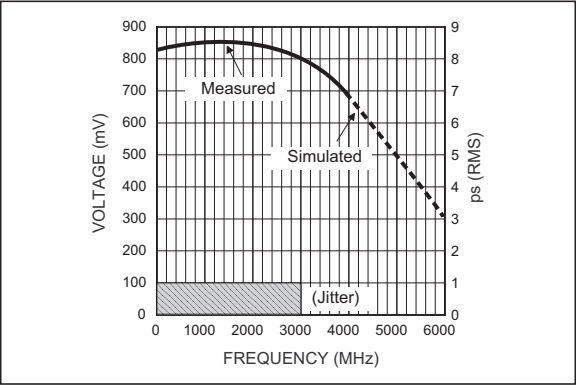
**4:** Input swing for which AC parameters are ensured.

**TEMPERATURE SPECIFICATIONS**

Parameters	Symbol	Min.	Typ.	Max.	Units	Conditions
<b>Temperature Ranges</b>						
Operating Temperature Range	$T_A$	-40	—	+85	°C	—
Storage Temperature Range	$T_S$	-65	—	+150	°C	—
Lead Temperature	$T_{LEAD}$	—	—	+260	°C	Soldering, 20 sec.
<b>Package Thermal Resistance (MSOP)</b>						
Junction-to-Ambient	$\theta_{JA}$	—	206	—	°C/W	Still Air
		—	155	—		500 lfpm
Junction-to-Case	$\theta_{JC}$	—	39	—	°C/W	—

## 2.0 TYPICAL CHARACTERISTICS

**Note:** The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.



**FIGURE 2-1:**  $f_{MAX}$  and RMS Jitter.

### 3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in [Table](#) .

#### PIN FUNCTION TABLE

Pin Number	Pin Name	Description
1	RESET	ECL Asynchronous Reset (with 75 kΩ resistor pull-down resistor to GND).
2, 3	CLK, /CLK	ECL, Clock Inputs (CLK input with 75 kΩ resistor pull-down resistor to GND, /CLK input with 75 kΩ resistor divider).
4	VBB	Reference Voltage Output.
5	VEE	Negative Supply Voltage.
6, 7	/Q, Q	ECL Outputs.
8	VCC	Positive Supply Voltage.

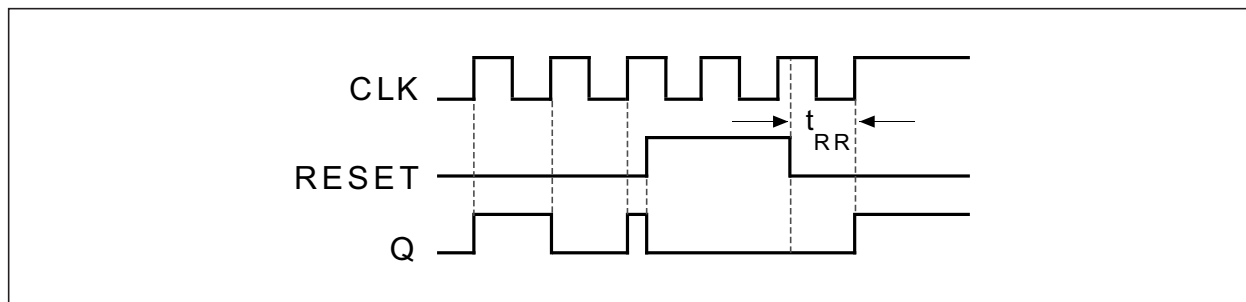
### 3.1 Truth Table

TABLE 3-1: TRUTH TABLE ([Note 1](#))

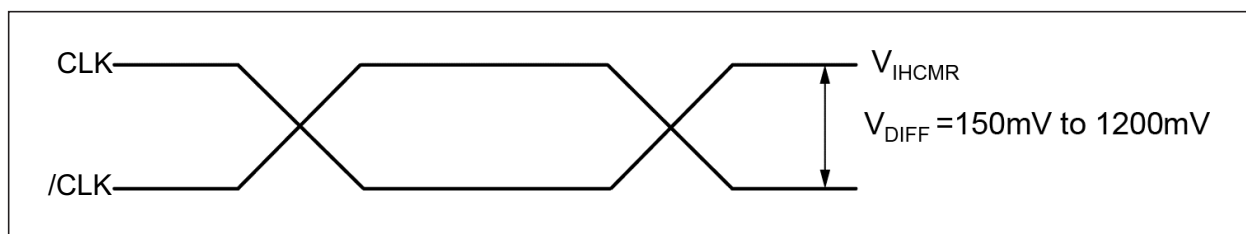
CLK	/CLK	RESET	Q	/Q
X	X	H	L	H
Z	/Z	L	F	F

**Note 1:** Z = Low-to-High Transition  
 /Z = High-to-Low Transition  
 F = Divide by 2 Function

## 4.0 TIMING DIAGRAM

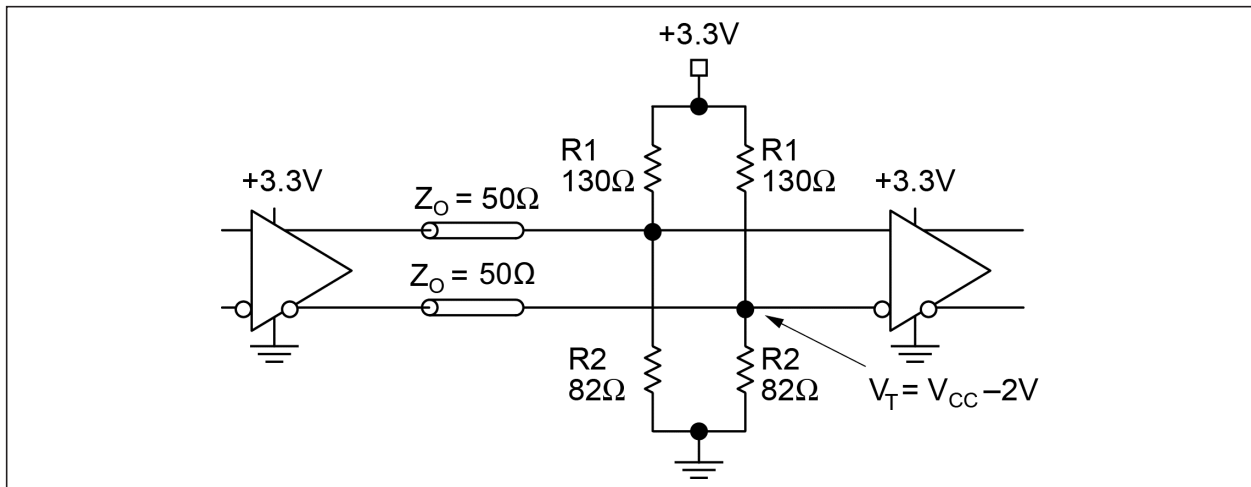


## 4.1 Input Waveform



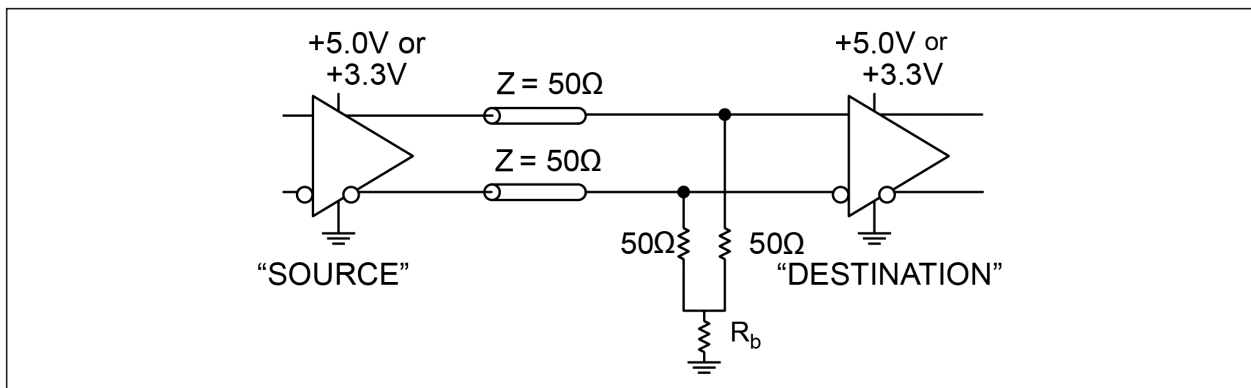
**FIGURE 4-1:** Input Waveform.

## 5.0 TERMINATION RECOMMENDATIONS



**FIGURE 5-1:** Parallel Termination - Thevenin Equivalent.

**Note:** For +5.0V systems:  $R1 = 82\Omega$ ,  $R2 = 130\Omega$



**FIGURE 5-2:** Three - Resistor Y - Termination

**Note:** Power-saving alternative to Thevenin termination

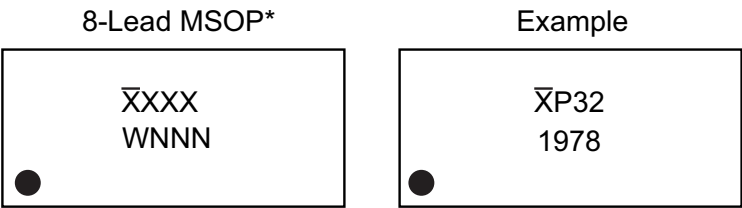
**Note:** Place termination resistors as close to destination inputs as possible

**Note:**  $R_b$  resistor sets the DC bias voltage equal to  $V_T$ . For +3.3V systems  $R_b = 46\Omega$  to  $50\Omega$   
For +5V systems,  $R_b = 110\Omega$ .

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## 6.0 PACKAGING INFORMATION

### 6.1 Package Marking Information



**Legend:**

XX...X

Y

YY

WW

NNN

(e3)

\*

Product code or customer-specific information

Year code (last digit of calendar year)

Year code (last 2 digits of calendar year)

Week code (week of January 1 is week '01')

Alphanumeric traceability code

Pb-free JEDEC® designator for Matte Tin (Sn)

This package is Pb-free. The Pb-free JEDEC designator (e3) can be found on the outer packaging for this package.

•, ▲, ▼

Pin one index is identified by a dot, delta up, or delta down (triangle mark).

**Note:**

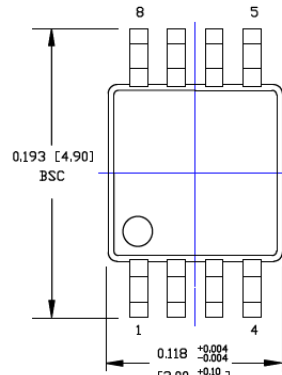
In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo.

Underbar ( \_ ) and/or Overbar ( ¯ ) symbol may not be to scale.

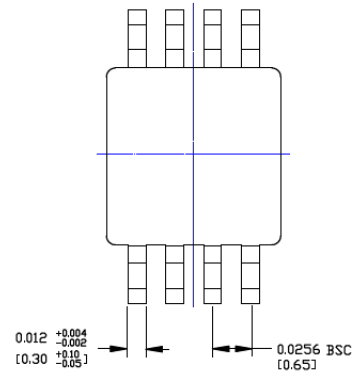
## TITLE

8 LEAD MSOP PACKAGE OUTLINE & RECOMMENDED LAND PATTERN

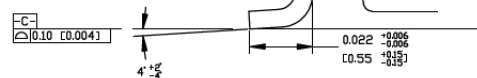
DRAWING #	MSOP-8LD-PL-1	UNIT	INCH [MM]
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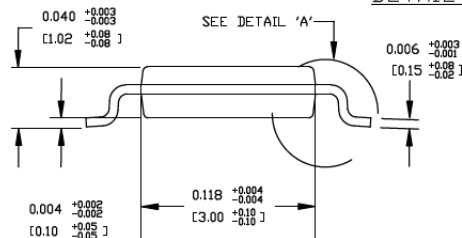
TOP VIEW



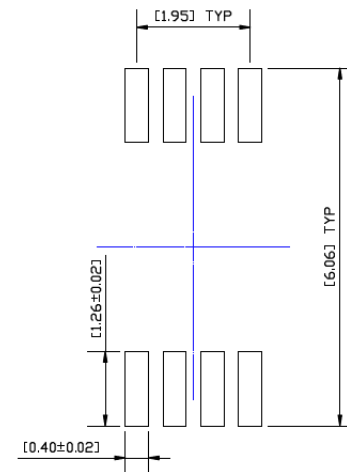
BOTTOM VIEW



DETAIL A



SIDE VIEW



RECOMMENDED LAND PATTERN

## NOTES:

1. DIMENSIONS ARE IN INCHES [MM].
2. CONTROLLING DIMENSION: MM
3. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.008 [0.20] PER SIDE.

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>.

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NOTES:

## APPENDIX A: REVISION HISTORY

### Revision A (March 2019)

- Converted Micrel document SY100EP32V to Microchip data sheet DS20006171A.
- Minor text changes throughout.
- Removed all reference to the EOL SY10EP32V version.
- Revised [Electrical Characteristics](#) section with updated Electrical tables.

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NOTES:

## PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

<u>PART NO.</u>	<u>-X</u>	<u>X</u>	<u>X</u>	<u>-XX</u>
Device	Voltage Option	Package	Temperature Range	Special Processing
<b>Device:</b>	SY100EP32: ECL +2 Divider			
<b>Voltage Option:</b>	V = 3.3V, 5V			
<b>Package:</b>	K = 8-Lead MSOP			
<b>Temperature Range:</b>	G = -40°C to +85°C (NiPdAu Pb-Free)			
<b>Special Processing:</b>	<blank> = 100/Tube TR = 1,000/Reel			

**Examples:**

a) SY100EP32VKG: 5V/3.3V ECL +2 Divider, -40°C to +85°C, 8-Lead MSOP, 100/Tube

b) SY100EP32VKG-TR: 5V/3.3V ECL +2 Divider, -40°C to +85°C, 8-Lead MSOP, 1,000/Reel

**Note 1:** Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.

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NOTES:

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