

## **PROTECTION PRODUCTS**

## Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20μs)	P <sub>pk</sub>	300	Watts
Peak Pulse Current (tp = 8/20μs)	I <sub>PP</sub>	20	А
Lead Soldering Temperature	T <sub>L</sub>	260 (10 sec.)	°C
Operating Temperature	T <sub>J</sub>	-55 to +125	°C
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics (T=25°C)

SMDA05CN-5						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>				5	V
Reverse Breakdown Voltage	$V_{_{BR}}$	I <sub>t</sub> = 1mA	6			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 5V, T=25°C			10	μΑ
Clamping Voltage	V <sub>c</sub>	I <sub>pp</sub> = 1A, tp = 8/20μs			9.8	V
Clamping Voltage	V <sub>c</sub>	$I_{pp} = 10A$ , tp = 8/20 $\mu$ s			11	V
Maximum Peak Pulse Current	I <sub>PP</sub>	tp = 8/20µs			20	А
Junction Capacitance	C <sub>j</sub>	Between I/O Pins and Ground V <sub>R</sub> = OV, f = 1MHz			350	pF

### SMDA15CN-5

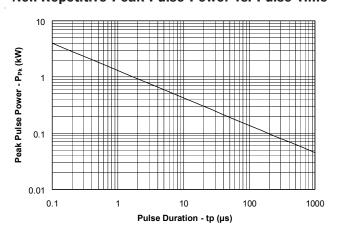
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>				15	V
Reverse Breakdown Voltage	$V_{_{BR}}$	I <sub>t</sub> = 1mA	16.7			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 15V, T=25°C			1	μΑ
Clamping Voltage	V <sub>c</sub>	$I_{pp} = 1A$ , tp = 8/20 $\mu$ s			24	V
Clamping Voltage	V <sub>c</sub>	$I_{pp} = 10A$ , tp = 8/20 $\mu$ s			30	V
Maximum Peak Pulse Current	I <sub>PP</sub>	tp = 8/20µs			10	Α
Junction Capacitance	C <sub>j</sub>	Between I/O Pins and Ground V <sub>R</sub> = OV, f = 1MHz			75	pF



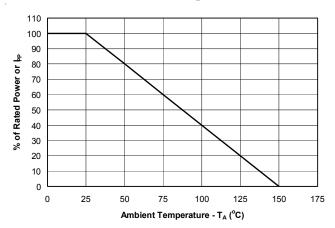
## PROTECTION PRODUCTS

## Typical Characteristics

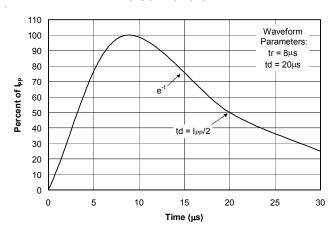
### Non-Repetitive Peak Pulse Power vs. Pulse Time



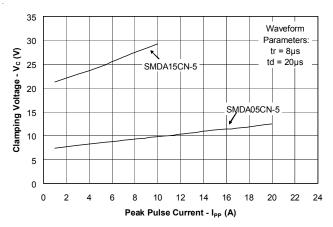
### **Power Derating Curve**



### **Pulse Waveform**



### Clamping Voltage vs. Peak Pulse Current







### **PROTECTION PRODUCTS**

# Applications Information

# **Device Connection Options for Protection of Four or Five Data Lines**

The SMDAxxCN-5 can be configured to protect either four or five bidirectional data lines. The options for connecting the devices are as follows:

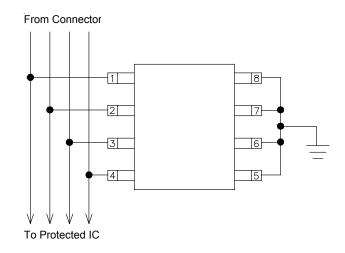
- 1. Bidirectional protection of four I/O lines is achieved by connecting pins 1, 2, 3, and 4 to the data lines. Pins 5, 6, 7, and 8 are connected to ground. The ground connection should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces. In this configuration, the device can withstand the maximum specified transient impulse on four lines simultaneously.
- 2. Bidirectional protection of five I/O lines is achieved by connecting pins 1, 2, 3, 4, and 5 to the data lines. Pins 6, 7, and 8 are connected to ground. The ground connection should be made directly to the circuit board ground plane for best results. In this configuration, the device can withstand the maximum rated transient impulse on any two lines simultaneously.

# Circuit Board Layout Recommendations for Suppression of ESD.

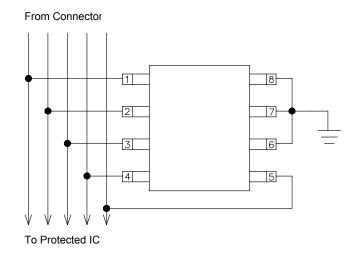
Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

### **Protection for Four Bidirectional Lines**



### **Protection for Five Bidirectional Lines**



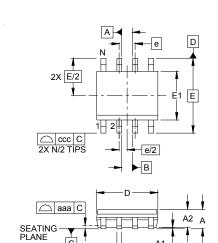
### **Matte Tin Lead Finish**

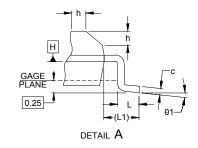
Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

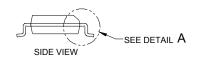


## PROTECTION PRODUCTS

## Outline Drawing - SO-8







	DIMENSIONS						
DIM	INCHES			MILLIMETERS			
ווווטן	MIN	NOM	MAX	MIN	NOM	MAX	
Α	.053	-	.069	1.35	-	1.75	
A1	.004	-	.010	0.10	-	0.25	
A2	.049	-	.065	1.25	-	1.65	
b	.012	-	.020	0.31	-	0.51	
С	.007	-	.010	0.17	-	0.25	
D	.189	.193	.197	4.80	4.90	5.00	
E1	.150	.154	.157	3.80	3.90	4.00	
E	.236 BSC			6.00 BSC			
е	.050 BSC			1	.27 BS	С	
h	.010	-	.020	0.25	-	0.50	
L	.016	.028	.041	0.40	0.72	1.04	
L1	(.041)			(1.04)			
N		8			8		
91	0°	-	8°	0°	-	8°	
aaa	.004				0.10		
bbb	.010			0.25			
CCC		.008 0.20					

#### NOTES:

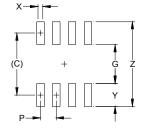
- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. DATUMS -A- AND -B- TO BE DETERMINED AT DATUM PLANE -H-

-bxN 

- DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- 4. REFERENCE JEDEC STD MS-012, VARIATION AA.

## Land Pattern - SO-8

C



DIMENSIONS				
DIM	INCHES	MILLIMETERS		
С	(.205)	(5.20)		
G	.118	3.00		
Р	.050	1.27		
Х	.024	0.60		
Y	.087	2.20		
Ζ	.291	7.40		

#### NOTES:

- 1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.
- 2. REFERENCE IPC-SM-782A, RLP NO. 300A.



## PROTECTION PRODUCTS

## Ordering Information

Part Number	Lead Finish	Qty per Reel	Reel Size
SMDA05CN-5.TB	SnPb	500	7 Inch
SMDA15CN-5.TB	SnPb	500	7 Inch
SMDA05CN-5.TBT	Pb Free	500	7 Inch
SMDA15CN-5.TBT	Pb Free	500	7 Inch
SMDA05CN-5	SnPb	95/Tube	N/A
SMDA15CN-5	SnPb	95/Tube	N/A
SMDA05CN-5.T	Pb Free	95/Tube	N/A
SMDA15CN-5.T	Pb Free	95/Tube	N/A

Note: Lead-free devices are RoHS/WEEE Compliant

## Contact Information

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