



SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	Ch-1	30	-	-	V
		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	Ch-2	30	-	-	
V <sub>DS</sub> temperature coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA	Ch-1	-	35	-	mV/°C
		I <sub>D</sub> = 250 μA	Ch-2	-	33	-	
V <sub>GS(th)</sub> temperature coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA	Ch-1	-	-4.5	-	
		I <sub>D</sub> = 250 μA	Ch-2	-	-5	-	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	Ch-1	1	-	2.5	V
		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	Ch-2	1.2	-	2.5	
Gate-body leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V	Ch-1	-	-	± 100	nA
			Ch-2	-	-	± 100	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	Ch-1	-	-	1	μA
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	Ch-2	-	-	1	
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	Ch-1	-	-	5	
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	Ch-2	-	-	5	
On-state drain current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	Ch-1	20	-	-	A
		V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	Ch-2	20	-	-	
Drain-source on-state resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.8 A	Ch-1	-	0.0200	0.0240	Ω
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	Ch-2	-	0.0105	0.0135	
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 7 A	Ch-1	-	0.0240	0.0300	
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 7 A	Ch-2	-	0.0135	0.0170	
Forward transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 7.8 A	Ch-1	-	17	-	S
		V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 A	Ch-2	-	24	-	
Dynamic <sup>a</sup>							
Input capacitance	C <sub>iss</sub>	Channel-1 V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz	Ch-1	-	435	-	pF
Output capacitance	C <sub>oss</sub>		Ch-2	-	846	-	
		Reverse transfer capacitance	C <sub>rss</sub>	Channel-2 V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz	Ch-1	-	
Ch-2	-			187	-		
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.8 A	Ch-1	-	8	12	nC
		V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	Ch-2	-	15.4	23	
		Channel-1 V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 7.8 A	Ch-1	-	3.8	6	
			Ch-2	-	7.3	11	
Gate-source charge	Q <sub>gs</sub>	Channel-2 V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 10 A	Ch-1	-	1.4	-	
			Ch-2	-	2.3	-	
Gate-drain charge	Q <sub>gd</sub>	Ch-1	-	1.1	-		
		Ch-2	-	2.2	-		
Gate resistance	R <sub>g</sub>	f = 1 MHz	Ch-1	0.6	3.2	6.4	Ω
			Ch-2	0.2	0.8	1.6	



SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT		
Dynamic <sup>a</sup>								
Turn-on delay time	t <sub>d(on)</sub>	Channel-1 V <sub>DD</sub> = 15 V, R <sub>L</sub> = 2.4 Ω I <sub>D</sub> ≅ 6.3 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 1 Ω	Ch-1	-	15	30	ns	
			Ch-2	-	15	30		
Rise time	t <sub>r</sub>		Ch-1	-	12	24		
			Ch-2	-	12	24		
Turn-off delay time	t <sub>d(off)</sub>	Channel-2 V <sub>DD</sub> = 15 V, R <sub>L</sub> = 1.5 Ω I <sub>D</sub> ≅ 10 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 1 Ω	Ch-1	-	13	26		
			Ch-2	-	13	26		
Fall time	t <sub>f</sub>		Ch-1	-	10	20		
			Ch-2	-	10	20		
Turn-on delay time	t <sub>d(on)</sub>	Channel-1 V <sub>DD</sub> = 15 V, R <sub>L</sub> = 2.4 Ω I <sub>D</sub> ≅ 6.3 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω	Ch-1	-	5	10		
			Ch-2	-	9	18		
Rise time	t <sub>r</sub>		Ch-1	-	10	20		
			Ch-2	-	9	18		
Turn-off delay time	t <sub>d(off)</sub>		Channel-2 V <sub>DD</sub> = 15 V, R <sub>L</sub> = 1.5 Ω I <sub>D</sub> ≅ 10 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω	Ch-1	-	15	30	
				Ch-2	-	14	28	
Fall time	t <sub>f</sub>			Ch-1	-	10	20	
				Ch-2	-	8	16	
Drain-Source Body Diode Characteristics								
Continuous source-drain diode current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	Ch-1	-	-	12	A	
			Ch-2	-	-	16		
Pulse diode forward current <sup>a</sup>	I <sub>SM</sub>		Ch-1	-	-	30		
			Ch-2	-	-	40		
Body diode voltage	V <sub>SD</sub>	I <sub>S</sub> = 6.3 A, V <sub>GS</sub> = 0 V	Ch-1	-	0.8	1.2	V	
		I <sub>S</sub> = 3 A, V <sub>GS</sub> = 0 V	Ch-2	-	0.78	1.2		
Body diode reverse recovery time	t <sub>rr</sub>	Channel-1 I <sub>F</sub> = 6.3 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C	Ch-1	-	15	30	ns	
			Ch-2	-	17	34		
Body diode reverse recovery charge	Q <sub>rr</sub>		Channel-2 I <sub>F</sub> = 10 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C	Ch-1	-	7	15	nC
				Ch-2	-	9.5	19	
Reverse recovery fall time	t <sub>a</sub>			Ch-1	-	9	-	ns
				Ch-2	-	10	-	
Reverse recovery rise time	t <sub>b</sub>			Ch-1	-	6	-	
				Ch-2	-	7	-	

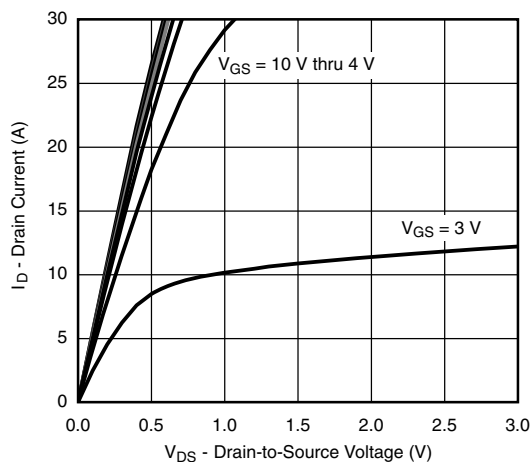
**Notes**

- a. Guaranteed by design, not subject to production testing  
b. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$

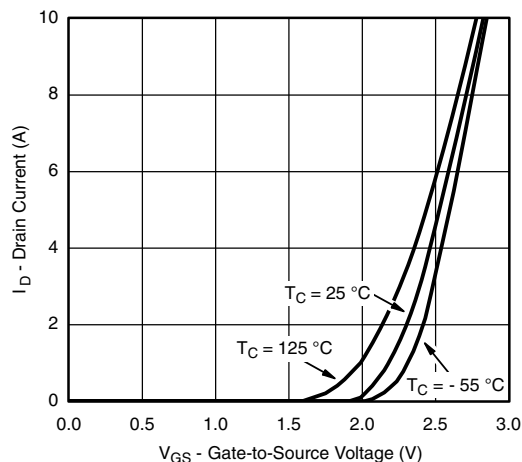
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



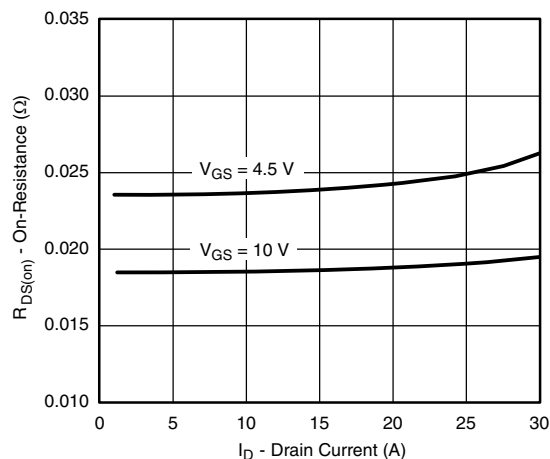
**CHANNEL-1 TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



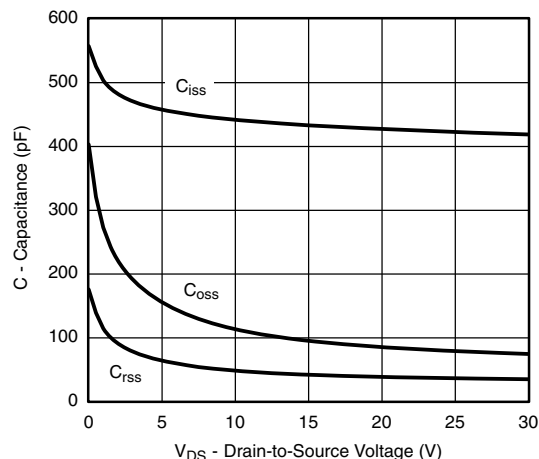
**Output Characteristics**



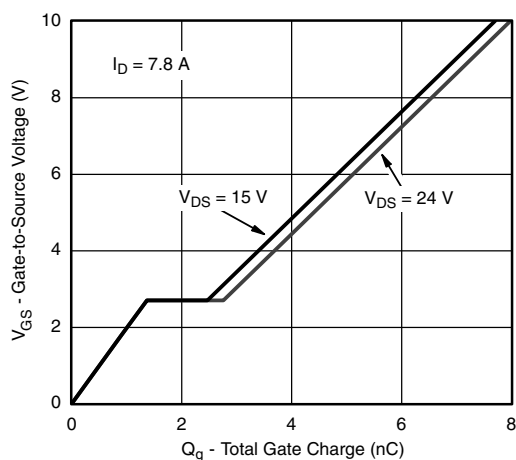
**Transfer Characteristics**



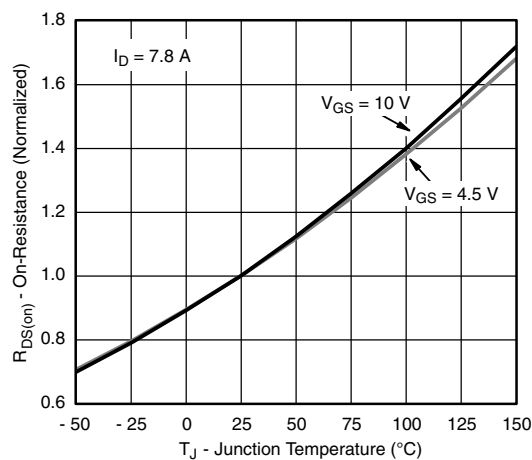
**On-Resistance vs. Drain Current**



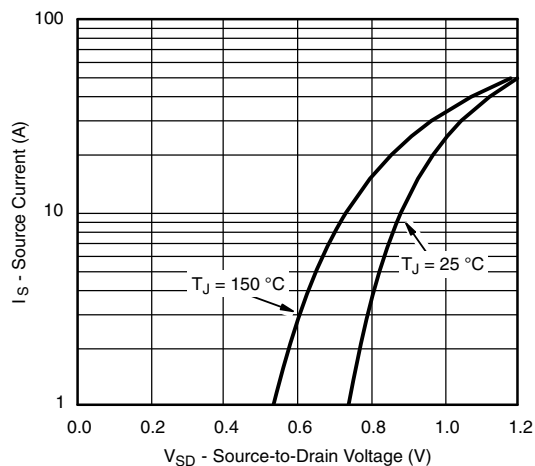
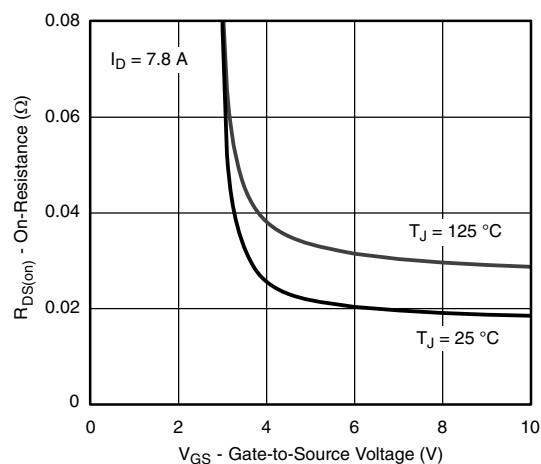
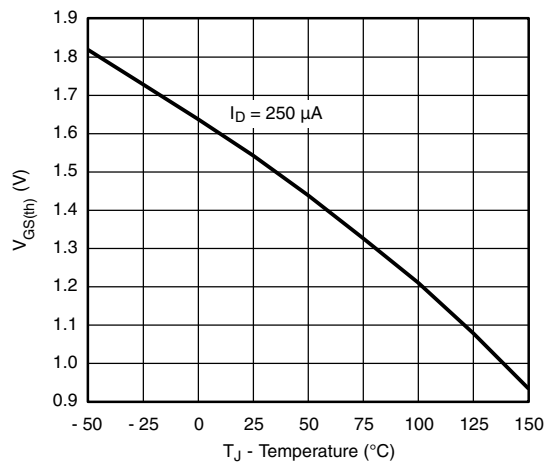
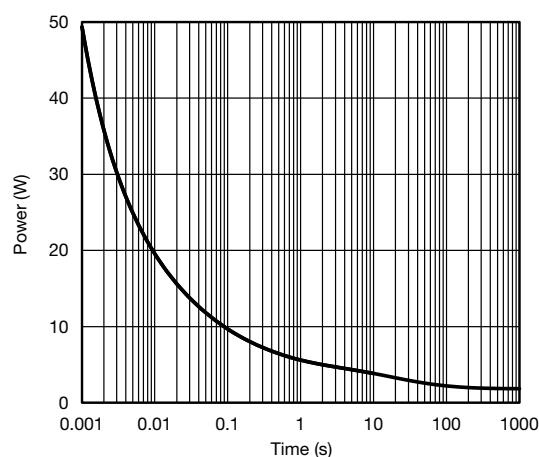
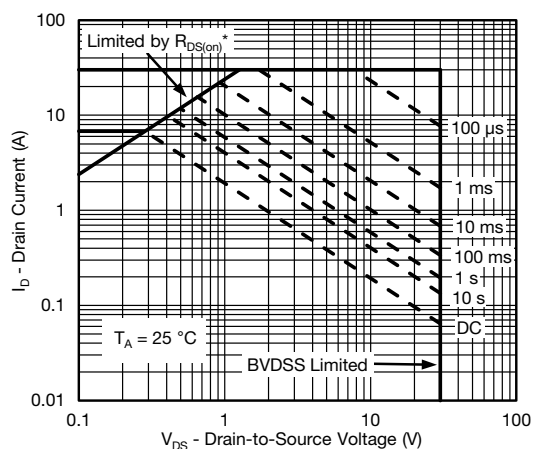
**Capacitance**

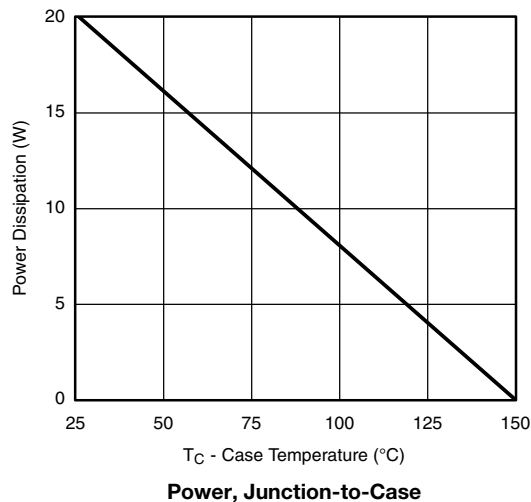
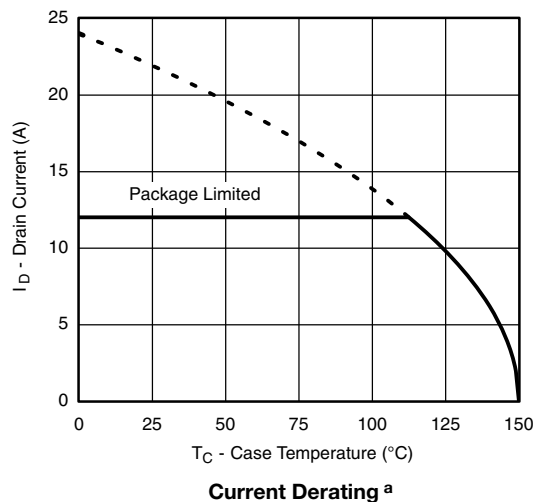


**Gate Charge**



**On-Resistance vs. Junction Temperature**

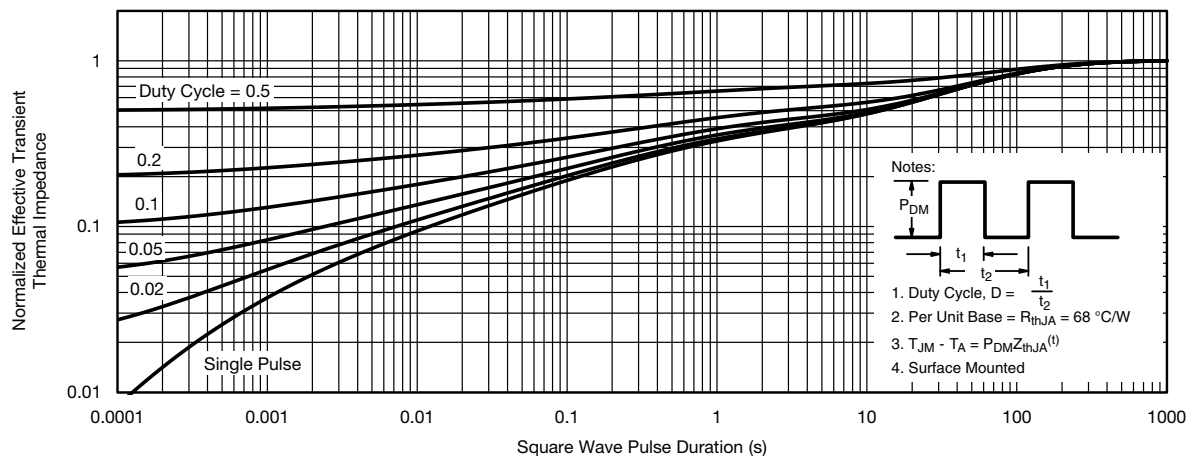
**CHANNEL-1 TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**Threshold Voltage**

**Single Pulse Power**

**Safe Operating Area, Junction-to-Ambient**

**CHANNEL-1 TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Note**

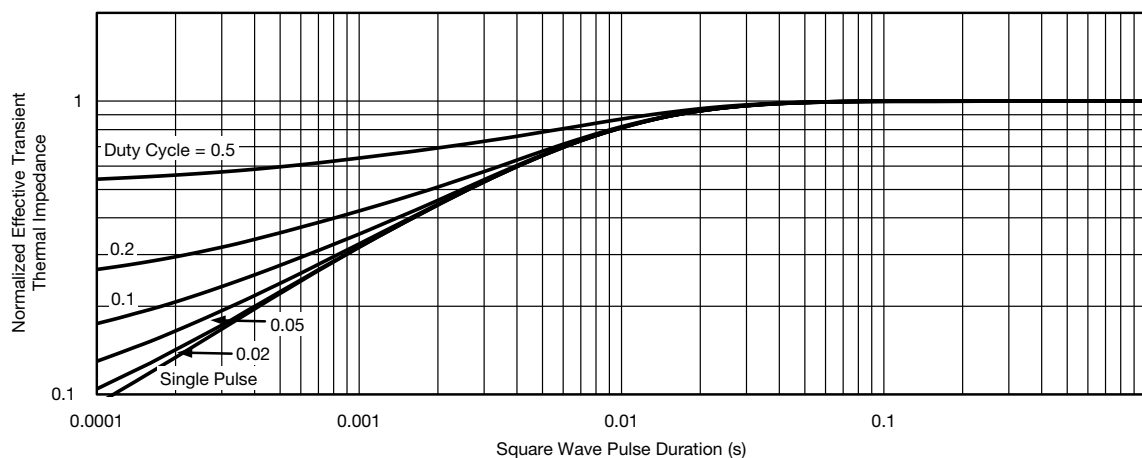
- a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



**CHANNEL-1 TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



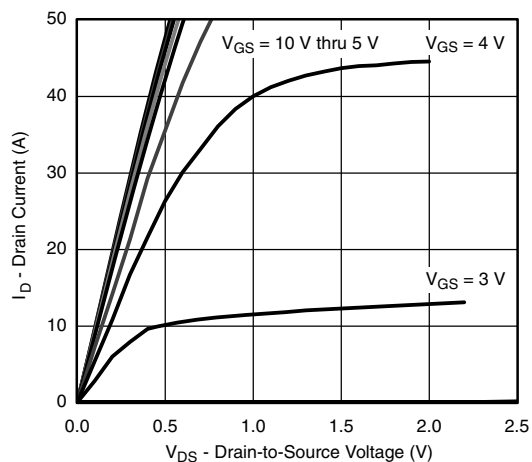
**Normalized Thermal Transient Impedance, Junction-to-Ambient**



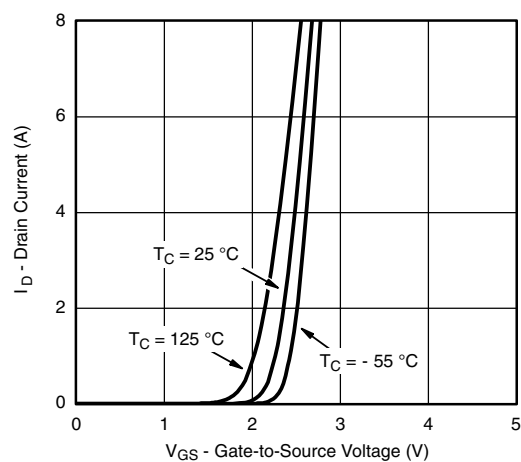
**Normalized Thermal Transient Impedance, Junction-to-Case**



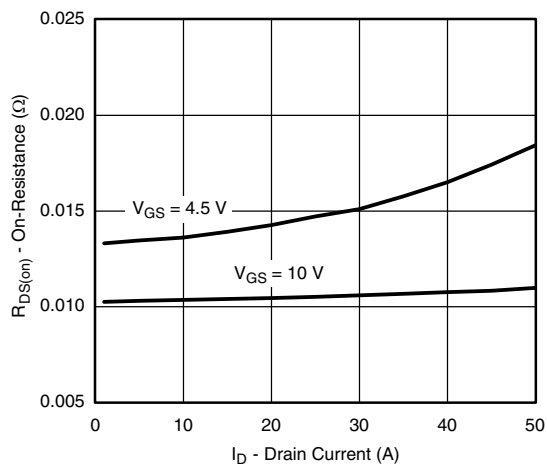
**CHANNEL-2 TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



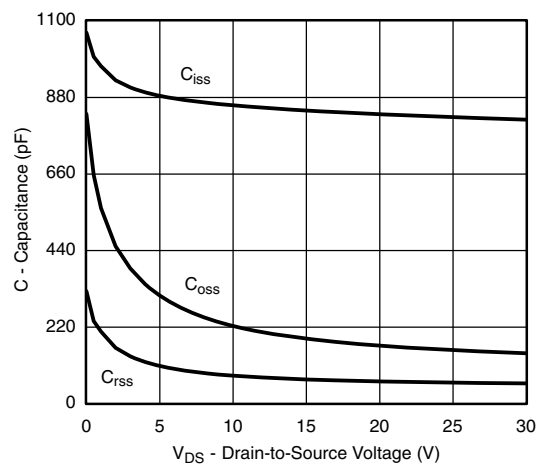
**Output Characteristics**



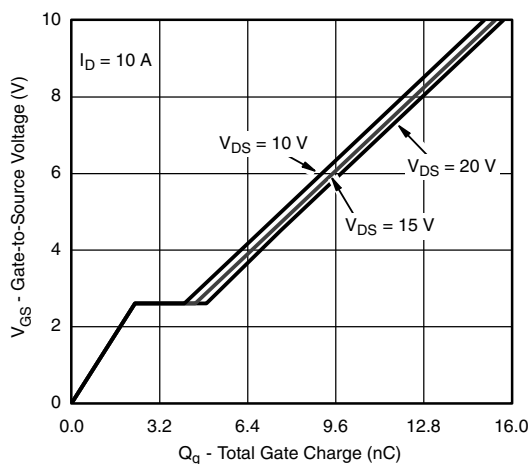
**Transfer Characteristics**



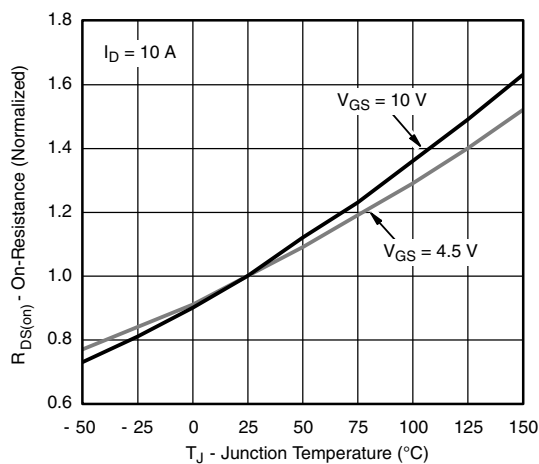
**On-Resistance vs. Drain Current**



**Capacitance**



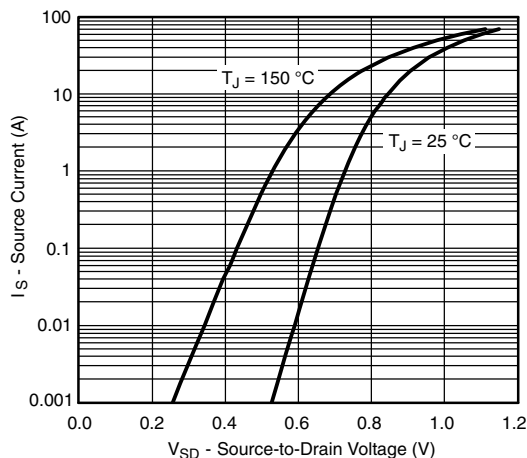
**Gate Charge**



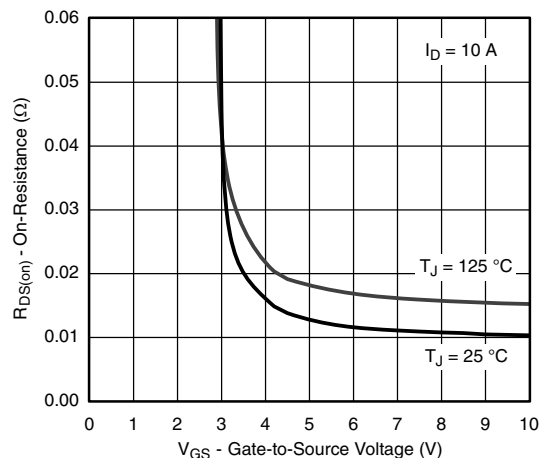
**On-Resistance vs. Junction Temperature**



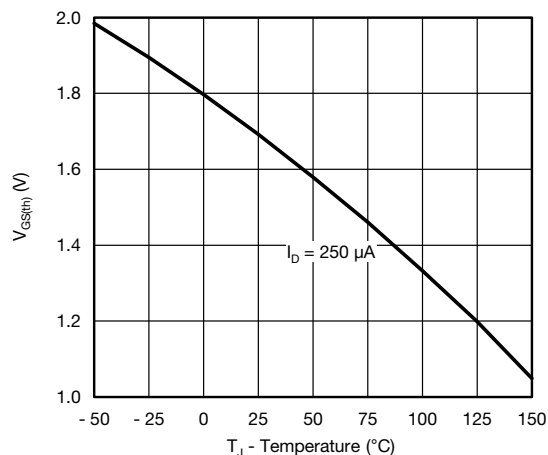
**CHANNEL-2 TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



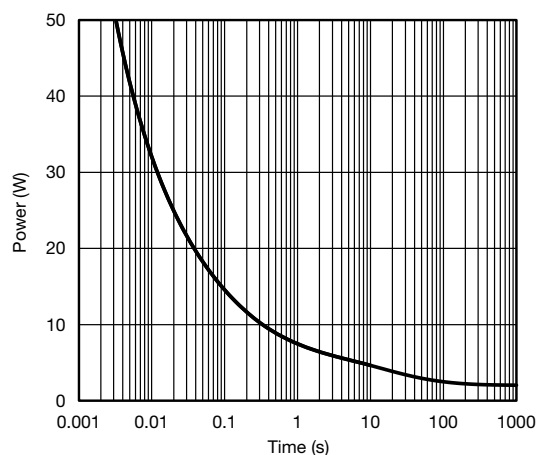
**Source-Drain Diode Forward Voltage**



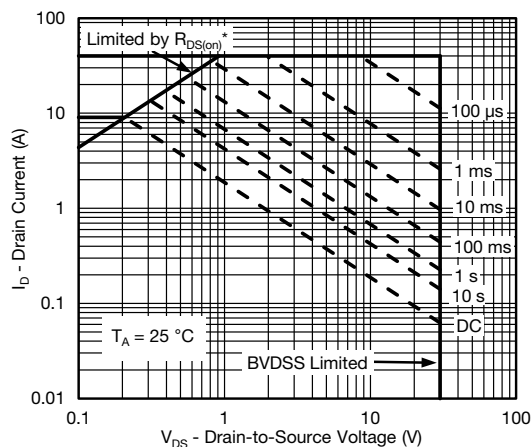
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**

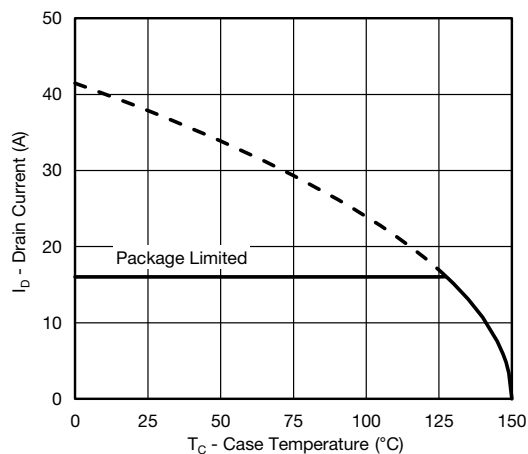
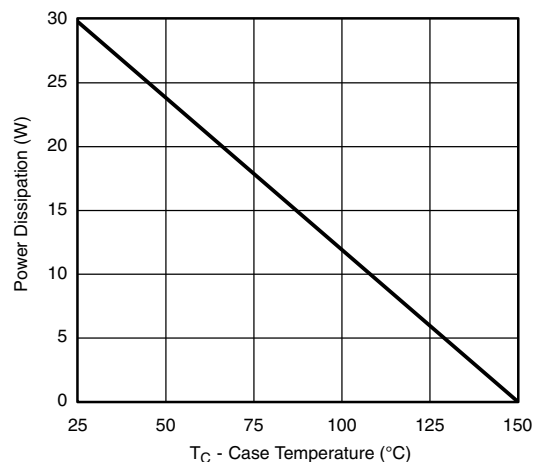


**Single Pulse Power**



**Safe Operating Area, Junction-to-Ambient**

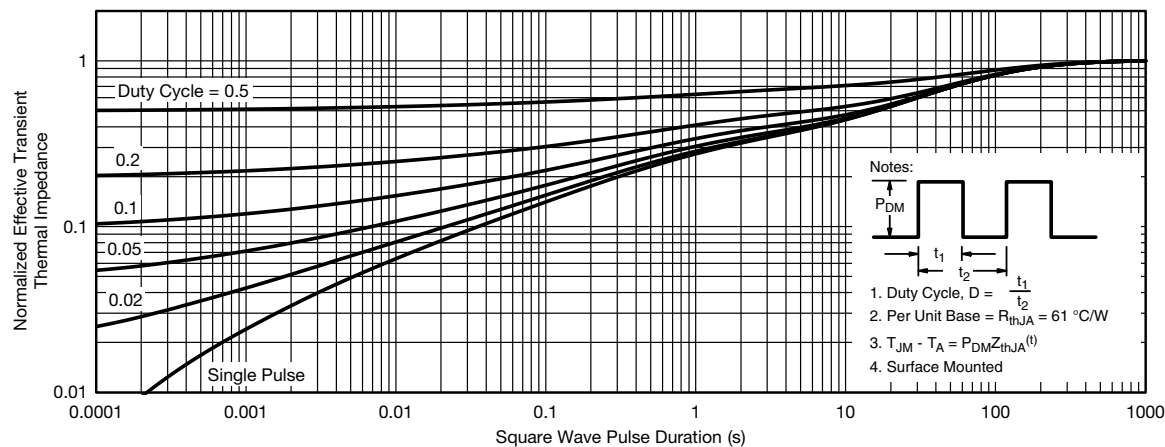


**CHANNEL-2 TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Current Derating <sup>a</sup>**

**Power, Junction-to-Case**
**Note**

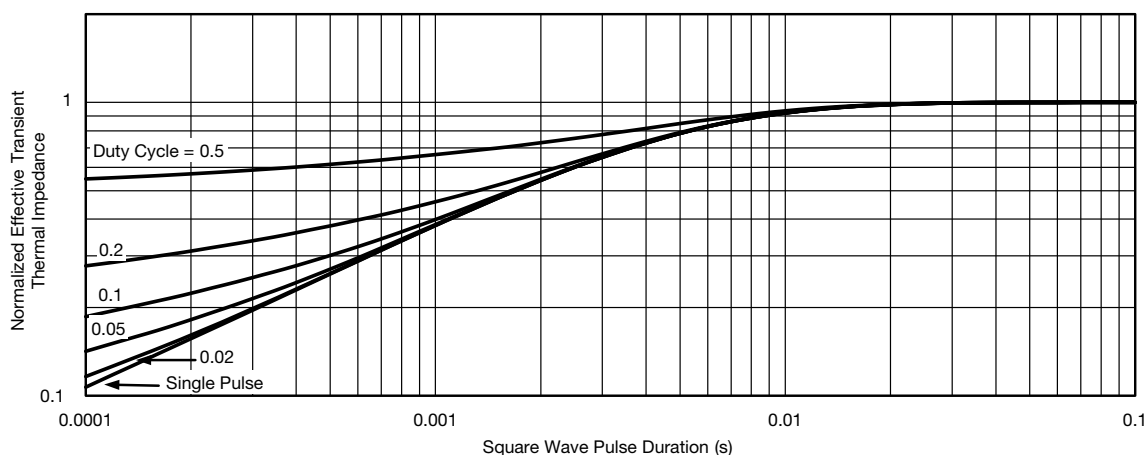
- a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



**CHANNEL-2 TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



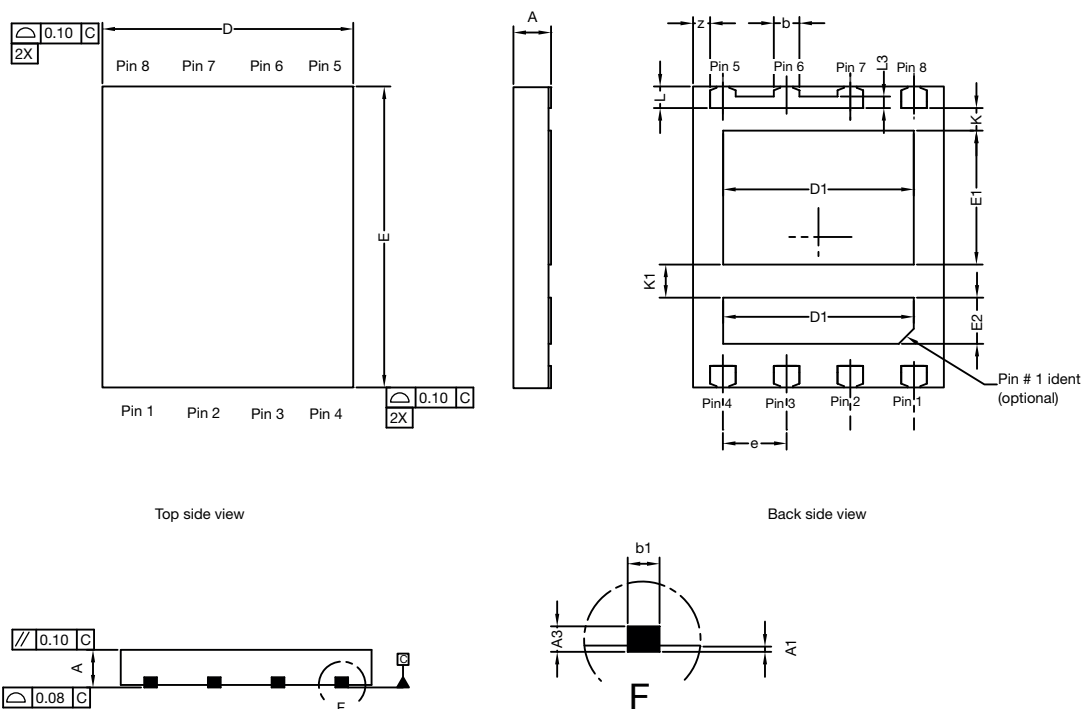
**Normalized Thermal Transient Impedance, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Case**

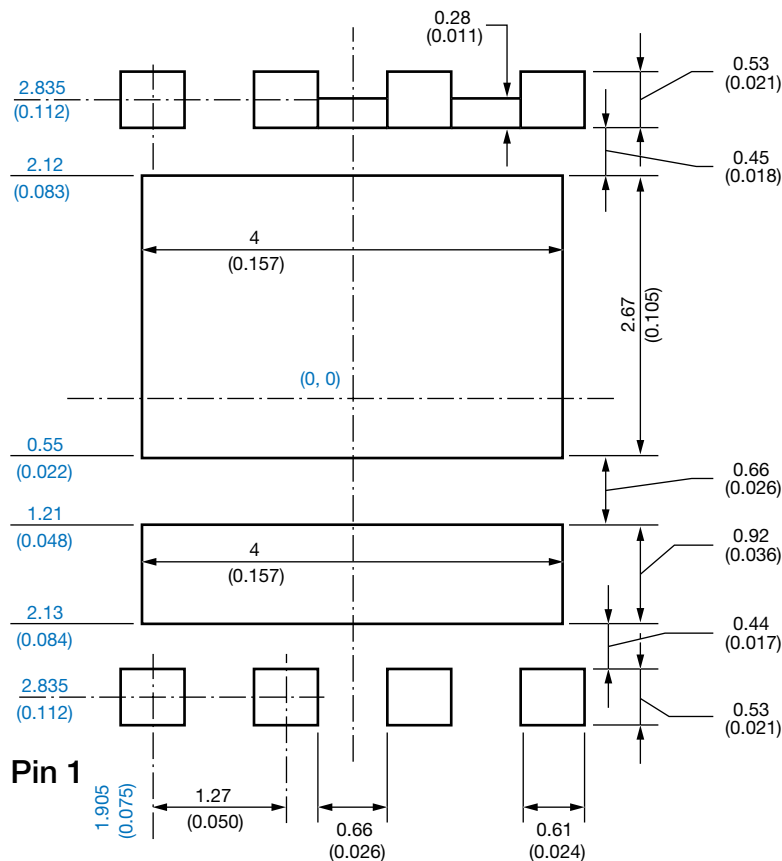
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## PowerPAIR® 6 x 5 Case Outline



DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.70	0.75	0.80	0.028	0.030	0.032
A1	0.00	-	0.10	0.000	-	0.004
A3	0.15	0.20	0.25	0.006	0.007	0.009
b	0.43	0.51	0.61	0.017	0.020	0.024
b1	0.25 BSC			0.010 BSC		
D	4.90	5.00	5.10	0.192	0.196	0.200
D1	3.75	3.80	3.85	0.148	0.150	0.152
E	5.90	6.00	6.10	0.232	0.236	0.240
E1 Option AA (for W/B)	2.62	2.67	2.72	0.103	0.105	0.107
E1 Option AB (for BWL)	2.42	2.47	2.52	0.095	0.097	0.099
E2	0.87	0.92	0.97	0.034	0.036	0.038
e	1.27 BSC			0.050 BSC		
K Option AA (for W/B)	0.45 typ.			0.018 typ.		
K Option AB (for BWL)	0.65 typ.			0.025 typ.		
K1	0.66 typ.			0.025 typ.		
L	0.33	0.43	0.53	0.013	0.017	0.020
L3	0.23 BSC			0.009 BSC		
z	0.34 BSC			0.013 BSC		
ECN: T14-0782-Rev. C, 22-Dec-14						
DWG: 6005						

# Recommended Minimum PAD for PowerPAIR® 6 x 5


**Note**

- Linear dimensions are in black, the same information is provided in ordinate dimensions which are in blue.



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