March 2014



MMBTA92 / PZTA92 PNP High-Voltage Amplifier

Description

This device is designed for high-voltage driver applications. Sourced from process 76.

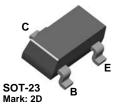


Figure 1. MMBTA92 Device Package

Ordering Information

Part Number	Top Mark	Package	Packing Method
MMBTA92	2D	SOT-23 3L	Tape and Reel
PZTA92	A92	SOT-223 4L	Tape and Reel

SOT-223

Figure 2. PZTA92 Device Package

Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	-300	V
V _{CBO}	Collector-Base Voltage	-300	V
V _{EBO}	Emitter-Base Voltage	-5	V
Ι _C	Collector Current - Continuous	-500	mA
T _{J,} T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

1. These ratings are based on a maximum junction temperature of 150°C.

2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

Thermal Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.		Unit
		MMBTA92 ⁽³⁾	PZTA92 ⁽⁴⁾	Onic
р	Total Device Dissipation	350	1000	mW
PD	Derate Above 25°C	2.8	8.0	mW/°C
R _{0JA}	Thermal Resistance, Junction to Ambient	357	125	°C/W

Notes:

3. Device is mounted on FR-4 PCB 1.6 inch x 1.6 inch x 0.06 inch.

4. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

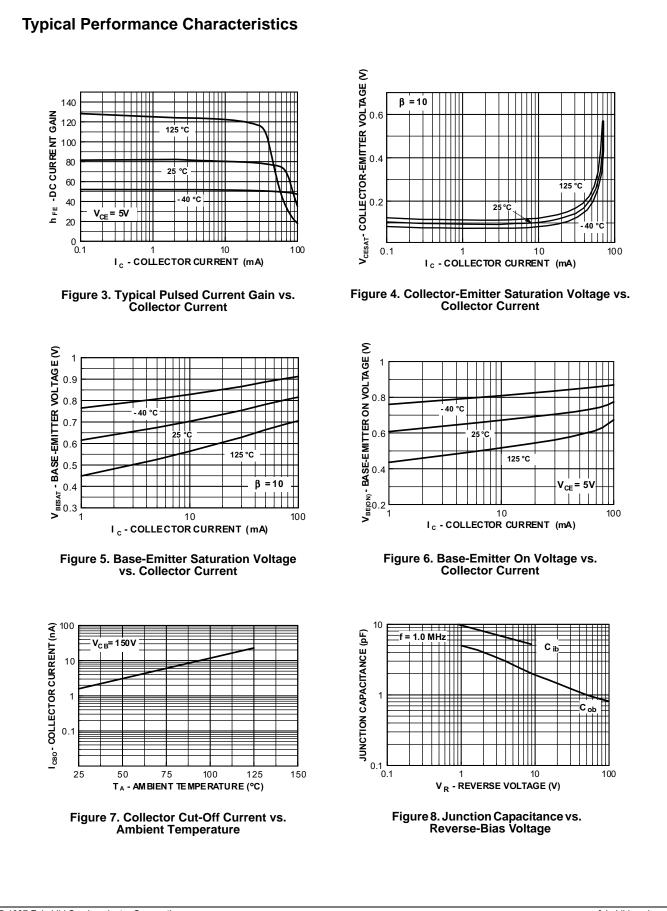
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage ⁽⁵⁾	I _C = -1.0 mA, I _B = 0	-300		V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -100 μA, I _E = 0	-300		V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -100 μA, I _C = 0	-5.0		V
I _{CBO}	Collector Cut-Off Current	V_{CB} = -200 V, I _E = 0		-0.25	μA
I _{EBO}	Emitter Cut-Off Current	V _{EB} = -3.0 V, I _C = 0		-0.1	μA
h _{FE}	DC Current Gain ⁽⁵⁾	I _C = -1.0 mA, V _{CE} = -10 V	25		
		I _C = -10 mA, V _{CE} = -10 V	40	250	
		I _C = -30 mA, V _{CE} = -10 V	25		
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽⁵⁾	I _C = -20 mA, I _B = -2.0 mA		-0.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage ⁽⁵⁾	I _C = -20 mA, I _B = -2.0 mA		-0.9	V
f _T	Current Gain - Bandwidth Product	I _C = -10 mA, V _{CE} = -20 V, f = 100 MHz	50		MHz
C _{cb}	Collector-Base Capacitance	V _{CB} = -20 V, I _E = 0, f = 1.0 MHz		6.0	pF

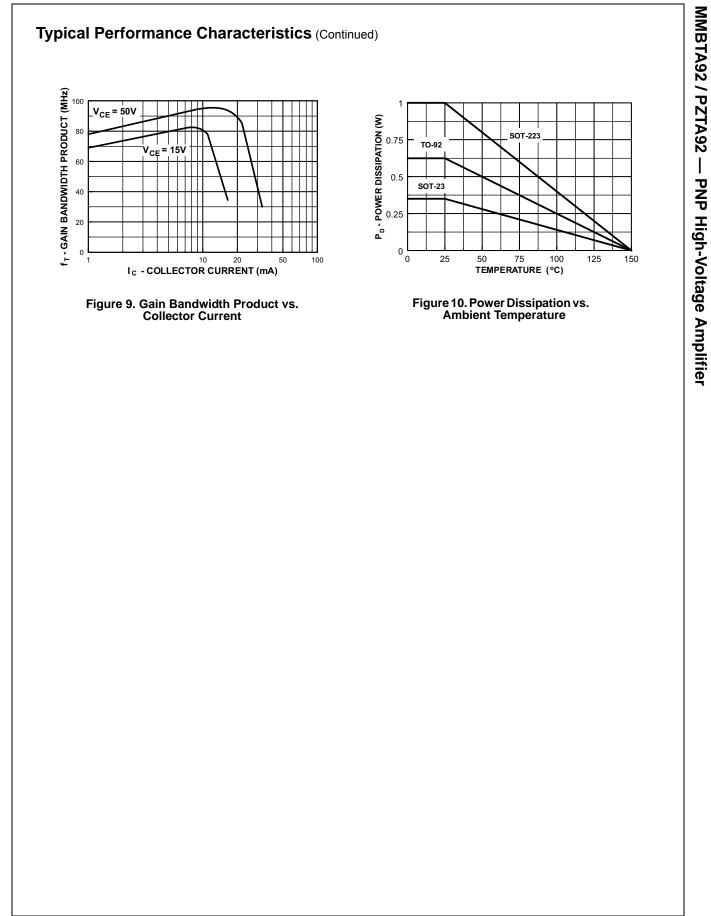
Note:

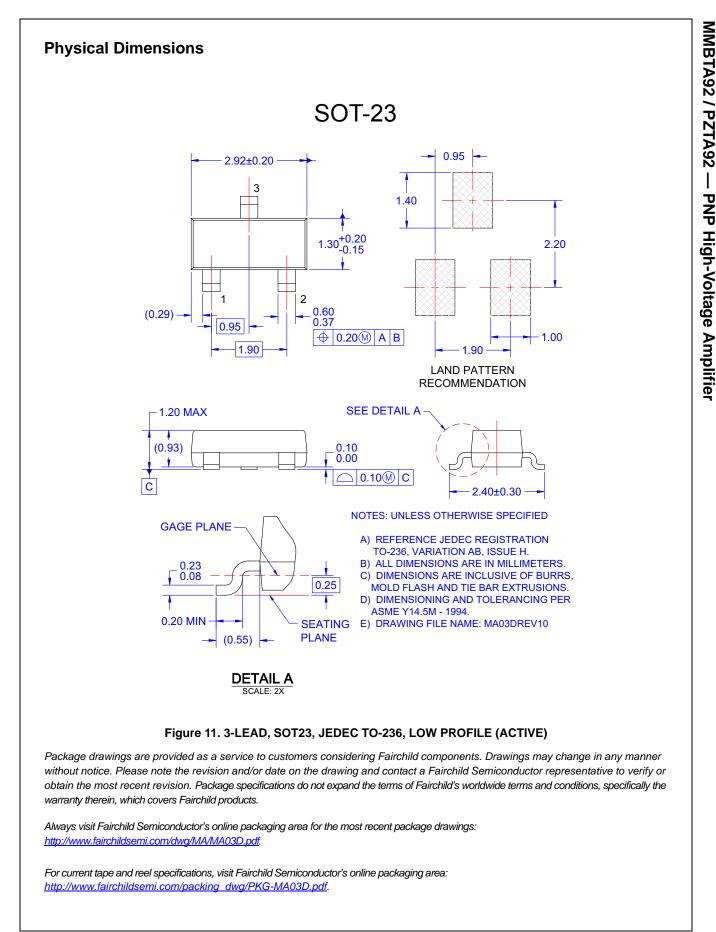
5. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2.0%.

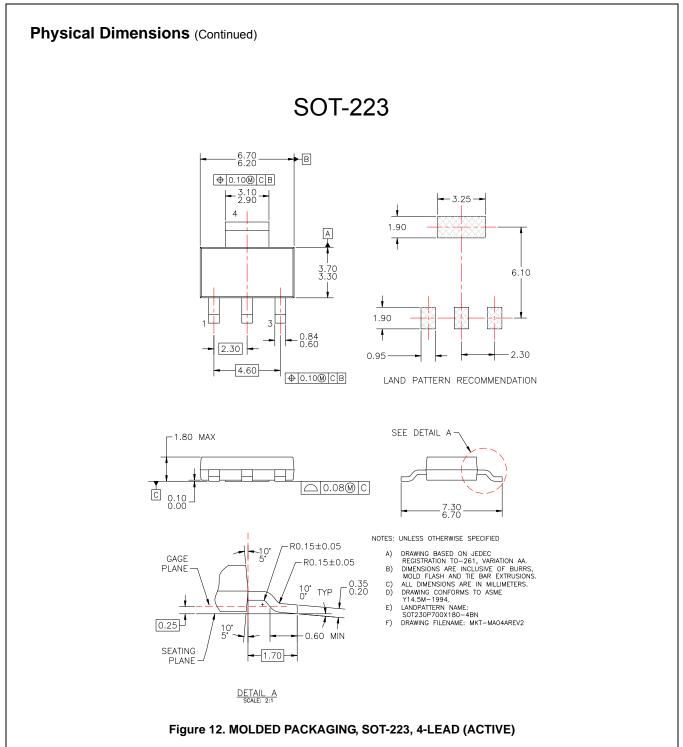


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