

INPUT CHARACTERISTICS					
Parameter	Conditions	Min.	Typ.	Max.	Units
Voltage range	Continuous operation, 5V input types	4.5	5	5.5	V
	Continuous operation, 12V input types	10.8	12	13.2	
	Continuous operation, 15V input types	13.5	15	16.5	
Reflected ripple current	NMV0505TSAC & NMV0512TSC		5		mA p-p
	All other output types		20	40	

OUTPUT CHARACTERISTICS					
Parameter	Conditions	Min.	Typ.	Max.	Units
Rated Power	T _A =-40°C to 120°C, see derating graph			1	W
Voltage Set Point Accuracy	See tolerance envelope				
Line regulation	High V _{IN} to low V _{IN}	All output types	1.0	1.2	%/%
		NMV0505TSAC & NMV0512TSC	1.1	1.2	

ISOLATION CHARACTERISTICS					
Parameter	Conditions	Min.	Typ.	Max.	Units
Isolation test voltage	Flash tested for 1 minute	3000			VDC
Resistance	Viso= 1000VDC	10			GΩ

GENERAL CHARACTERISTICS					
Parameter	Conditions	Min.	Typ.	Max.	Units
Switching frequency	NMV0505TSAC		55		kHz
	NMV0512TSC		60		
	5V input types		120	135	
	12V input types		150	170	
	15V input types		90	110	

TEMPERATURE CHARACTERISTICS					
Parameter	Conditions	Min.	Typ.	Max.	Units
Specification	NMV0505TSAC & NMV0512TSC	-40		105	°C
	All other output types	-40		85	
Storage		-50		125	
Case Temperature above ambient	NMV0505TSAC		16		
	NMV0512TSC		14		
	5V output types			28	
	All other output types			25	
Cooling	Free air convection				

ABSOLUTE MAXIMUM RATINGS	
Lead temperature 1.5mm from case for 10 seconds	260°C
Wave Solder	Wave Solder profile not to exceed the profile recommended in IEC 61760-1 Section 6.1.3. Please refer to application notes for further information.
Input voltage V _{IN} , NMV05 types	7V
Input voltage V _{IN} , NMV12 types	15V
Input voltage V _{IN} , NMV15 types	18V

TECHNICAL NOTES

ISOLATION VOLTAGE

¹ 'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions NMV series of DC-DC converters are all 100% production tested at their stated isolation voltage. This is 3kVDC for 1 minute.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

The NMV has been recognised by Underwriters Laboratory for functional insulation, both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. The NMV series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enamelled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognised parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

SAFETY APPROVAL

The NMV series has been recognised by Underwriters Laboratory (UL) to UL 60950 for functional insulation. The NMV0505TSAC & NMV0512TSC are pending recognition to UL62368-1.

The NMV Series of converters are not internally fused so to meet the requirements of UL 60950 an anti-surge input line fuse should always be used with ratings as defined below.

NMV05xxxxC: 0.5A

NMV12xxxxC: 0.2A

NMV15xxxxC: 0.2A

All fuses should be UL recognised and rated to at least the maximum allowable DC input voltage.

File number E151252 applies.

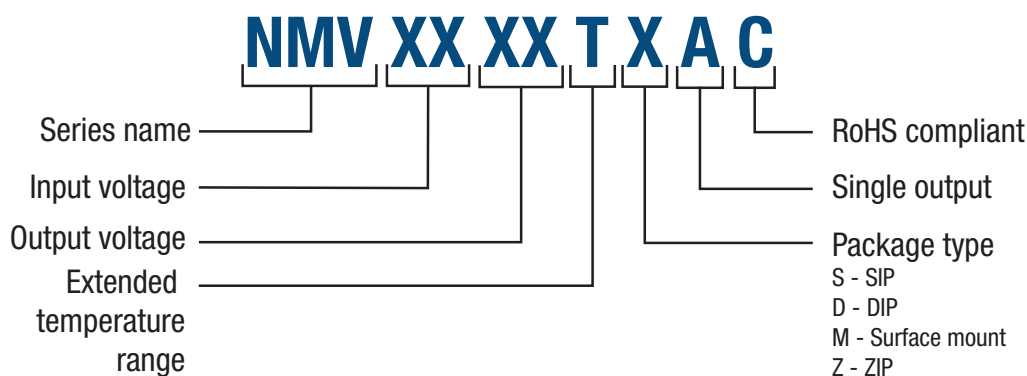
RoHS COMPLIANCE INFORMATION



This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. Please refer to [application notes](#) for further information. The pin termination finish on the SIP package type is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. The DIP types are Matte Tin over Nickel Preplate. Both types in this series are backward compatible with Sn/Pb soldering systems.

For further information, please visit www.murata-ps.com/rohs

PART NUMBER STRUCTURE



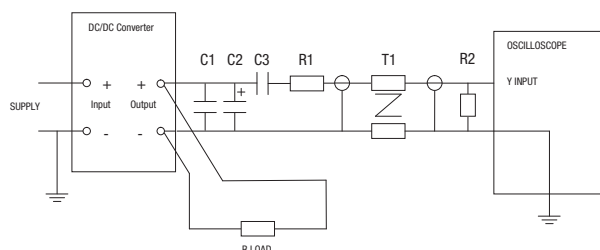
CHARACTERISATION TEST METHODS

Ripple & Noise Characterisation Method

Ripple and noise measurements are performed with the following test configuration.

C1	1 μ F X7R multilayer ceramic capacitor, voltage rating to be a minimum of 3 times the output voltage of the DC-DC converter
C2	10 μ F tantalum capacitor, voltage rating to be a minimum of 1.5 times the output voltage of the DC-DC converter with an ESR of less than 100m Ω at 100 kHz
C3	100nF multilayer ceramic capacitor, general purpose
R1	450 Ω resistor, carbon film, $\pm 1\%$ tolerance
R2	50 Ω BNC termination
T1	3T of the coax cable through a ferrite toroid
RLOAD	Resistive load to the maximum power rating of the DC-DC converter. Connections should be made via twisted wires
Measured values are multiplied by 10 to obtain the specified values.	

Differential Mode Noise Test Schematic



APPLICATION NOTES

Minimum load

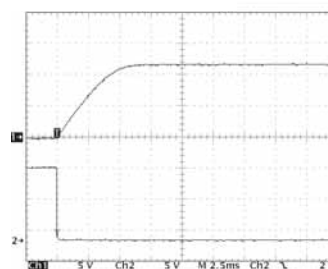
The minimum load to meet datasheet specification is 10% of the full rated load across the specified input voltage range. Lower than 10% minimum loading will result in an increase in output voltage, which may rise to typically double the specified output voltage if the output load falls to less than 5%.

Capacitive loading and start up

Typical start up times for this series, with a typical input voltage rise time of 2.2 μ s and output capacitance of 10 μ F, are shown in the table below. The product series will start into a capacitance of 47 μ F with an increased start time, however, the maximum recommended output capacitance is 10 μ F.

Typical Start-Up Wave Form

Start-up time		Start-up time	
	μ s		μ s
NMV0505TSAC	215	NMV1209xC	4140
NMV0505xC	1966	NMV1212xC	8650
NMV0509xC	5360	NMV1215xC	11171
NMV0512xC	11180	NMV1505xC	803
NMV0515xC	16270	NMV1512xC	3510
NMV0512TSC	2300	NMV1515xC	8361
NMV1205xC	1290		



APPLICATION NOTES (Continued)

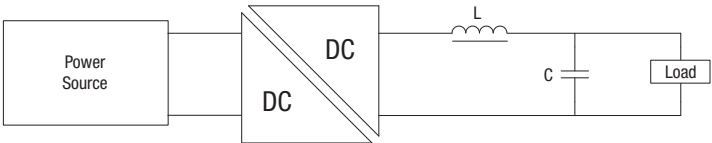
Output Ripple Reduction

By using the values of inductance and capacitance stated, the output ripple at the rated load is lowered to 5mV p-p max.

Component selection

Capacitor: It is required that the ESR (Equivalent Series Resistance) should be as low as possible, ceramic types are recommended. The voltage rating should be at least twice (except for 15V output), the rated output voltage of the DC-DC converter.

Inductor: The rated current of the inductor should not be less than that of the output of the DC-DC converter. At the rated current, the DC resistance of the inductor should be such that the voltage drop across the inductor is <2% of the rated voltage of the DC-DC converter. The SRF (Self Resonant Frequency) should be >20MHz.

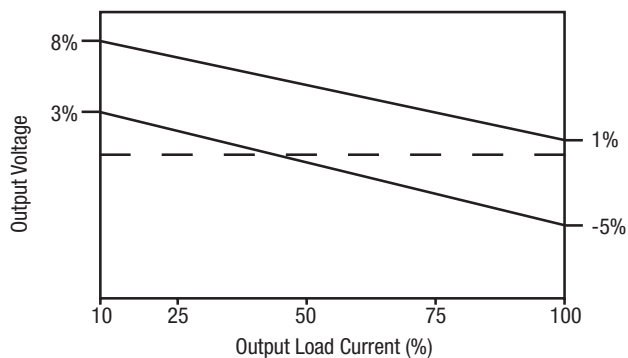


	Inductor			Capacitor
	L, μ H	SMD	Through Hole	C, μ F
NMV0505TSAC	22	84223C	11R223C	22
NMV0505xC	22	82223C	11R223C	1
NMV0509xC	100	82104C	11R104C	0.47
NMV0512TSC	10	82103C	11R103C	22
NMV0512xC	150	82154C	11R154C	0.33
NMV0515xC	220	82224C	11R224C	0.22
NMV1205xC	22	82223C	11R223C	2.2
NMV1209xC	100	82104C	11R104C	1
NMV1212xC	150	82154C	11R154C	0.33
NMV1215xC	220	82224C	11R224C	0.22
NMV1505xC	22	82223C	11R223C	1
NMV1512xC	150	82154C	11R154C	0.33
NMV1515xC	220	82224C	11R224C	0.22

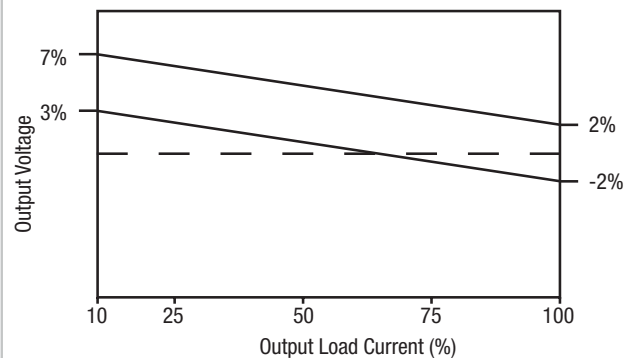
TOLERANCE ENVELOPES

The voltage tolerance envelope shows typical load regulation characteristics for this product series. The tolerance envelope is the maximum output voltage variation due to changes in output loading.

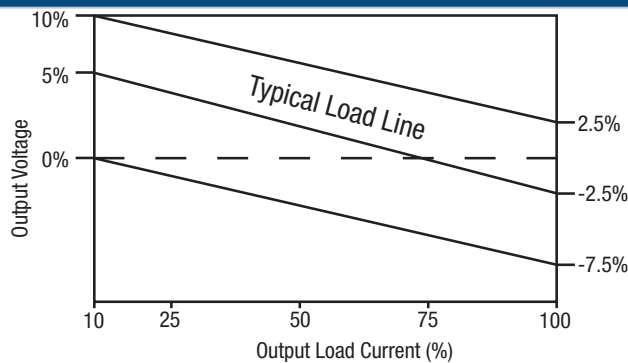
NMV0505TSAC



NMV0512TSC

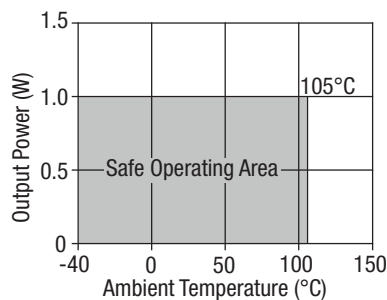


All other variants

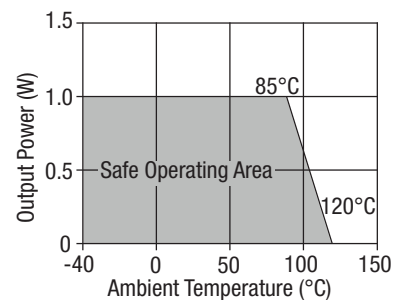


TEMPERATURE DERATING GRAPH

NMV0505TSAC & NMV0512TSC:

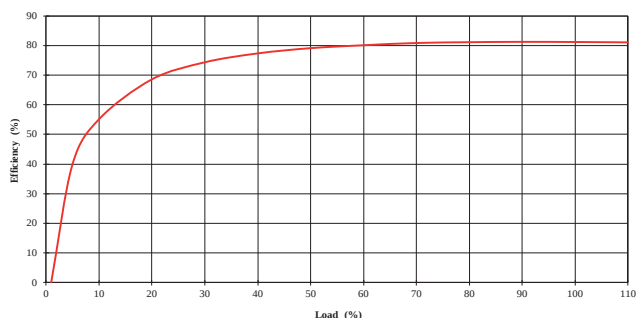


All other variants:

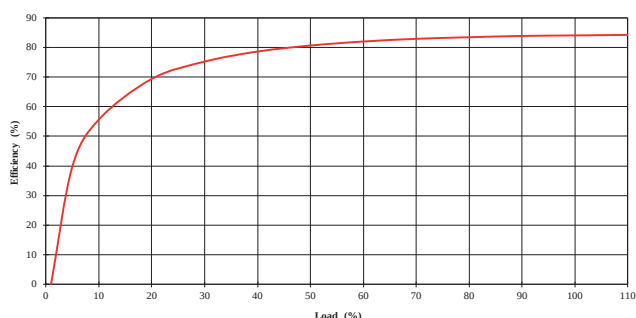


EFFICIENCY VS LOAD

NMV0505TSAC



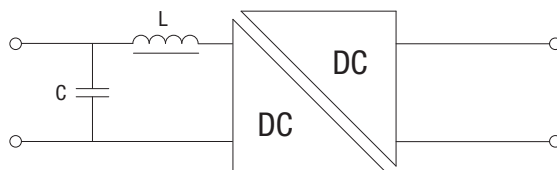
NMV0512TSC



EMC FILTERING AND SPECTRA

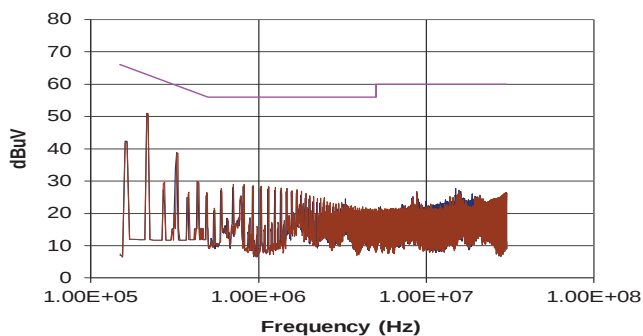
FILTERING

The following filter circuit and filter table shows the input filters typically required to meet conducted emissions limits for EN 55022 curve B using Quasi-Peak and average detectors according to CISPR 22.

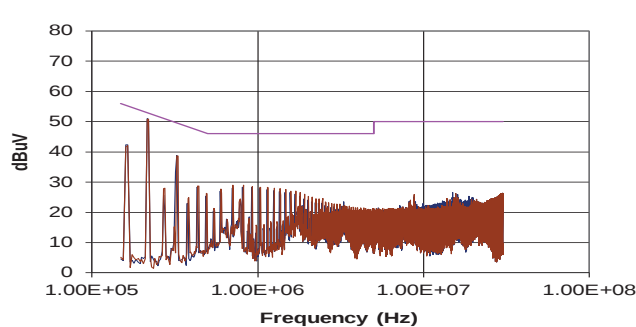


Part Number	Inductor		Capacitor	
	L, μ H	SMD	C, μ F	SMD
NMV0505TSAC	10 μ H	23100C	2.2 μ F	GRM188C71E225KE11D
NMV0512TSC	10 μ H	23100C	2.2 μ F	GRM188C71E225KE11D

NMV0505TSAC (Quasi-Peak)

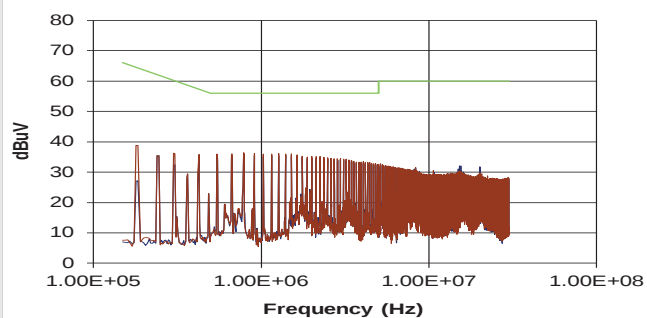


NMV0505TSAC (Average)

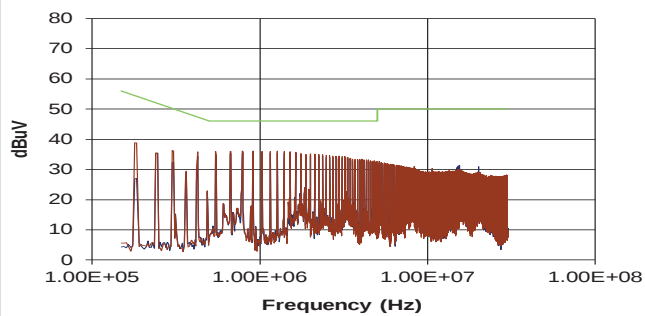


EMC FILTERING AND SPECTRA (Continued)

NMV0512TSC (Quasi-Peak)



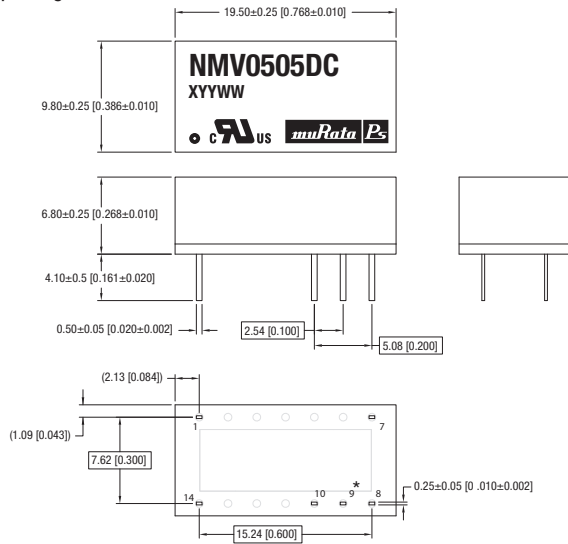
NMV0512TSC (Average)



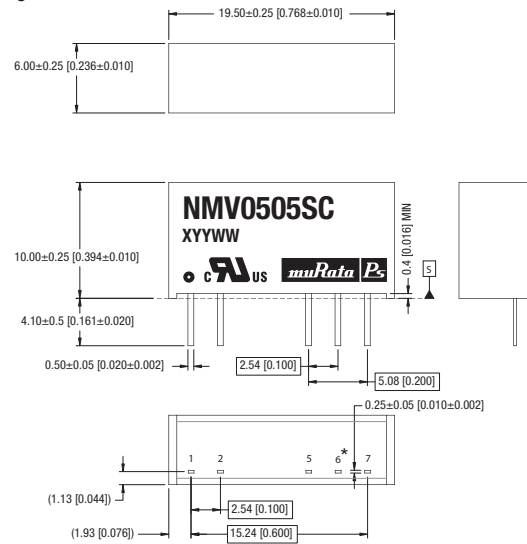
PACKAGE SPECIFICATIONS

MECHANICAL DIMENSIONS

DIP package



SIP package



* Pin not fitted on single output variants.
All dimensions in mm (inches) Controlling dimension is mm.
All pins on a 2.54 (0.100) pitch and within ±0.1 (0.004) of true position from pin 1 at seating plane 'S'

Weight: 2.4g (DIP) 2.1g (SIP)

PIN CONNECTIONS

Single output variants

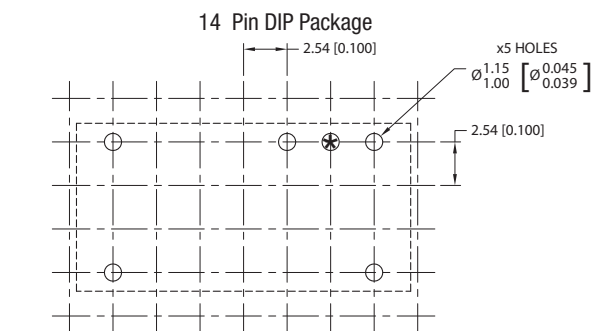
14 Pin DIP		7 Pin SIP	
Pin	Function	Pin	Function
1	-V _{IN}	1	+V _{IN}
7	NC	2	-V _{IN}
8	+V _{OUT}	5	-V _{OUT}
10	-V _{OUT}	7	+V _{OUT}
14	+V _{IN}		

Dual output variants

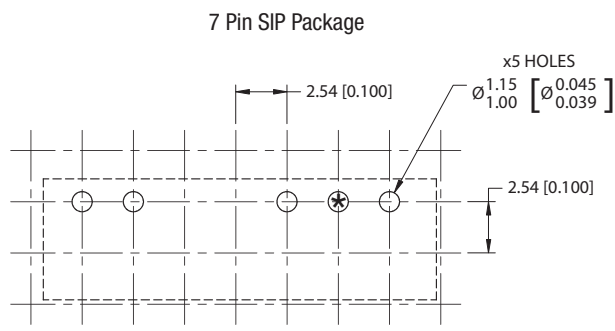
14 Pin DIP		7 Pin SIP	
Pin	Function	Pin	Function
1	-V _{IN}	1	+V _{IN}
7	NC	2	-V _{IN}
8	+V _{OUT}	5	-V _{OUT}
9	OV	6	OV
10	-V _{OUT}	7	+V _{OUT}
14	+V _{IN}		

PACKAGE SPECIFICATIONS (Continued)

RECOMMENDED FOOTPRINT DETAILS

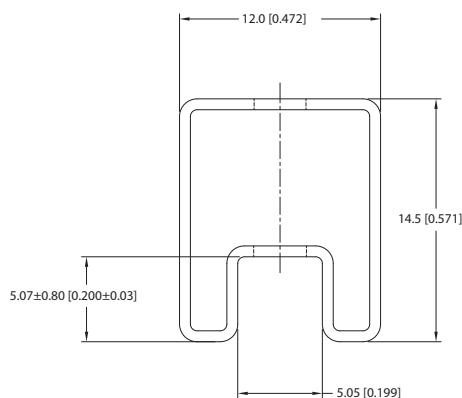


*Hole not required for single output variants.

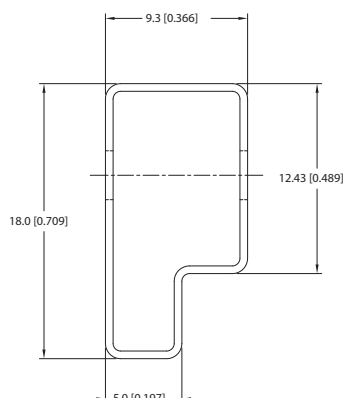


TUBE OUTLINE DIMENSIONS

14 Pin DIP Tube



7 Pin SIP Tube



Unless otherwise specified all dimensions in mm [inches] $\pm 0.55\text{mm}$ [0.022]. Tube length
 (14 Pin DIP) : 520mm [20.472] ± 2.0 [0.079].
 Tube length (7 Pin SIP) : 520mm [20.472] ± 2.0 [0.079].

Tube Quantity : 25

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Refer to: <https://www.murata.com/en-eu/products/power/requirements>

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