

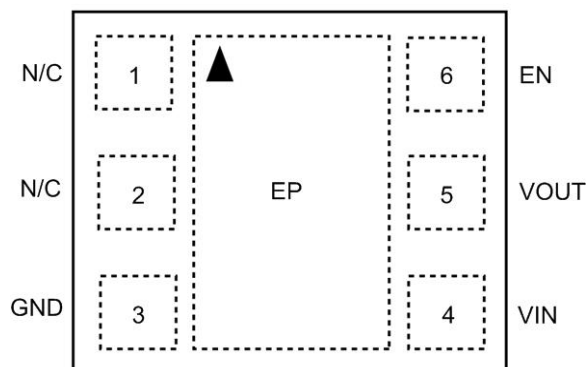
Ordering Information

Part Number	Marking Code	Voltage ⁽¹⁾	Junction Temperature Range	Package ⁽²⁾	Lead Finish ⁽³⁾
MIC5363-1.2YMT	34	1.2	−40°C to +125°C	6-Pin 1.2 mm × 1.2mm Thin MLF [®]	Pb-Free
MIC5363-2.8YMT	3G	2.8	−40°C to +125°C	6-Pin 1.2 mm × 1.2mm Thin MLF [®]	Pb-Free
MIC5363-3.3YMT	3S	3.3	−40°C to +125°C	6-Pin 1.2 mm × 1.2mm Thin MLF [®]	Pb-Free
MIC5364-1.2YMT ⁽⁴⁾	44	1.2	−40°C to +125°C	6-Pin 1.2 mm × 1.2mm Thin MLF [®]	Pb-Free
MIC5364-2.8YMT ⁽⁴⁾	4X	2.8	−40°C to +125°C	6-Pin 1.2 mm × 1.2mm Thin MLF [®]	Pb-Free
MIC5364-3.3YMT ⁽⁴⁾	4S	3.3	−40°C to +125°C	6-Pin 1.2 mm × 1.2mm Thin MLF [®]	Pb-Free

Notes:

- Other voltages available. Contact Micrel for details.
- “▲” = Pin 1 identifier.
- Green, RoHS-compliant. Level finish is NiPdAu. Mold compound is Halogen free.
- MIC5364 offers Auto-Discharge function.

Pin Configuration



6-Pin 1.2mm × 1.2mm Thin MLF[®] (MT)

Pin Description

Pin Number	Pin Name	Pin Function
1	N/C	Not internally connected.
2	N/C	Not internally connected.
3	GND	Ground.
4	VIN	Supply Input. Decouple with 1μF ceramic capacitor.
5	VOUT	Output Voltage. Decouple with 1μF ceramic capacitor.
6	EN	Enable Input. Active HIGH. Logic HIGH = ON, logic LOW = OFF. Do not leave floating.
EP	ePad	Exposed Heatsink Pad. Connect to ground for best thermal performance.

Absolute Maximum Ratings⁽¹⁾

Supply Voltage (V_{IN})..... -0.3V to +6V
 Enable Voltage (V_{EN})..... -0.3V to $V_{IN} + 0.3V$
 Power Dissipation (P_D)..... Internally Limited⁽³⁾
 Lead Temperature (soldering, 10s)..... 260°C
 Junction Temperature (T_J)..... -40°C to +125°C
 Storage Temperature (T_S)..... -65°C to +150°C
 ESD Rating⁽⁴⁾..... 2kV

Operating Ratings⁽²⁾

Supply Voltage (V_{IN})..... +2.5V to +5.5V
 Enable Voltage (V_{EN})..... 0V to V_{IN}
 Junction Temperature (T_J)..... -40°C to +125°C
 Junction Thermal Resistance
 1.2mm × 1.2mm Thin MLF® (θ_{JA})..... 173°C/W

Electrical Characteristics⁽⁵⁾

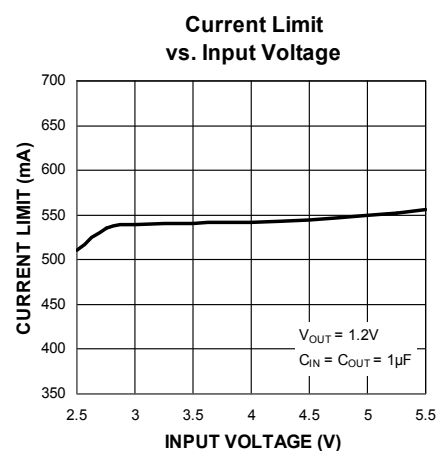
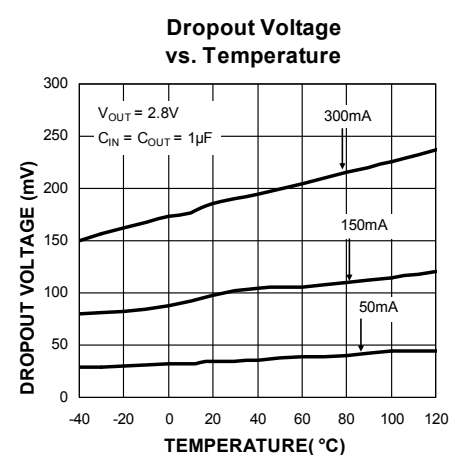
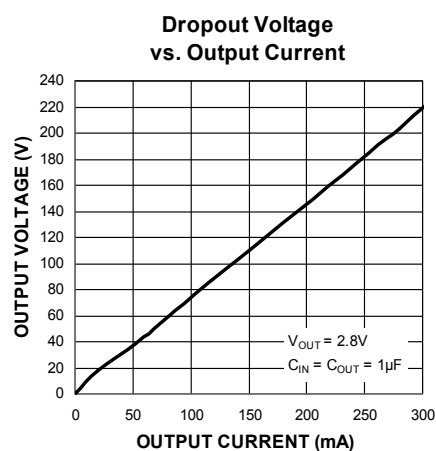
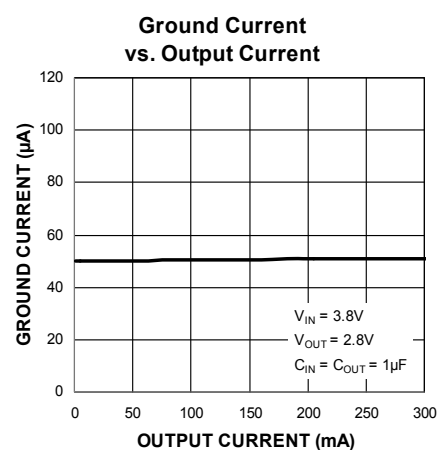
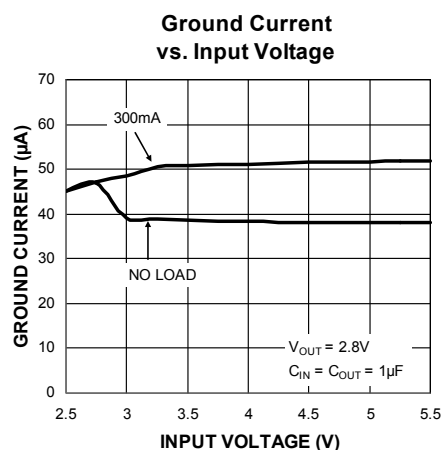
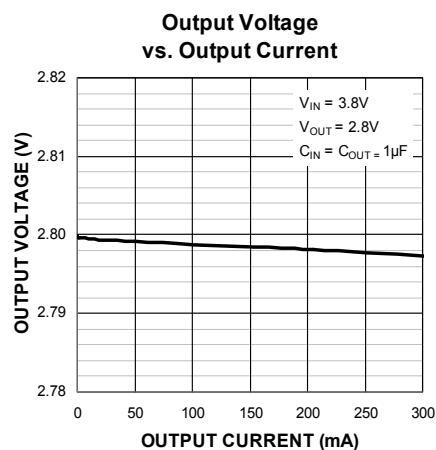
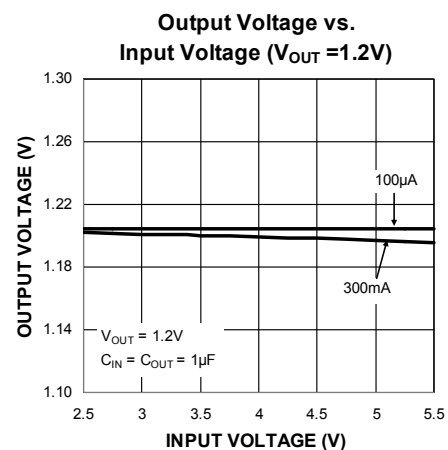
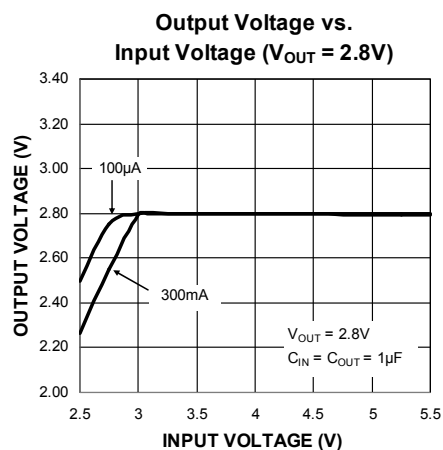
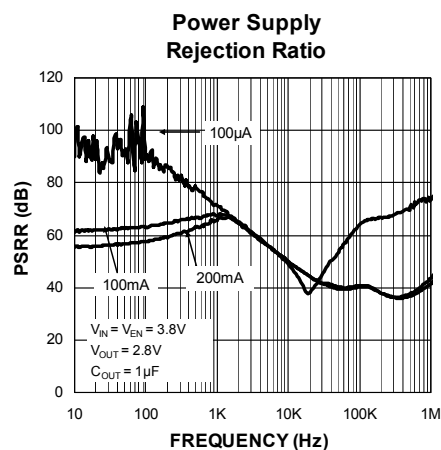
$V_{IN} = V_{EN} = V_{OUT} + 1V$, 2.5V if $V_{OUT} \leq 1.5V$; $I_{OUT} = 100\mu A$; $C_{OUT} = 1\mu F$; $T_J = 25^\circ C$, **bold** values indicate -40°C to +125°C, unless noted.

Parameter	Condition	Min.	Typ.	Max.	Units
Output Voltage Accuracy	Variation from nominal V_{OUT}	-2.0		2.0	%
	Variation from nominal V_{OUT} , -40°C to +125°C	-3.0		3.0	
Line Regulation ⁽⁶⁾	$V_{IN} = \text{Max}(V_{OUT} + 1V, 2.5V)$ to 5.5V, $I_{OUT} = 100\mu A$		0.02	0.2	%/V
Load Regulation	$I_{OUT} = 100\mu A$ to 300mA		0.3	0.7	%
Dropout Voltage	$I_{OUT} = 50mA$; $V_{OUT} \geq 2.8V$		35	65	mV
	$I_{OUT} = 300mA$; $V_{OUT} \geq 2.8V$		225	380	
	$I_{OUT} = 50mA$; $2.5V \leq V_{OUT} < 2.8V$		55	100	
	$I_{OUT} = 300mA$; $2.5V \leq V_{OUT} < 2.8V$		300	600	
Ground Pin Current	$V_{EN} = \text{High}$; $I_{OUT} = 0mA$		38	53	μA
	$V_{EN} = \text{High}$; $I_{OUT} = 300mA$		55	70	
Shutdown Current	$V_{EN} = 0V$		0.1	1	μA
Supply Ripple Rejection	$f = 1kHz$; $C_{OUT} = 1\mu F$		70		dB
Current Limit	$V_{OUT} = 0V$	325	520	680	mA
Output Voltage Noise	$C_{OUT} = 1\mu F$; 10Hz to 100kHz		200		μV_{RMS}
Auto-Discharge NFET Resistance	MIC5364 only; $V_{EN} = 0V$; $V_{IN} = 3.6V$; $I_{OUT} = -3mA$		30		Ω
Enable Inputs (EN)					
Enable Input Voltage	Logic Low			0.2	V
	Logic High	1.2			
Enable Input Current	$V_{IL} \leq 0.2V$		0.01	1	μA
	$V_{IH} \geq 1.2V$		0.01	1	
Turn-On Time	$C_{OUT} = 1\mu F$		60	150	μs

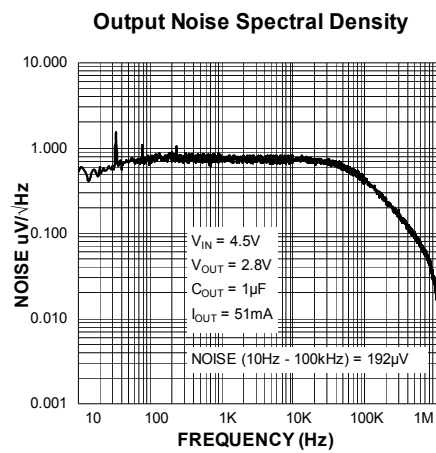
Notes:

- Exceeding the absolute maximum rating may damage the device.
- The device is not guaranteed to function outside its operating rating.
- The maximum allowable power dissipation of any T_A (ambient temperature) is $P_{D(max)} = (T_{J(max)} - T_A) / \theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
- Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5k Ω in series with 100pF.
- Specification for packaged product only.
- Regulation is measured at constant junction temperature using low duty cycle pulse testing; changes in output voltage due to heating effects are covered by the thermal regulation specification.
- Dropout voltage is defined as the input-to-output differential at which the output voltage drops 2% below its nominal value.

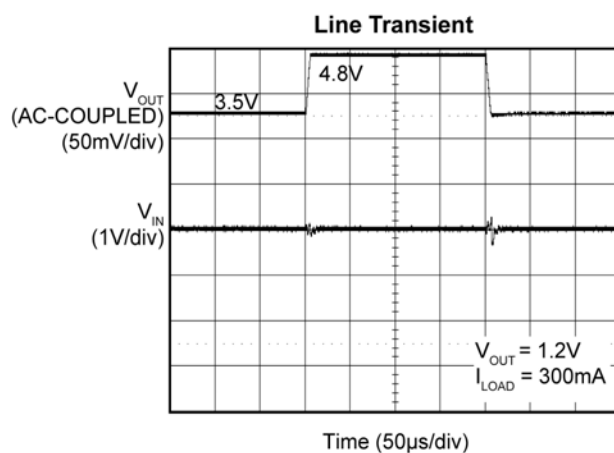
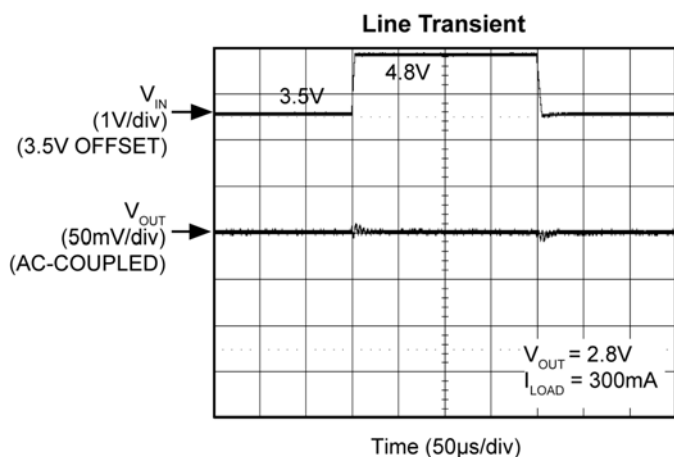
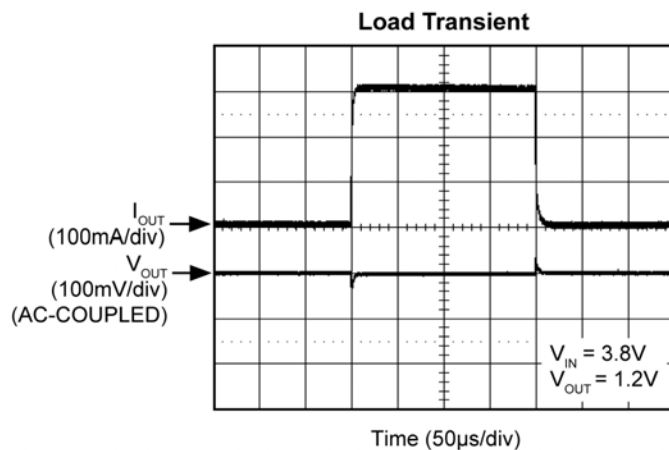
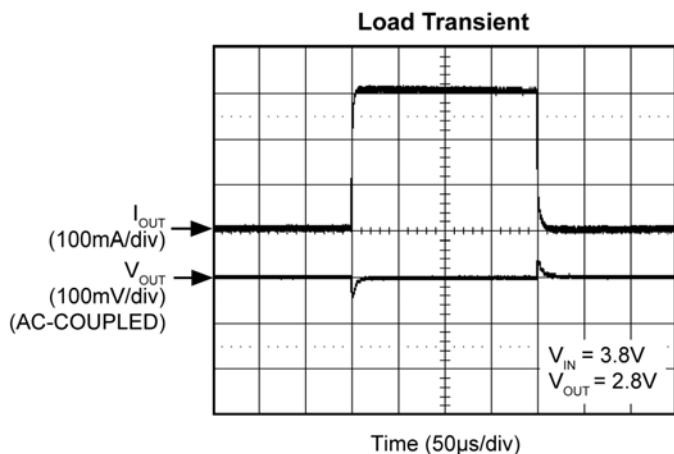
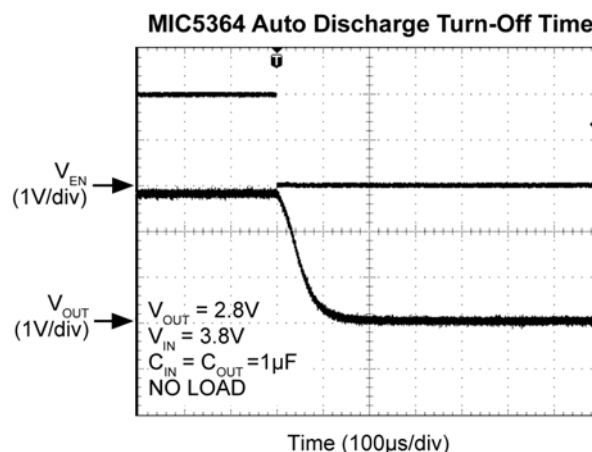
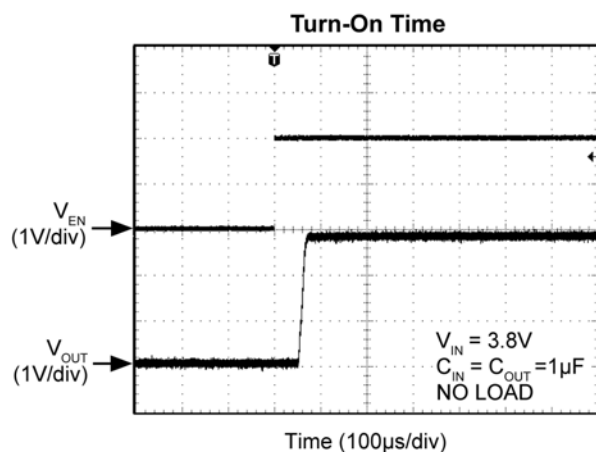
Typical Characteristics



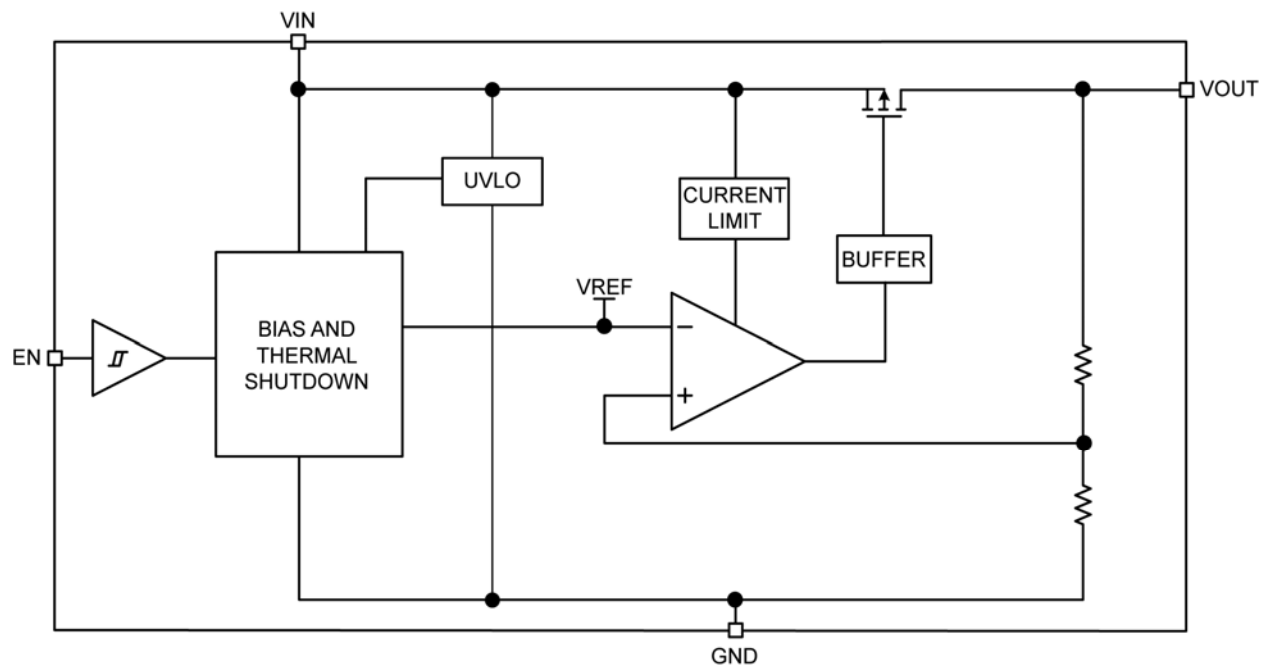
Typical Characteristics (Continued)



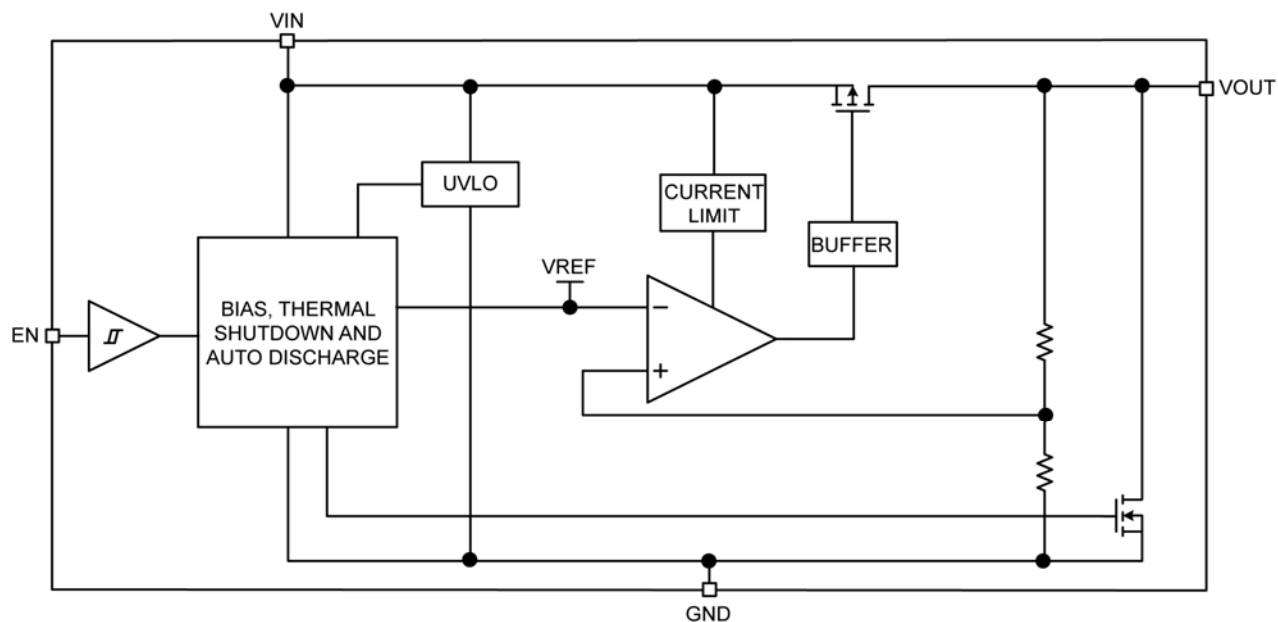
Functional Characteristics



Functional Diagram



MIC5363 Block Diagram



MIC5364 Block Diagram

Application Information

The MIC5363/4 is a 300mA LDO, packaged in a 1.2mm x 1.2mm Thin MLF[®] package. The MIC5364 includes an auto-discharge feature which automatically discharges the output capacitor when the output is disabled. The MIC5363/4 consists of an internal reference, error amplifier, P-channel pass transistor, and internal feedback resistors. The error amplifier compares the feedback voltage with that of the reference. Depending upon whether the feedback is lower or higher than the reference determines whether the gate of the pass transistor is pulled low to allow more current and increase output voltage or pulled high to reduce current. The MIC5363/4 regulator is fully protected from damage due to fault conditions through linear current limiting and thermal shutdown.

Input Capacitor

The MIC5363/4 is a high-performance, high-bandwidth device. An input capacitor of 1μF from the input pin to ground is required to provide stability. Low-ESR ceramic capacitors provide optimal performance in small board area. Additional high-frequency capacitors, such as small valued NPO dielectric type capacitors, help filter out high-frequency noise and are good practice in any RF-based circuit. X5R or X7R dielectrics are recommended for the input capacitor. Y5V dielectrics lose most of their capacitance over temperature and are therefore not recommended.

Output Capacitor

The MIC5363/4 requires an output capacitor of 1μF or greater to maintain stability. The design is optimized for use with low-ESR ceramic chip capacitors. High-ESR capacitors may cause high-frequency oscillation. The output capacitor can be increased, but performance has been optimized for a 1μF ceramic output capacitor.

X7R and X5R dielectric ceramic capacitors are recommended for their temperature performance. X7R capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60% respectively over their operating temperature ranges. To use a ceramic-chip capacitor with Y5V dielectric the value must be much higher than an X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

No Load Stability

The MIC5363/4 will remain stable and in regulation without the need of a minimum load. This reduces the amount and therefore cost of external components.

Enable/Shutdown

The MIC5363/4 comes with an active-high enable pin that allows the regulator to be disabled. Forcing the enable pin low disables the regulator and sends it into an off-mode current state drawing virtually zero current. When disabled, the MIC5364 switches an internal 30Ω load on the regulator output to discharge the external capacitor.

Forcing the enable pin high enables the output voltage. The active-high enable pin uses CMOS technology and cannot be left floating. A floating enable pin may cause an indeterminate state on the output.

Thermal Considerations

The MIC5363/4 is designed to provide a 300mA continuous current output in a very-small package. Maximum operating temperature can be calculated based on the output currents and the voltage drop across the part. For example, if the input voltage is 3.6V, $V_{OUT} = 2.8V$ and the output current = 300mA. The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT}) I_{OUT} + V_{IN} I_{GND}$$

Because this is a CMOS device and the ground current is typically <60μA over the load range, the power dissipation contributed by the ground current is < 1% which can be ignored for this calculation:

$$\begin{aligned} P_D &= (3.6V - 2.8V) \times 300mA \\ P_D &= 0.24W \end{aligned}$$

To determine the maximum ambient operating temperature of the package, use the junction to ambient thermal resistance of the device and the following basic equation:

$$P_{D(MAX)} = \left(\frac{T_{J(MAX)} - T_A}{\theta_{JA}} \right)$$

$$T_{J(MAX)} = 125^{\circ}C$$

$$\theta_{JA} = 173^{\circ}C/W$$

Substituting P_D for $P_{D(MAX)}$ and solving for the ambient operating temperature will give the maximum operating conditions for the regulator circuit. The junction-to-ambient thermal resistance for the standard footprint is 173°C/W .

The maximum power dissipation must not be exceeded for proper operation.

For example, when operating a 2.8V application with an input voltage of 3.6V and 300mA output with a standard footprint layout, the maximum ambient operating temperature T_A can be determined as follows:

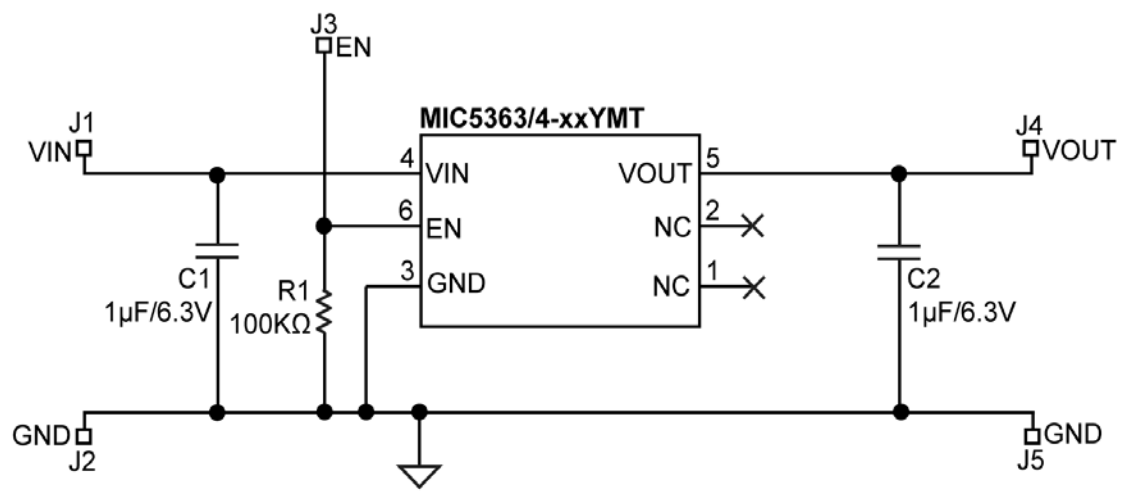
$$0.24\text{W} = (125^{\circ}\text{C} - T_A) / (173^{\circ}\text{C/W})$$

$$T_A = 83^{\circ}\text{C}$$

Therefore, a MIC5363-2.8YMT application with a 300mA output current can accept an ambient operating temperature of 83°C in a 1.2mm x 1.2mm Thin MLF[®] package. For a full discussion of heat sinking and thermal effects on voltage regulators refer to the "Regulator Thermals" section of Micrel's *Designing with Low-Dropout Voltage Regulators* handbook. This information can be found on Micrel's website at:

http://www.micrel.com/PDF/other/LDOBk_ds.pdf

Evaluation Board Schematic

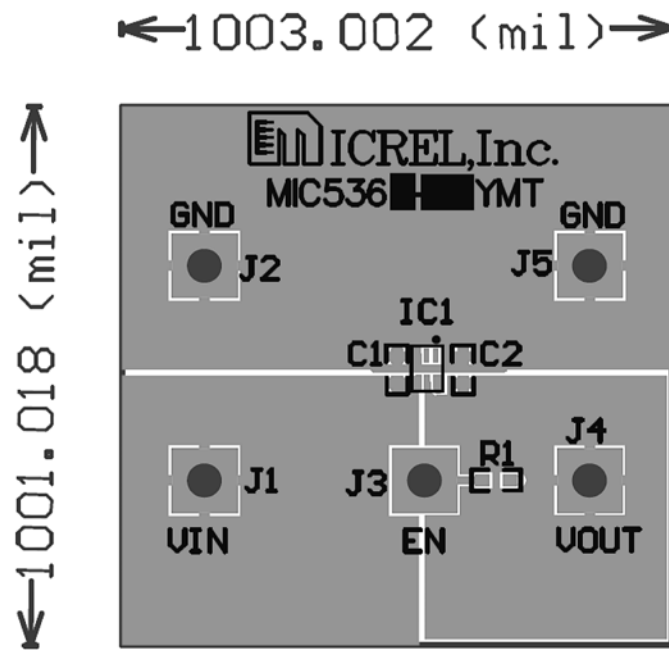


Bill of Materials

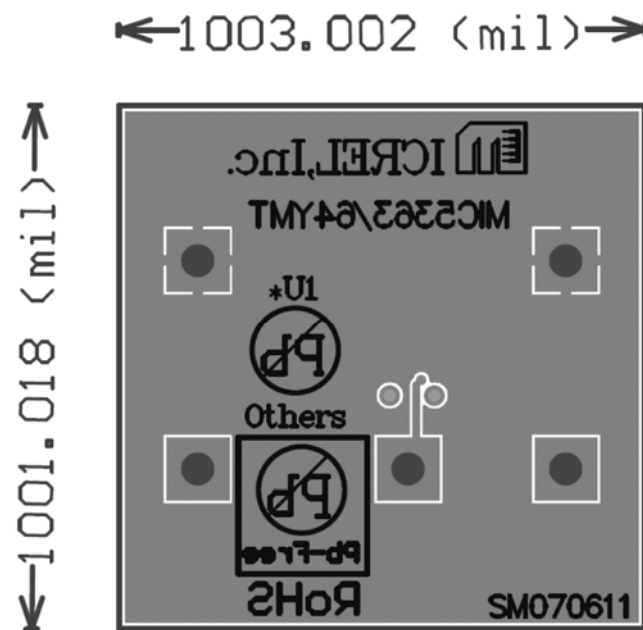
Item	Part Number	Manufacturer	Description	Qty.
C1, C2	GRM155R61A105KE15D	Murata ⁽¹⁾	1µF/6.3V ceramic capacitor, X5R, size 0402	2
R1	CRCW04021003FKED	Vishay ⁽²⁾	Resistor, 100k (0403 size), 1%	1
U1	MIC5363/4-xxYMT	Micrel, Inc. ⁽³⁾	300mA µCap LDO in 1.2mm × 1.2mm Thin MLF®	1

- Notes:
- 1. Murata Tel: www.murata.com.
 - 2. Vishay: www.vishay.com.
 - 3. Micrel, Inc.: www.micrel.com.

PCB Layout Recommendations

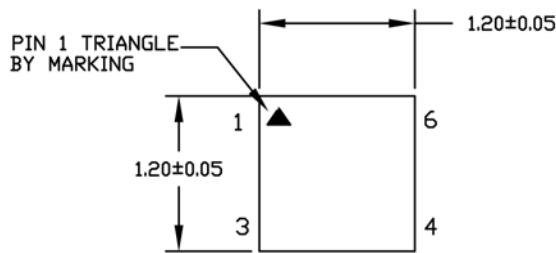


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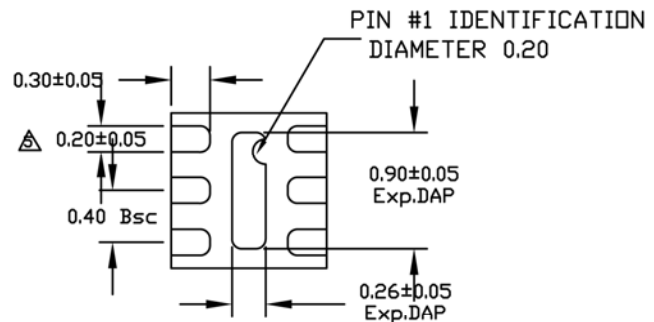


Bottom Layer

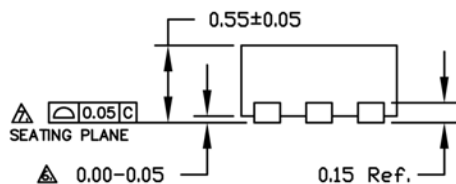
Package Information



TOP VIEW



BOTTOM VIEW



SIDE VIEW

- NOTES :
1. ALL DIMENSIONS ARE IN MILLIMETERS.
 2. MAX. PACKAGE WARPAGE IS 0.05 mm.
 3. MAXIMUM ALLOWABLE BURRS IS 0.08 mm IN ALL DIRECTIONS.
 4. PIN #1 ID ON TOP WILL BE LASER/INK MARKED. DIMENSION APPLIES TO METALIZED TERMINAL AND IS MEASURED BETWEEN 0.20 AND 0.25 mm FROM TERMINAL TIP.
 5. APPLIED ONLY FOR TERMINALS.
 6. APPLIED FOR EXPOSED PAD AND TERMINALS

6-Pin 1.2mm x 1.2mm Thin MLF®

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