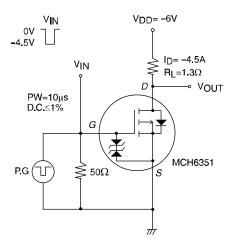
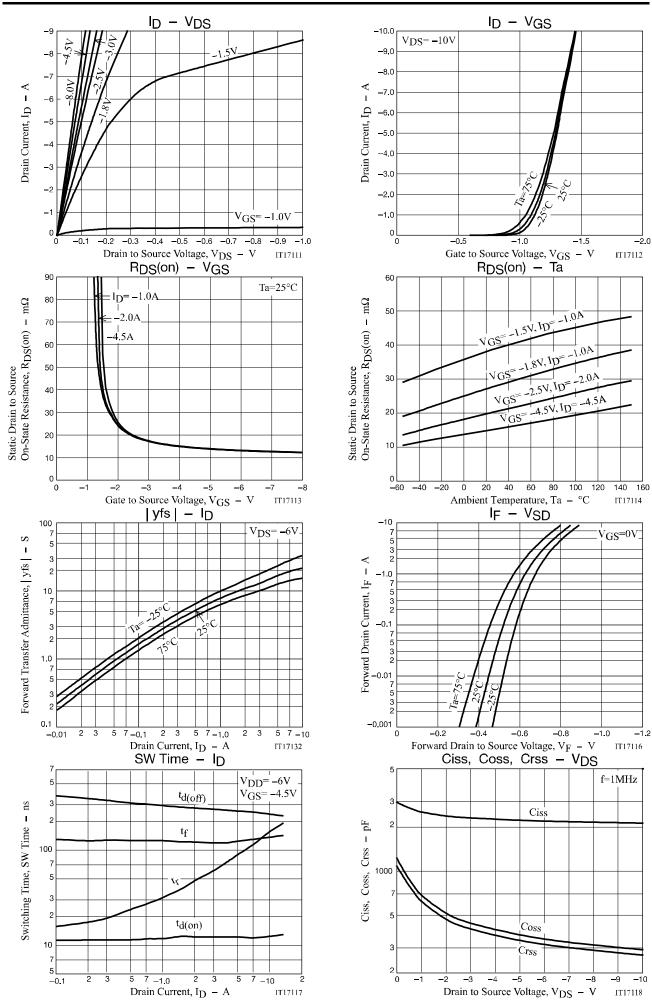
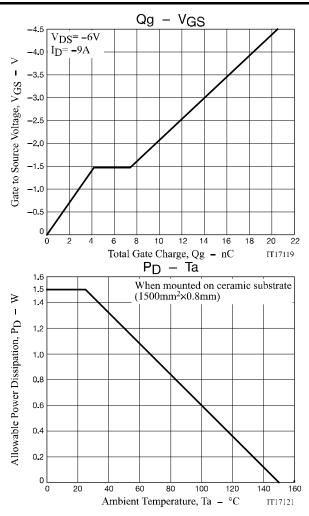
Electrical Characteristics at $Ta = 25^{\circ}C$

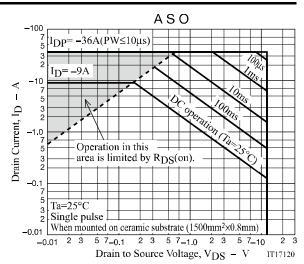
Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-12			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-12V, V _{GS} =0V			-1	μA
Gate to Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	V _{GS} (off)	V _{DS} =-6V, I _D =-1mA	-0.4		-1.3	V
Forward Transfer Admittance	yfs	V _{DS} =-6V, I _D =-4.5A		16.5		S
Static Drain to Source On-State Resistance	R _{DS} (on)1	ID=-4.5A, VGS=-4.5V		14	16.9	mΩ
	R _{DS} (on)2	ID=-2.0A, VGS=-2.5V		19	24	mΩ
	R _{DS} (on)3	ID=-1.0A, VGS=-1.8V		28	40	mΩ
	R _{DS} (on)4	ID=-1.0A, VGS=-1.5V		37	74	mΩ
Input Capacitance	Ciss	V _{DS} =-6V, f=1MHz		2200		pF
Output Capacitance	Coss			350		pF
Reverse Transfer Capacitance	Crss			320		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		12.3		ns
Rise Time	tr			89		ns
Turn-OFF Delay Time	t _d (off)			260		ns
Fall Time	tf			122		ns
Total Gate Charge	Qg	V _{DS} =-6V, V _{GS} =-4.5V, I _D =-9A		20.5		nC
Gate to Source Charge	Qgs			4.2		nC
Gate to Drain "Miller" Charge	Qgd			3.2		nC
Diode Forward Voltage	V _{SD}	I _S =-9A, V _{GS} =0V		-0.83	-1.2	V

Switching Time Test Circuit

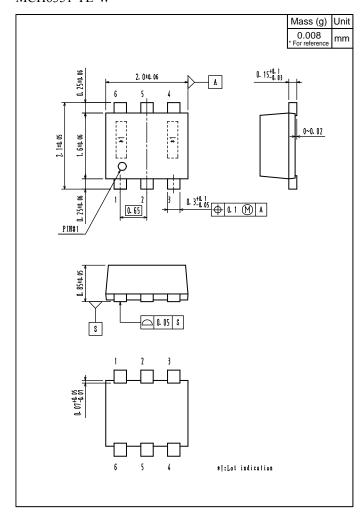




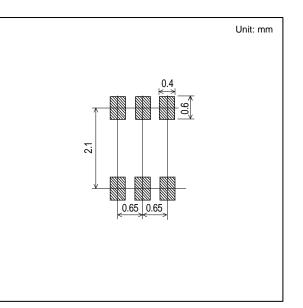




Outline Drawing MCH6351-TL-W



Land Pattern Example



Note on usage : Since the MCH6351 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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