

SEMICONDUCTOR

FGL60N100BNTD 1000 V, 60 A NPT Trench IGBT

Features

- · High Speed Switching
- Low Saturation Voltage: V_{CE(sat)} = 2.5 V @ I_C = 60 A
- · High Input Impedance
- Built-in Fast Recovery Diode

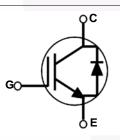
Applications

· UPS, Welder

General Description

Using Fairchild's proprietary trench design and advanced NPT technology, the 1000V NPT IGBT offers superior conduction and switching performances, high avalanche ruggedness and easy parallel operation. This device offers the optimum performance for hard switching application such as UPS, welder applications.





Absolute Maximum Ratings

Symbol	Description		Ratings	Unit	
V _{CES}	Collector to Emitter Voltage		1000	V	
V _{GES}	Gate to Emitter Voltage		± 25	V	
	Collector Current	@ T _C = 25°C	60	A	
Collecto	Collector Current	@ T _C = 100 ^o C	42	A	
I _{CM (1)}	Pulsed Collector Current	@ T _C = 25°C	200	A	
IF	Diode Continuous Forward Current	@ T _C = 100°C	15	A	
P _D	Maximum Power Dissipation	@ T _C = 25°C	180	W	
	Maximum Power Dissipation	@ T _C = 100 ^o C	72	W	
TJ	Operating Junction Temperature		-55 to +150	°C	
T _{stg}	Storage Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C	

Notes: 1: Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Ratings	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	0.69	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	2.08	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	25	°C/W

March 2014

Part Nu	mber	Top Mark	Package	Packing Method	Reel Size	Tape W	idth	Quantity
FGL60N100BNTD FGL60N100BNTD T		TO-264	TO-264 Tube		N/A		30	
Electric	al Cha	aracteristics o	f the IG	BT $T_{C} = 25^{\circ}C$ unless otherwise	noted			
Symbol		Parameter		Test Conditions	Min.	Тур.	Max	. Unit
Off Charac	teristics							
BV _{CES}	Collecto	or to Emitter Breakdow	n Voltage	/ _{GE} = 0 V, I _C = 1 mA	1000	-	-	V
I _{CES}	Collecto	or Cut-Off Current		$V_{CE} = V_{CES}, V_{GE} = 0 V$	-	-	1	mA
I _{GES}	G-E Lea	akage Current	Ň	$V_{GE} = V_{GES}, V_{CE} = 0 V$	-	-	±500	nA
On Charac	teristics							
V _{GE(th)}	G-E Thr	eshold Voltage	1	_C = 60 mA, V _{CE} = V _{GE}	4.0	5.0	7.0	V
				_C =10 A, V _{GE} = 15 V	-	1.5	1.8	V
V _{CE(sat)}	Collecto	or to Emitter Saturation	Voltage	_C = 60 A, V _{GE} = 15 V,	-	2.5	2.9	V
Dynamic C	haracter	istics				- I	1	
C _{ies}	Input Ca	apacitance			-	6000	-	pF
C _{oes}	Output 0	Capacitance		/ _{CE} = 10 V _, V _{GE} = 0 V, = 1MHz	-	260	-	pF
C _{res}	Reverse	e Transfer Capacitance			-	200	-	pF
Switching	Characte	eristics						
t _{d(on)}	Turn-Or	n Delay Time			-	140	-	ns
t _r	Rise Tin	ne		$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 60 \text{ A},$ $R_{G} = 51 \Omega, V_{GE} = 15 \text{ V},$	-	320	-	ns
t _{d(off)}	Turn-Of	f Delay Time		nductive Load, $T_C = 25^{\circ}C$	-	630	-	ns
t _f	Fall Tim	e			-	130	-	ns
Qg	Total Ga	ate Charge		(000) (L 00 ;	-	275	-	nC
Q _{ge}	Gate to	Emitter Charge		/ _{CE} = 600 V, I _C = 60 A, / _{GE} = 15 V, T _C = 25 ^o C	-	45	-	nC
Q _{gc}	Gate to	Collector Charge		GE 10 1, 10 20 0	-	95	-	nC

Electrical Characteristics of the Diode $T_{C} = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Unit
V _{FM}	Diode Forward Voltage	I _F = 15 A	-	1.2	1.7	V
		I _F = 60 A	-	1.8	2.1	V
t _{rr}	Diode Reverse Recovery Time	I _F = 60 A, di/dt = 20 A/us	-	1.2	1.5	us
I _R	Instantaneous	V _{RRM} = 1000 V	-	0.05	2.0	uA

Typical Performance Characteristics

Figure 1. Typical Output Characteristics

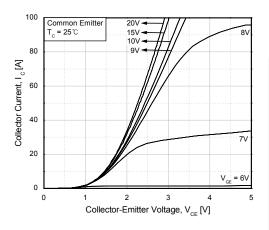


Figure 3. Saturation Voltage vs. Case Temperature at Variant Current Level

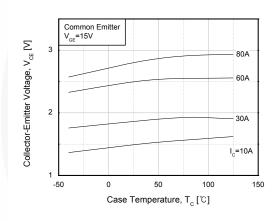


Figure 5. Saturation Voltage vs. V_{GE}

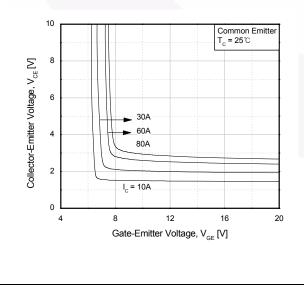


Figure 2. Typical Saturation Voltage Characteristics

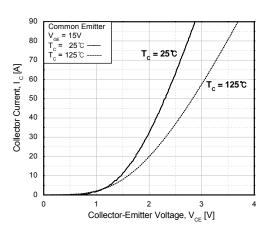
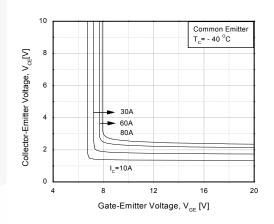
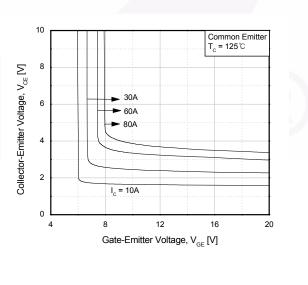


Figure 4. Saturation Voltage vs. V_{GE}



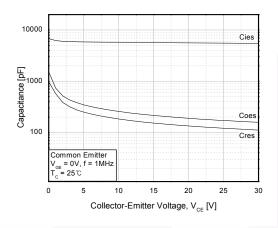


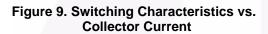


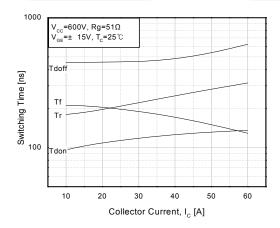
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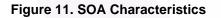
Typical Performance Characteristics

Figure 7. Capacitance Characteristics









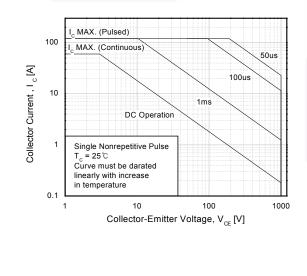


Figure 8. Switching Loss vs. Gate Resistance

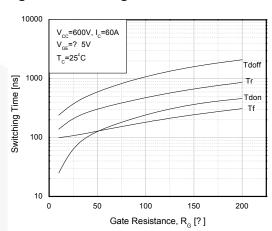


Figure 10. Gate Charge Characteristics

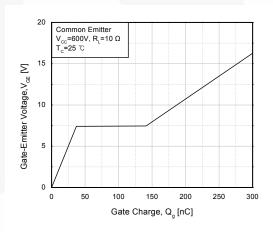
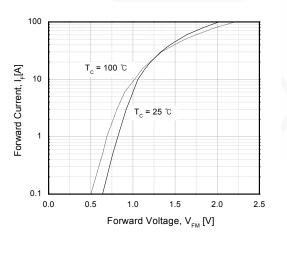
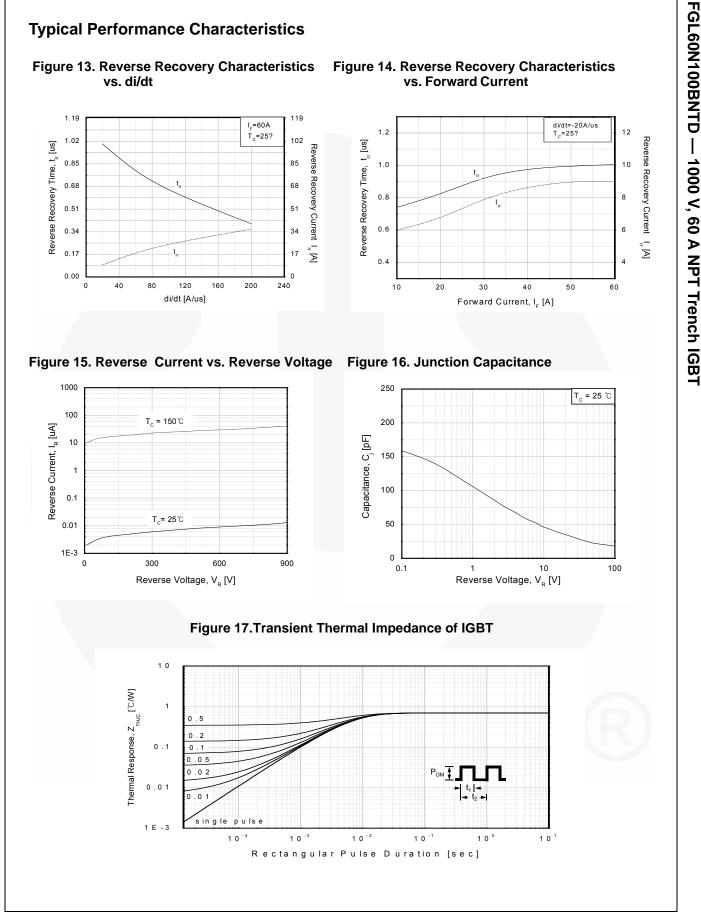
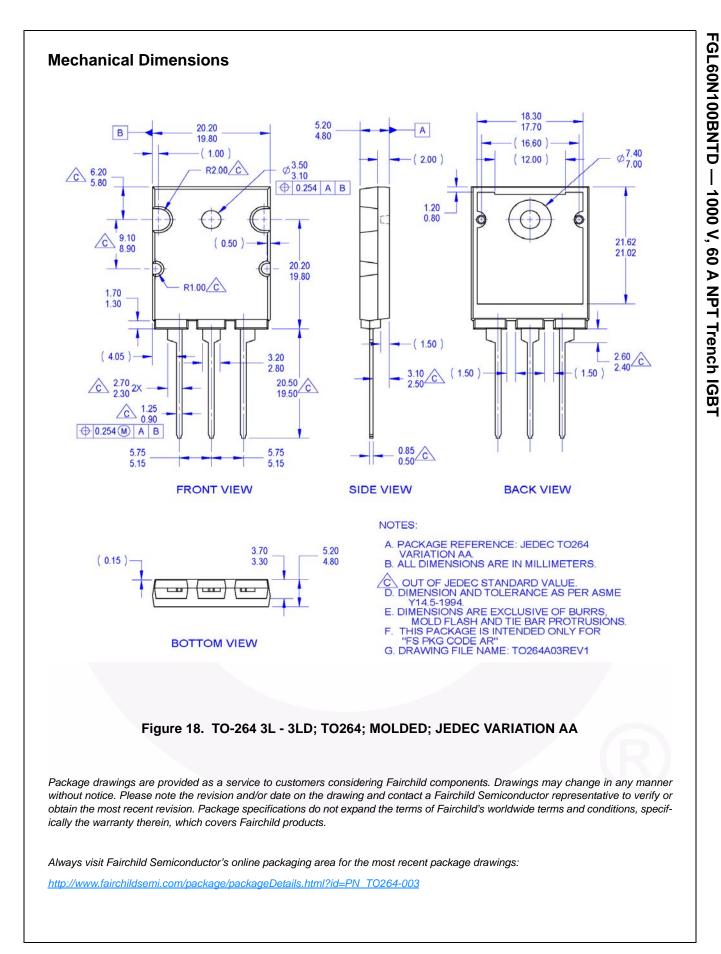


Figure 12. Forward Characteristics





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