# Sim Models & Software

# Sim Models



# Verilog: 4Gb Mobile LPDDR3 2E0F (ZIP)

Verilog simulation model for 4Gb Mobile LPDDR3

File Type: ZIP

**Updated:** 09/07/2016

DRAM Mobile LPDDR3

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## IBIS: 168-Ball 2E0F QDP Mobile LPDDR3 SDRAM (Ball-Height/0.27) (ZIP)

Rev 1.0 168-Ball, QDP, Single-Channel, LPDDR3 SDRAM

File Type: ZIP

**Updated:** 08/23/2015

Mobile LPDDR3
Mobile Memory Solutions

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# **RoHS Certificates**

#### **RoHS Certificates**

# **RoHS Certificate of Compliance (PDF)**

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

**Updated:** 01/2017

EDFA232A2PF-GD-F

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#### **RoHS Certificates**

### **China RoHS Certificate (PDF)**

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Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

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# Technical Notes

#### **SEARCH (9) LPDRAM TECHNICAL NOTES**

#### **Technical Notes**

#### Technical Note: Low-Power Function of Mobile RAM - Deep Power Down (DPD) (PDF)

**DRAM LPDRAM** 

(E0598E21) This technical note describes Deep Power Down (DPD), one of the low-power functions that have been adapted to Mobile DRAM.

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### **Technical Notes**

## Technical Note: Low-Power Function of Mobile RAM - Partial Array Self Refresh (PASR) (PDF)

**DRAM LPDRAM** 

(E0597E10) This technical note describes Partial Array Self Refresh (PASR), one of the low-power functions that have been adapted to Mobile DRAM.

File Type: PDF

**Updated: 02/26/2014** 

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#### **Technical Notes**

## Technical Note: Low-Power Function of Mobile RAM - Auto Temperature Compensated Self Refresh (ATCSR) (PDF)

DRAM **LPDRAM** 

(E0599E20) This technical note describes Auto Temperature Compensated Self Refresh (ATCSR), one of the low-power functions that have been adapted to Mobile DRAM.

File Type: PDF

**Updated:** 02/26/2014

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#### **Technical Notes**

## TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)

**DDR SDRAM** DDR2 SDRAM **DDR3 SDRAM** DDR3L-RS See More Tags

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

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**Updated:** 02/14/2013

#### **Technical Notes**

#### TN-46-23: Backward Compatibility for Faster LPDDR SDRAM (PDF)

DRAM **LPDRAM Mobile Memory Solutions** 

(TN-46-23) This technical note reviews the timing differences between the faster and slower LPDDR SDRAM speeds and explains how faster parts function compatibly with slower parts.

**Updated:** 02/04/2013

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# Parts with the same Data Sheet (2)

EDFA232A2PF-GD-F ( Current )		EDF8132A3PF-GD-F	EDFM432A1PH-GD-F	
Part Status	Production	Production	Production	
Technology	LPDDR3	LPDDR3	LPDDR3	
Density	16Gb	8Gb	12Gb	
RoHS	Yes	Yes	Yes	
Depth	512Mb	256Mb	384Mb	
Width	x32	x32	x32	
Voltage	1.2V	1.2V	1.2V	
Package	FBGA	VFBGA	VFBGA	
Pin Count	168-ball	168-ball	168-ball	
Clock Rate	800 MHz	800 MHz	800 MHz	
Data Rate	DDR3-1600	DDR3-1600	DDR3-1600	
C	Cycle Time			
Op. Temp.	-30C to +85C	-30C to +85C	-30C to +85C	
Brand	Micron	Micron	Micron	

# Where to Buy

# Orderable Parts

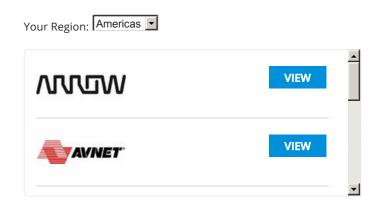
	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
EDFA232A2P D Downloaded from Arro	Production	Dry Pack	N/A	N/A	N/A	No		N/A

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