

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	100	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (V _{GS} = 10V)	Steady State	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	I _D	10.8 7.6	А
	Steady State	$T_C = +25$ °C $T_C = +100$ °C	I _D	98.4 69.6	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	250	Α		
Maximum Continuous Body Diode Forward Current			Is	95	Α
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)			I _{SM}	250	Α
Avalanche Current, L=0.3mH			I _{AS}	15	Α
Avalanche Energy, L=0.3mH			E _{AS}	33.7	mJ

Thermal Characteristics

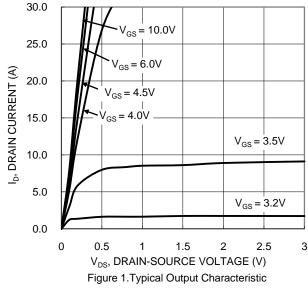
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	1.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	99	°C/W
Total Power Dissipation	T _C = +25°C	P _D	125	W
Thermal Resistance, Junction to Case		R _{0JC}	1.2	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +175	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	100	1	1	٧	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	l	l	1	μΑ	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	1	1	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	1.4	1.9	3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
		I	6.9	8.6	mΩ	$V_{GS} = 10V, I_D = 13A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	1	7.5	12		$V_{GS} = 6V, I_{D} = 13A$	
		-	10	20		$V_{GS} = 4.5V, I_D = 5A$	
Diode Forward Voltage	V_{SD}	I	0.8	1.3	٧	$V_{GS} = 0V, I_{S} = 13A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	1	2592	_		$V_{DS} = 50V$, $V_{GS} = 0V$ f = 1MHz	
Output Capacitance	Coss	1	792	1	pF		
Reverse Transfer Capacitance	C _{rss}	1	45	_			
Gate Resistance	R_g	I	2	1	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_g	1	53.7	1		\/ F0\/ I- 12A	
Gate-Source Charge	Q_{gs}	I	10.6	1	nC	$V_{DD} = 50V, I_{D} = 13A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q_{gd}	l	8.2	1			
Turn-On Delay Time	t _{D(ON)}		11.6			$V_{DD} = 50V, V_{GS} = 10V,$ $I_{D} = 13A, R_{g} = 6\Omega$	
Turn-On Rise Time	t _R	I	14.1	_	no		
Turn-Off Delay Time	t _{D(OFF)}	I	42.9	1	ns		
Turn-Off Fall Time	t _F	1	22	_			
Reverse Recovery Time	t _{RR}	_	49.8	_	ns	I 12	
Reverse Recovery Charge	Q _{RR}	_	85.1	_	nC	I _F = 13A, di/dt = 100A/μs	

5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.6. Short duration pulse test used to minimize self-heating effect.7. Guaranteed by design. Not subject to product testing.





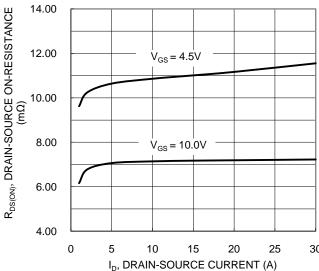


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

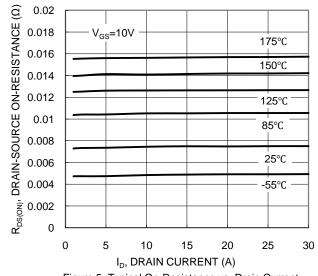
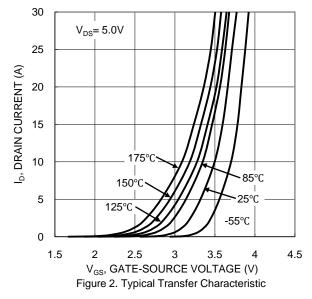
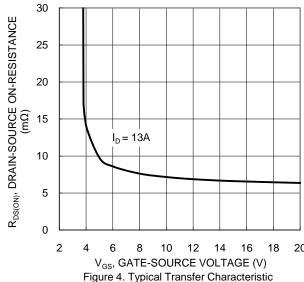


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





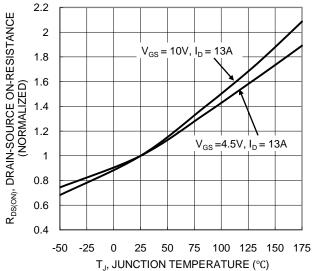


Figure 6. On-Resistance Variation with Temperature





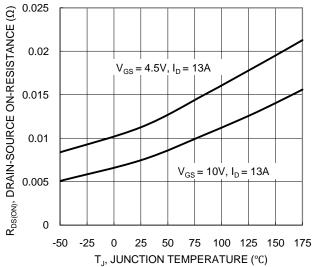


Figure 7. On-Resistance Variation with Temperature

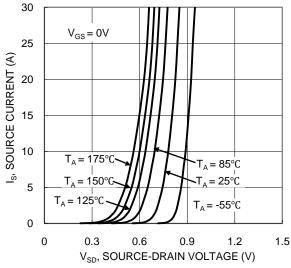


Figure 9. Diode Forward Voltage vs. Current

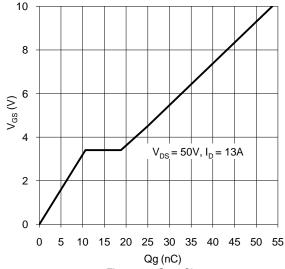


Figure 11. Gate Charge

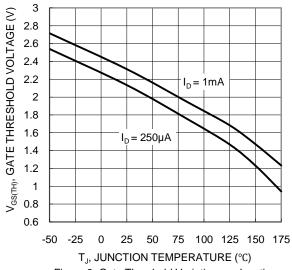
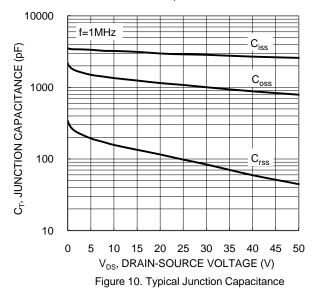
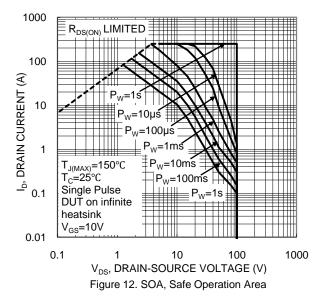


Figure 8. Gate Threshold Variation vs. Junction Temperature







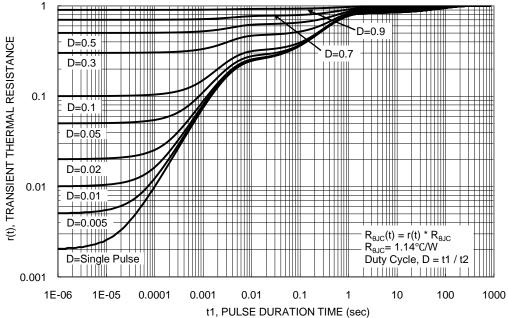


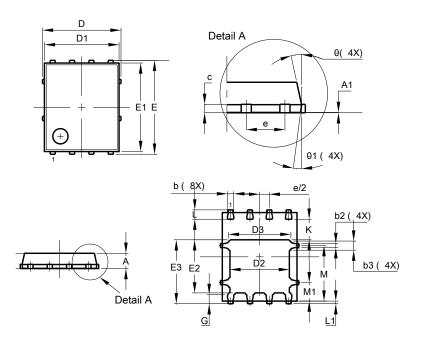
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

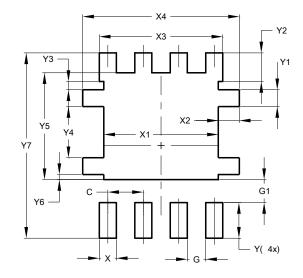


PowerDI5060-8						
Dim	Min	Тур				
Α	0.90	1.10	1.00			
A1	0.00	0.05	_			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330 0.277				
D	5.15 BSC					
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90 4.30		4.10			
Е	6.15 BSC					
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
e G	1.27 BSC					
G	0.51	0.71 0.6				
K	0.51 – –					
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
М	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
θ	10°	12º	11º			
θ1	6°	80	7º			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
X	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Υ	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			



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