

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	-30	V
Gate-Source Voltage			V <sub>GSS</sub>	±12	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	Steady state	T <sub>A</sub> = +25°C	I <sub>D</sub>	-4.8	A
		T <sub>A</sub> = +70°C		-3.8	
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I <sub>DM</sub>	-35	A
Maximum Continuous Body Diode Forward Current (Note 6)			I <sub>S</sub>	-3.5	A
Avalanche Current (Note 7) L = 0.1mH			I <sub>AS</sub>	-19	A
Avalanche Energy (Note 7) L = 0.1mH			E <sub>AS</sub>	19	mJ

**Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	P <sub>D</sub>	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	R <sub>θJA</sub>	102	°C/W
	t < 10s		57	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	P <sub>D</sub>	1.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	R <sub>θJA</sub>	75	°C/W
	t < 10s		40	
Thermal Resistance, Junction to Case (Note 6)		R <sub>θJC</sub>	18	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	-1.0	µA	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±12V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.5	—	-1.3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	28	48	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -5A
		—	34	57		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3.5A
		—	44	80		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -2.5A
		—	—	—		V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.7	-1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	C <sub>iss</sub>	—	1438	—	pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	92	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	76	—	pF	
Gate Resistance	R <sub>g</sub>	—	11.1	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	29.6	—	nC	V <sub>DS</sub> = -20V, I <sub>D</sub> = -5A
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	—	13.5	—	nC	
Gate-Source Charge	Q <sub>gs</sub>	—	2.4	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	3.0	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	2.5	—	ns	V <sub>DS</sub> = -15V, V <sub>GS</sub> = -10V, R <sub>L</sub> = 3Ω, R <sub>G</sub> = 6Ω
Turn-On Rise Time	t <sub>r</sub>	—	2.7	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	108	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	34	—	ns	
Reverse Recovery Time	t <sub>RR</sub>	—	10	—	ns	I <sub>F</sub> = -5.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	—	3.6	—	nC	I <sub>F</sub> = -5.0A, di/dt = 100A/µs

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
  - I<sub>AS</sub> and E<sub>AS</sub> rating are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

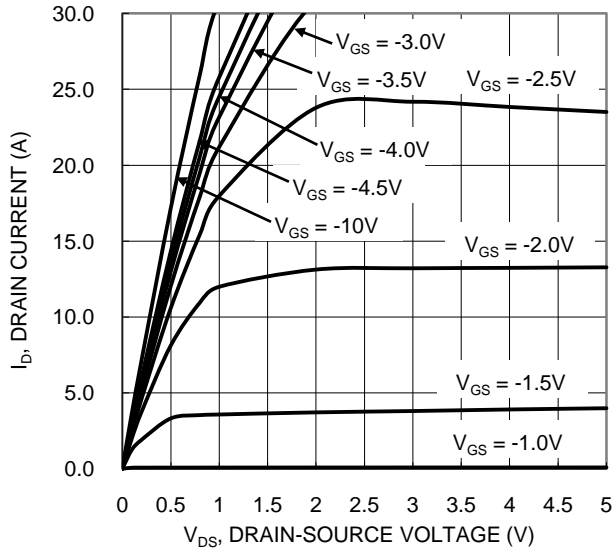


Figure 1. Typical Output Characteristic

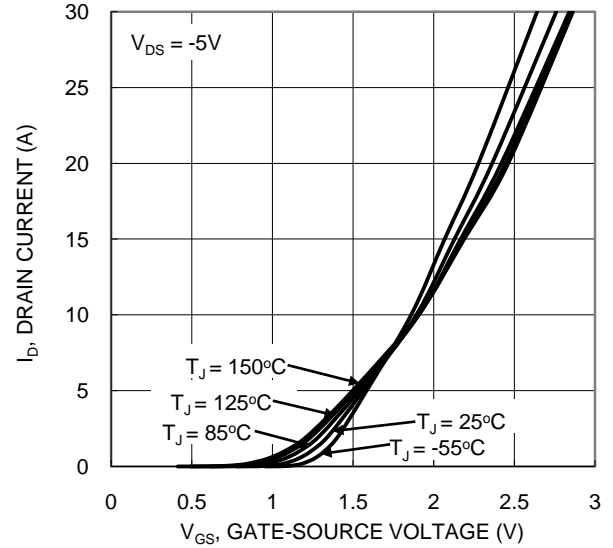


Figure 2. Typical Transfer Characteristic

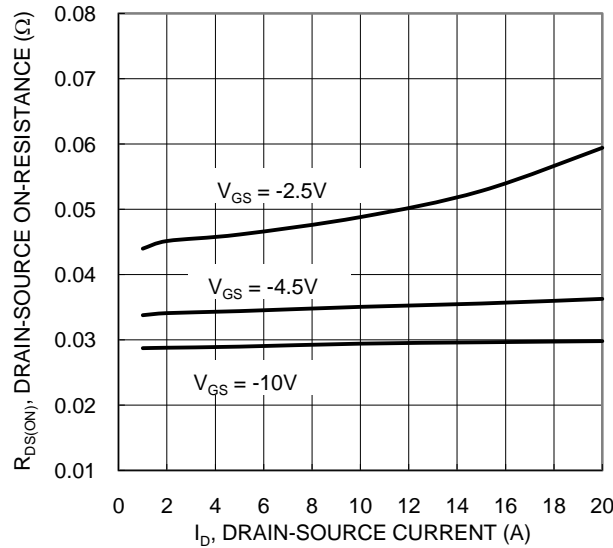


Figure 3. Typical On-Resistance vs Drain Current and Gate Voltage

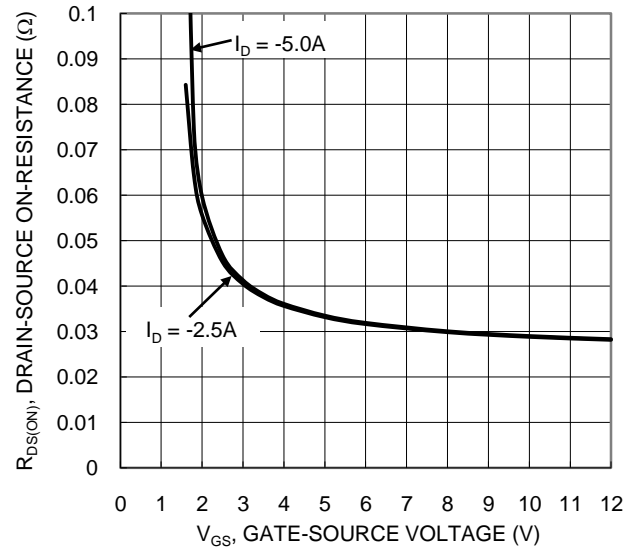


Figure 4. Typical Transfer Characteristic

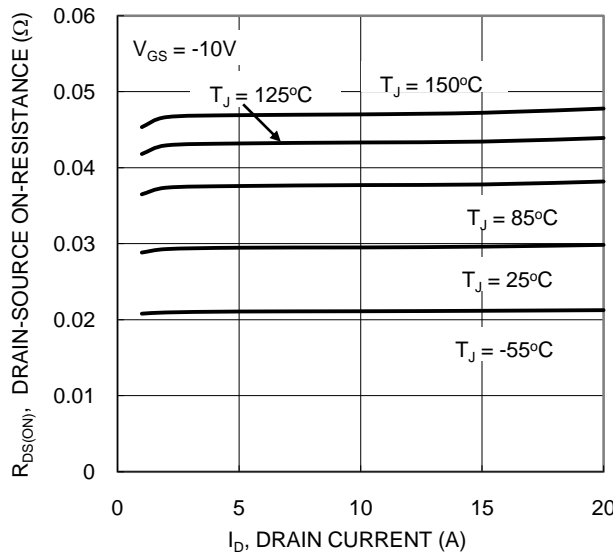


Figure 5. Typical On-Resistance vs Drain Current and Temperature

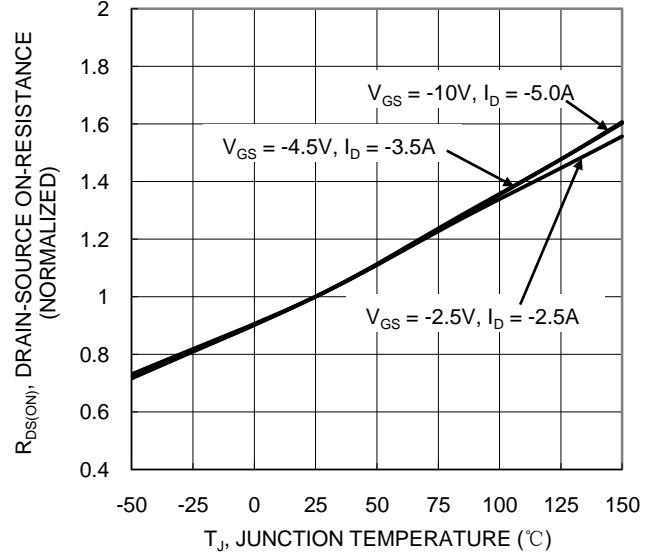


Figure 6. On-Resistance Variation with Temperature

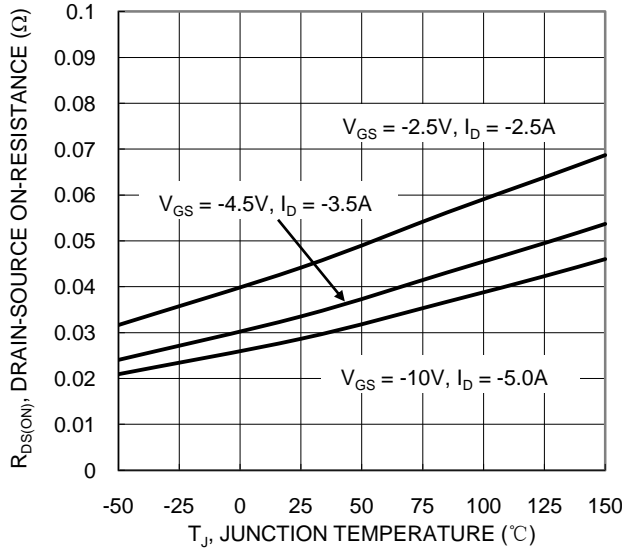


Figure 7. On-Resistance Variation with Temperature

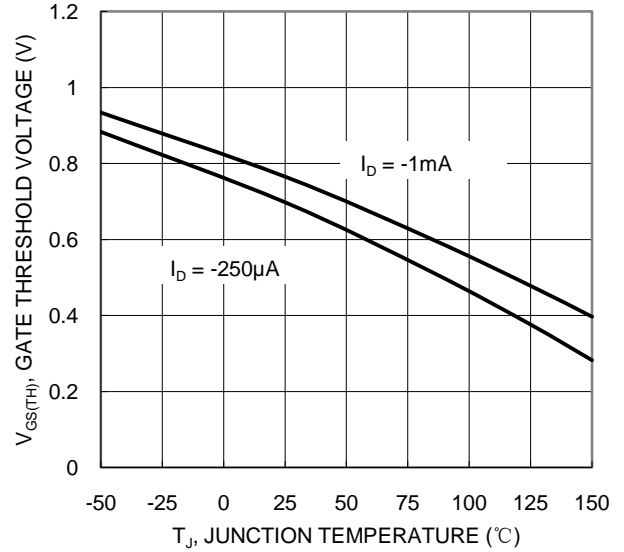


Figure 8. Gate Threshold Variation vs Junction Temperature

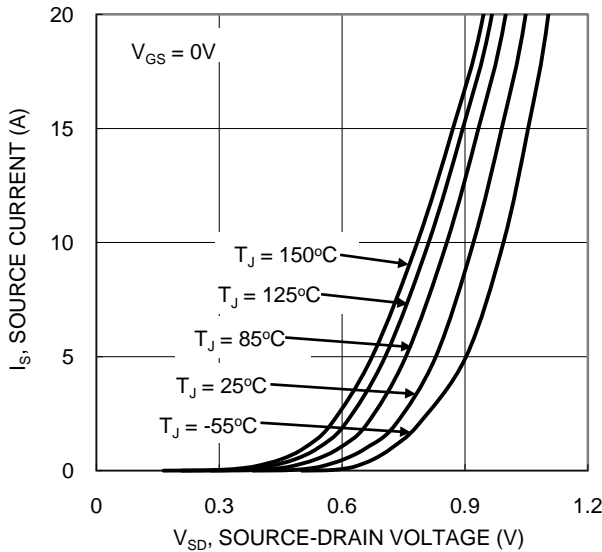


Figure 9. Diode Forward Voltage vs Current

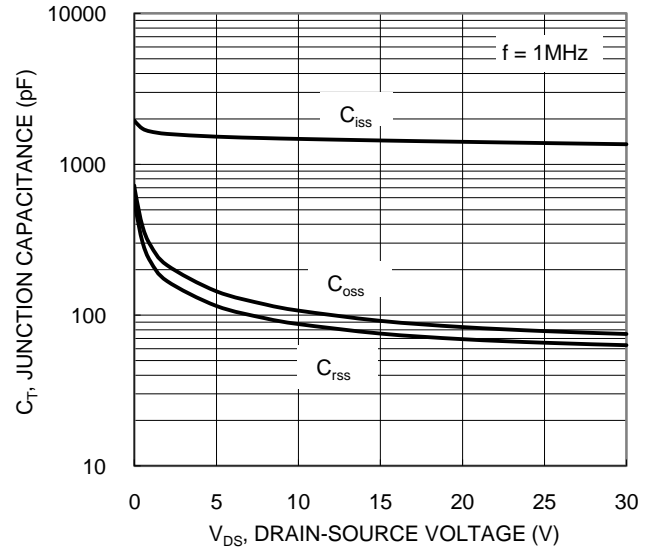


Figure 10. Typical Junction Capacitance

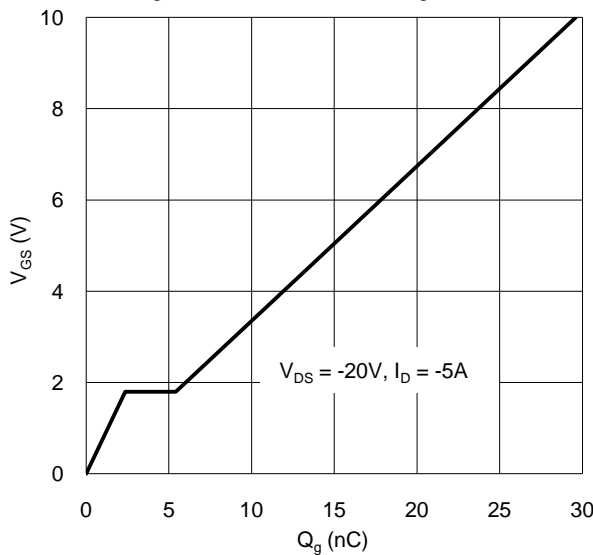


Figure 11. Gate Charge

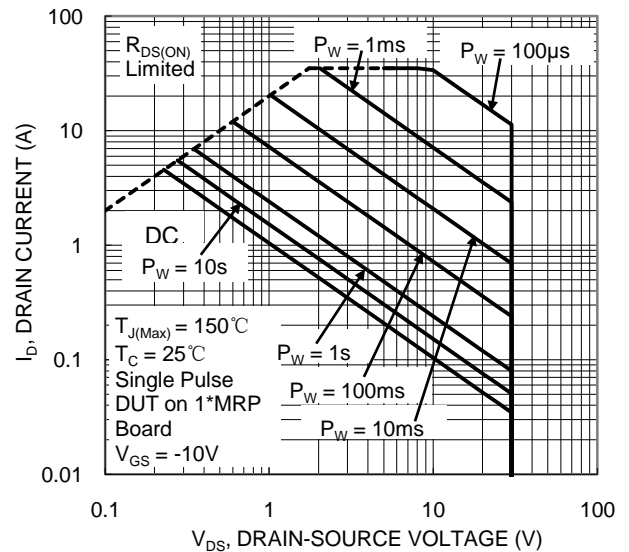


Figure 12. SOA, Safe Operation Area

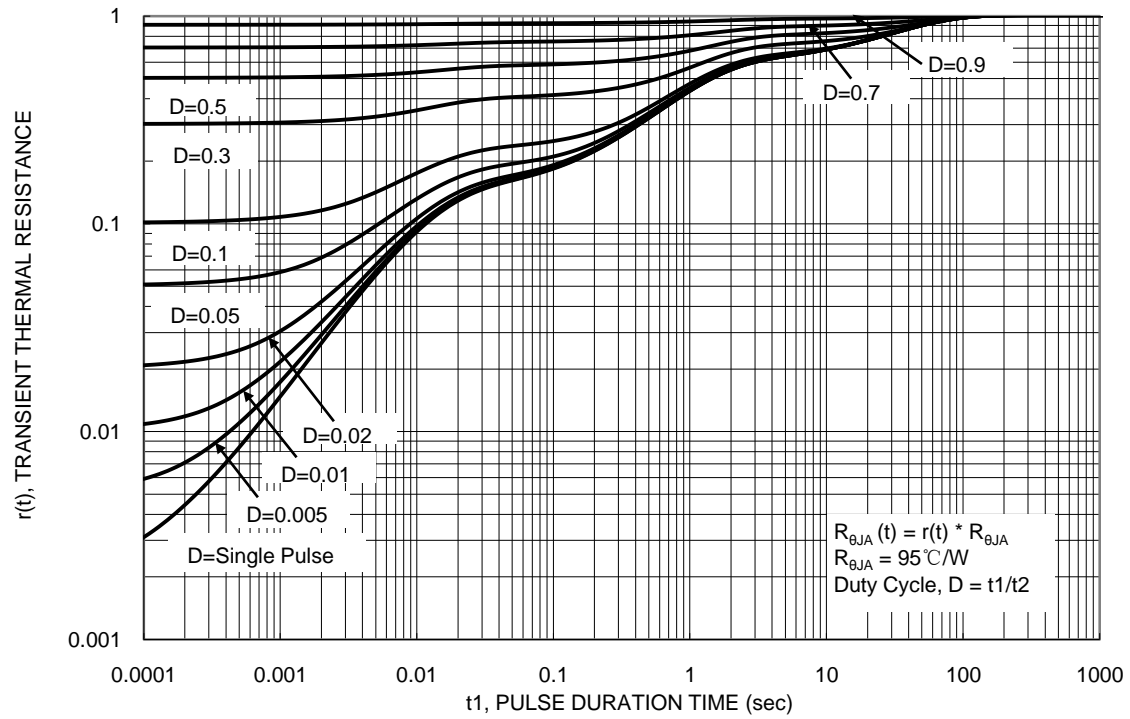
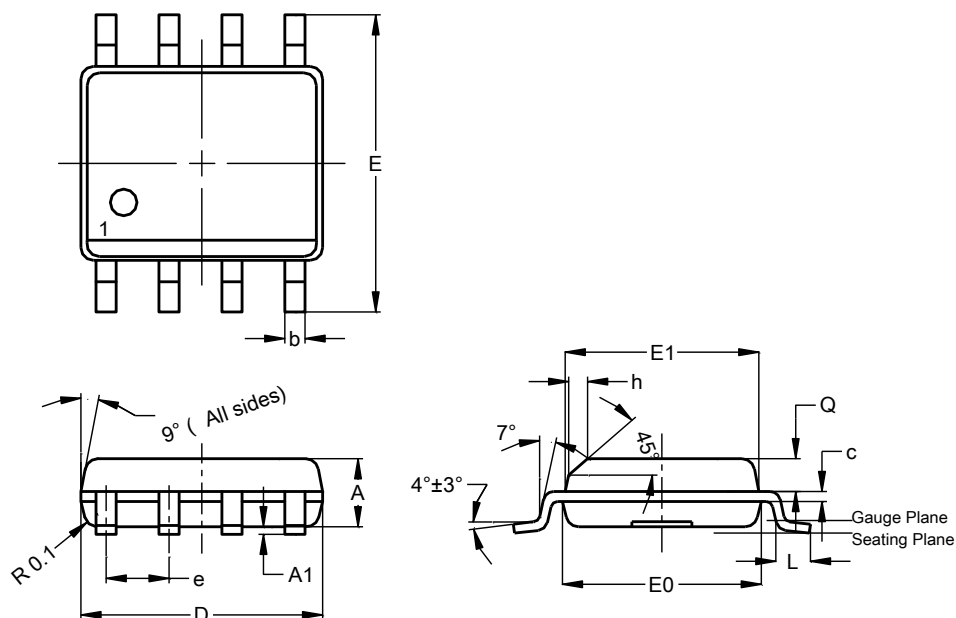


Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SO-8



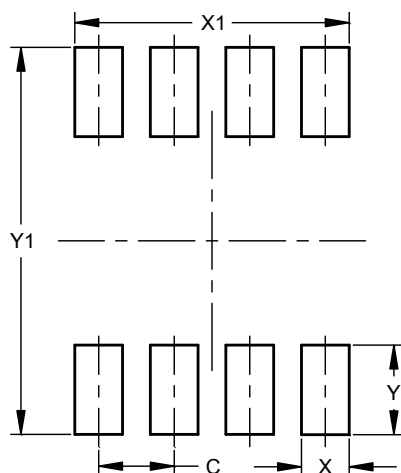
SO-8			
Dim	Min	Max	Typ
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65

All Dimensions in mm

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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