

# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V <sub>DSS</sub>	-30	V		
Gate-Source Voltage	V <sub>GSS</sub>	±20	V		
Continuous Drain Current (Note 7) V <sub>GS</sub> = -10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	۱ <sub>D</sub>	-8.6 -7.0	А
	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	۱ <sub>D</sub>	-11.7 -9.3	А
Continuous Drain Current (Note 7) $V_{GS}$ = -4.5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	۱ <sub>D</sub>	-7.1 -5.6	А
	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-9.6 -7.6	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	-80	А		
Maximum Continuous Body Diode Forward Current (Note 7)			ls	-3.0	А

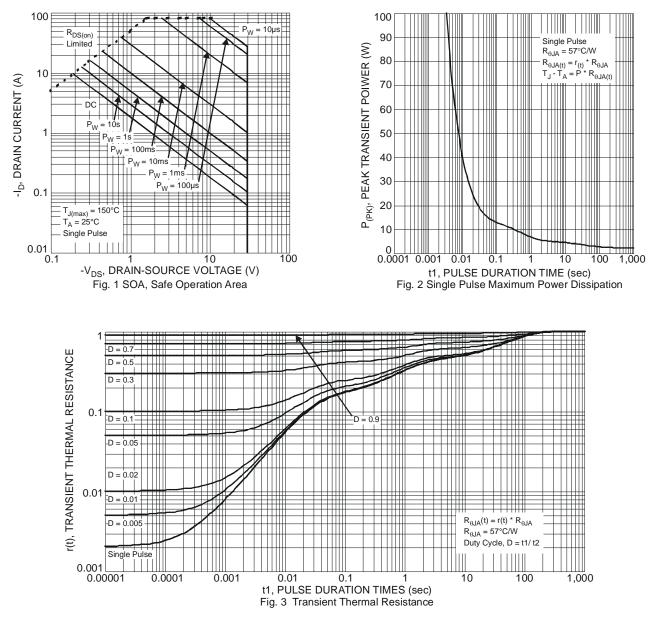
### **Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 6)		PD	0.9	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	P	140	°C/W
Thermal Resistance, Junction to Amblent (Note 6)	t<10s	R <sub>0JA</sub>	72	°C/W
Total Power Dissipation (Note 7)		PD	2.2	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	Р	57	°C/W
Thermal Resistance, Junction to Ambient (Note 7)	t<10s	$R_{\theta JA}$	30	°C/W
Thermal Resistance, Junction to Case (Note 7)		R <sub>0JC</sub>	7.1	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

Notes: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.



### DMP3008SFGQ





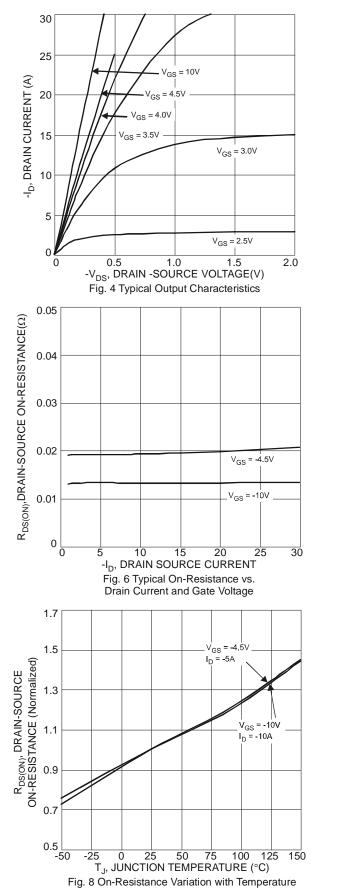
### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

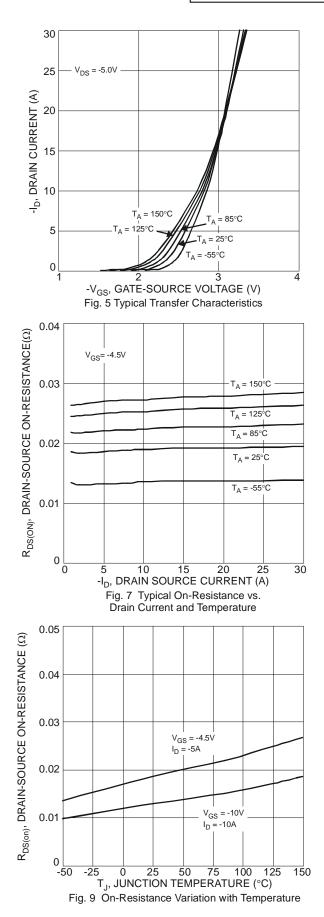
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)				I.			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30			V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	-1.0	μA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.1	-1.6	-2.1	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
Static Drain-Source On-Resistance	Proven	—	12.5	17	mΩ	$V_{GS} = -10V, I_D = -10A$	
	R <sub>DS(ON)</sub>		18.5	25	11152	$V_{GS} = -4.5V, I_D = -10A$	
Forward Transfer Admittance	Y <sub>fs</sub>	_	13		S	$V_{DS} = -15V, I_{D} = -10A$	
Diode Forward Voltage	V <sub>SD</sub>	_	-0.7	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	2,230			$V_{DS} = -15V, V_{GS} = 0V$ f = 1.0MHz	
Output Capacitance	C <sub>oss</sub>	_	328		pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	294				
Gate Resistance	R <sub>G</sub>	_	6.4		Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	_	47				
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	23		nC		
Gate-Source Charge	Q <sub>gs</sub>	_	9.4	_	ne	V <sub>DS</sub> = -15V, I <sub>D</sub> = -10A	
Gate-Drain Charge	Q <sub>gd</sub>		5.6				
Turn-On Delay Time	t <sub>D(ON)</sub>	_	10.5	_			
Turn-On Rise Time	t <sub>R</sub>		8.5		nS	$V_{GS} = -10V, V_{DS} = -15V, R_G = 6\Omega$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>		90		113		
Turn-Off Fall Time	t <sub>F</sub>	_	40				

8. Short duration pulse test used to minimize self-heating effect.9. Guaranteed by design. Not subject to product testing. Notes:



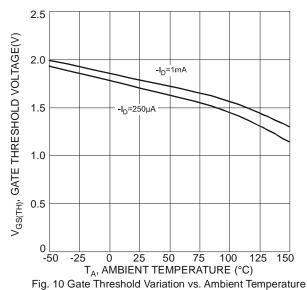
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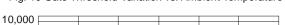


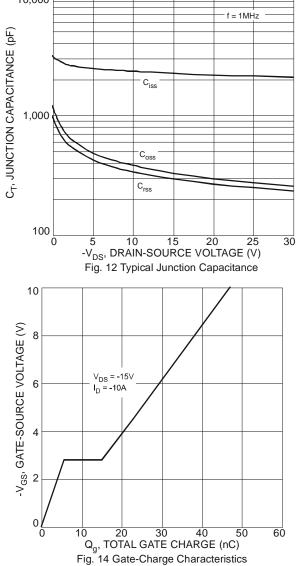


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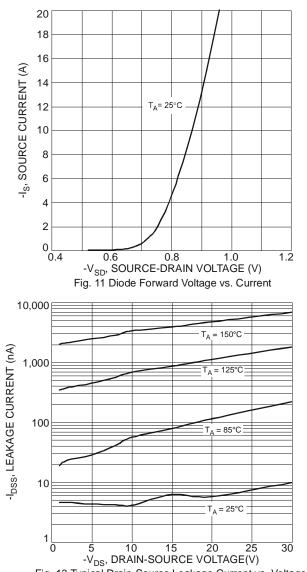
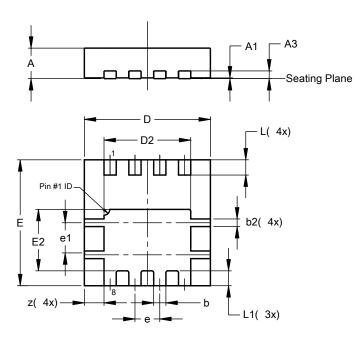


Fig. 13 Typical Drain-Source Leakage Current vs. Voltage



### **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



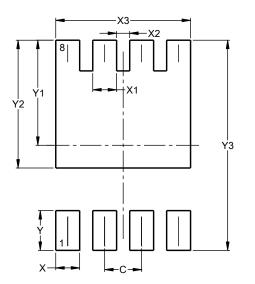
	PowerDI3333-8					
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	-	-	0.203			
b	0.27	0.37	0.32			
b2	_	_	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
ш	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
e	-	-	0.65			
e1	0.79	0.89	0.84			
L	0.35	0.45	0.40			
L1	_	_	0.39			
z	_	-	0.515			
All Dimensions in mm						

#### PowerDI3333-8

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### PowerDI3333-8



Dimensions	Value (in mm)
C	0.650
Х	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700



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