

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	Ι _D	7.5 6.1	A
	t<10s	T _A = +25°C T _A = +70°C	Ι _D	10 7.8	A
Maximum Continuous Body Diode Forward Current (Note 5)			ls	2.5	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	60	А
Avalanche Current (Note 6) L = 0.1mH			I _{AR}	14	А
Avalanche Energy (Note 6) L = 0.1mH			E _{AR}	10	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units	
Tatal Bower Dissingtion (Nato E)	T _A = +25°C	C	2.0	W	
Total Power Dissipation (Note 5)	T _A = +70°C	PD	1.3		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	61		
mermai Resistance, Junction to Ambient (Note 5)	t < 10s	$R_{ ext{ heta}JA}$	37	°C/W	
Thermal Resistance, Junction to Case	$R_{ ext{ heta}JC}$	6.4			
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to 150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

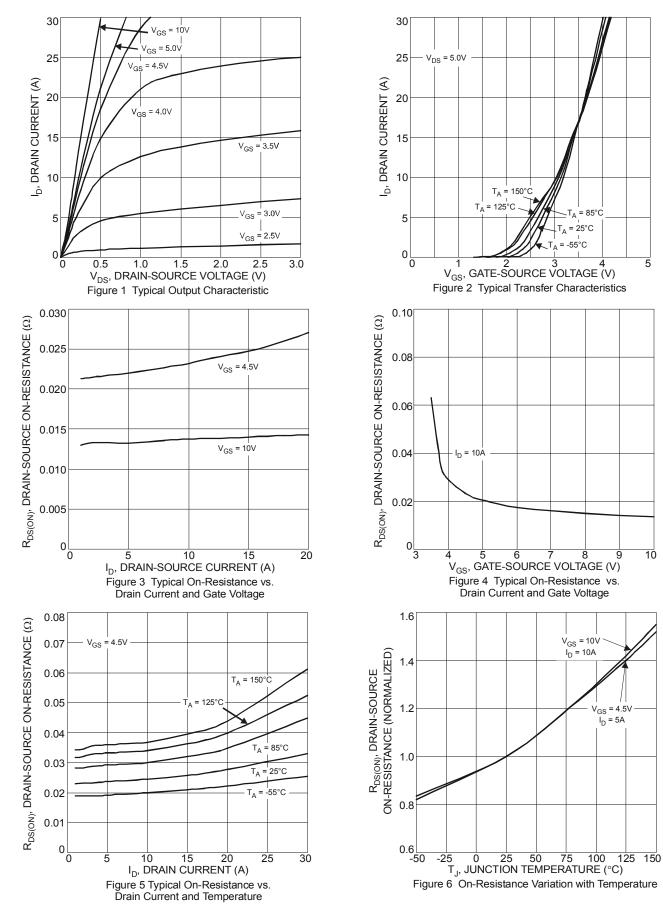
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	—		1	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	—	—	±1	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	0.8	—	2.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	_	_	14	18	mΩ	V _{GS} = 10V, I _D = 7.8A	
	R _{DS (ON)}	_	23	28		V _{GS} = 4.5V, I _D = 7.0A	
Forward Transfer Admittance	Y _{fs}	_	9	-	S	V _{DS} = 10V, I _D = 7.8A	
Diode Forward Voltage	V _{SD}	_	0.70	1.0	V	$V_{GS} = 0V, I_{S} = 6.3A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	605	_	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	74	_			
Reverse Transfer Capacitance	C _{rss}	_	58	_			
Gate resistance	Rg	_	1.5	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	5.3	_		V _{DS} = 15V, I _D = 7.8A	
Total Gate Charge (V _{GS} = 10V)	Qg	_	11.6	_	nC		
Gate-Source Charge	Qgs	_	2	_	nc		
Gate-Drain Charge	Q _{gd}	_	2.4	_			
Turn-On Delay Time	t _{D(on)}	_	3.8	_		V_{DD} = 15V, V_{GS} = 4.5V, R_{L} = 2.4 Ω , R_{G} = 1 Ω ,	
Turn-On Rise Time	tr	_	4.1	_	1		
Turn-Off Delay Time	t _{D(off)}	_	17.9		ns		
Turn-Off Fall Time	t _f	_	4.7				
Reverse Recovery Time	t _{rr}	_	5.5		ns		
Reverse Recovery Charge	Qrr	_	2.6	_	nC	I _F = 12A, di/dt = 500A/μs	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. 6. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$ 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing. Notes:



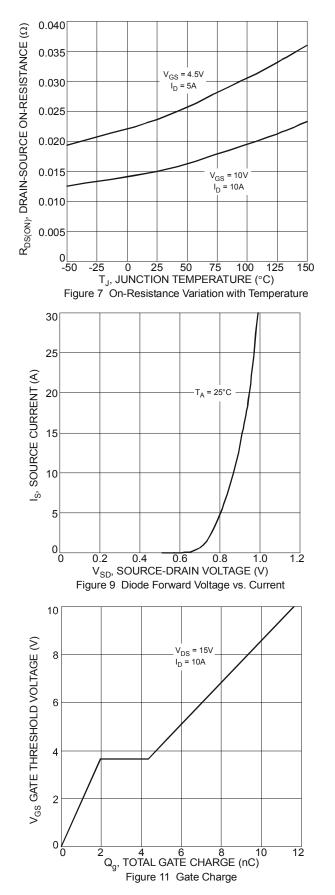
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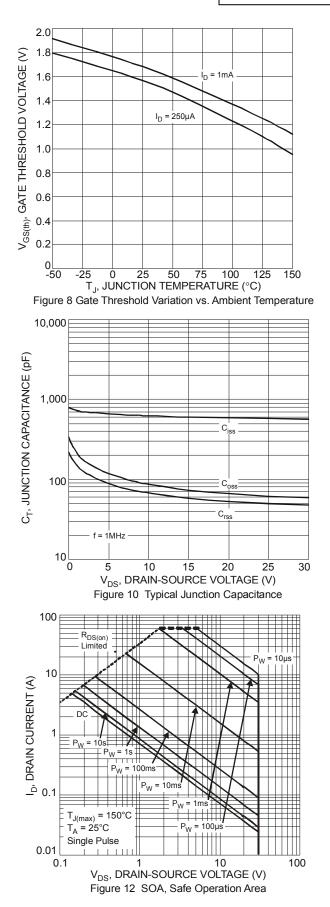
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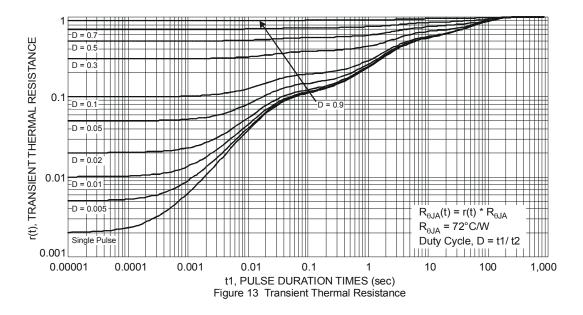




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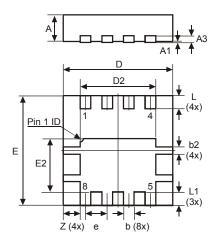
4 of 6 www.diodes.com





Package Outline Dimensions

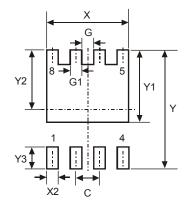
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



POWERDI3333-8						
Dim	Min	Max	Тур			
D	3.25	3.35	3.30			
ш	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
E2	1.56	1.66	1.61			
Α	0.75	0.85	0.80			
A1	0	0.05	0.02			
A3	-	-	0.203			
b	0.27	0.37	0.32			
b2	-	-	0.20			
L	0.35	0.45	0.40			
L1	_	_	0.39			
е	_	_	0.65			
Ζ	-	-	0.515			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.650
G	0.230
G1	0.420
Y	3.700
Y1	2.250
Y2	1.850
Y3	0.700
Х	2.370
X2	0.420

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