

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±20	V
Drain Current (Note 5)	I _D	16	A
Steady State		13	
Pulsed Drain Current (Note 6)	I _{DM}	64	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	2.5	W
Thermal Resistance, Junction to Ambient	R _{θJA}	50	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	1.1	—	2.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	—	9 13	mΩ	V _{GS} = 10V, I _D = 16A V _{GS} = 4.5V, I _D = 10A
Forward Transconductance	g _{fs}	—	16	—	S	V _{DS} = 10V, I _D = 12A
Diode Forward Voltage	V _{SD}	0.5	—	1.2	V	V _{GS} = 0V, I _S = 16A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	2096	—	pF	V _{DS} = 15V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	329	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	258	—	pF	
Gate Resistance	R _G	—	1.2	—	Ω	V _{GS} = 0V, f = 1MHz
SWITCHING CHARACTERISTICS (Note 8)						
Total Gate Charge	Q _g	—	22.4 43.7	—	nC	V _{DS} = 15V, V _{GS} = 4.5V, I _D = 16A V _{DS} = 15V, V _{GS} = 10.0V, I _D = 16A
Gate-Source Charge	Q _{gs}	—	5.5	—		V _{DS} = 15V, V _{GS} = 10V, I _D = 16A
Gate-Drain Charge	Q _{gd}	—	12.6	—		V _{DS} = 15V, V _{GS} = 10V, I _D = 16A
Turn-On Delay Time	t _{d(on)}	—	7.11	—	ns	V _{GS} = 10V, V _{DS} = 15V, R _D = 15Ω, R _G = 6Ω
Rise Time	t _r	—	10.3	—		
Turn-Off Delay Time	t _{d(off)}	—	58.3	—		
Fall Time	t _f	—	32.1	—		

- Notes:
- Device mounted on 2 oz. Copper pads on FR-4 PCB, with R_{θJA} = +50°C
 - Pulse width ≤10μs, Duty Cycle ≤1%.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

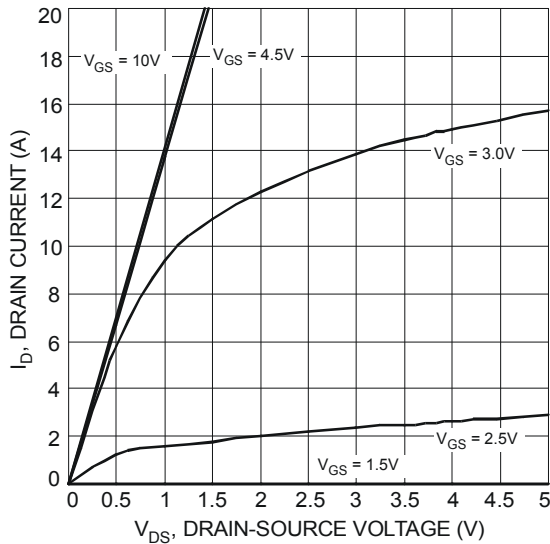


Fig. 1 Typical Output Characteristics

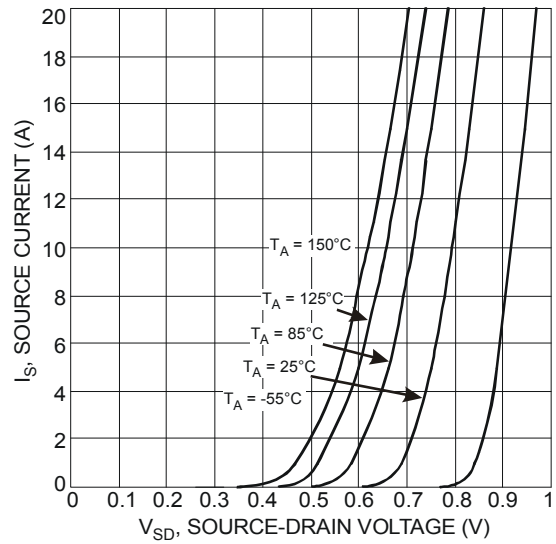


Fig. 2 Source Current vs. Source-Drain Voltage

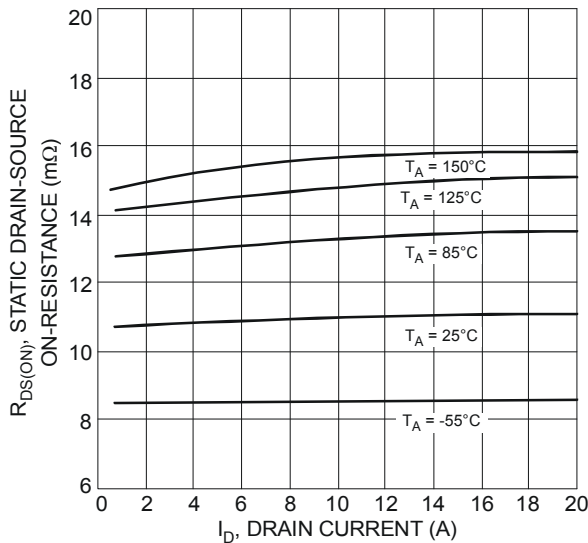


Fig. 3 Drain-Source On-Resistance vs. Drain Current

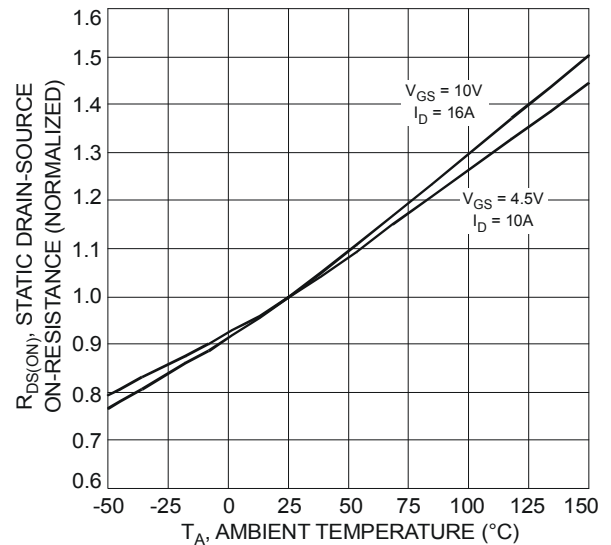


Fig. 4 On-Resistance Variation with Temperature

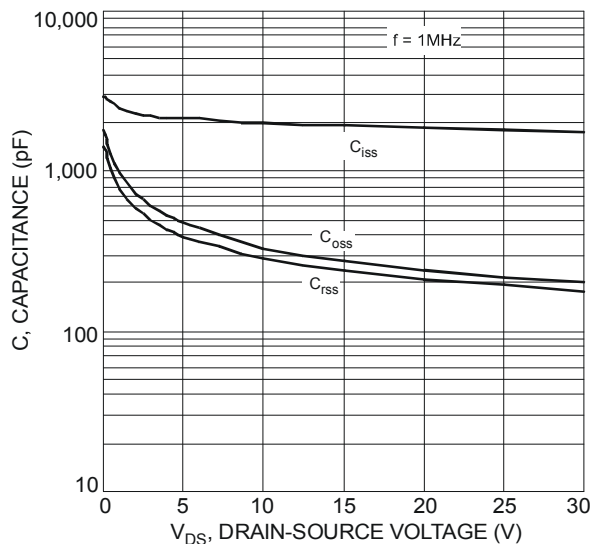


Fig. 5 Typical Capacitance

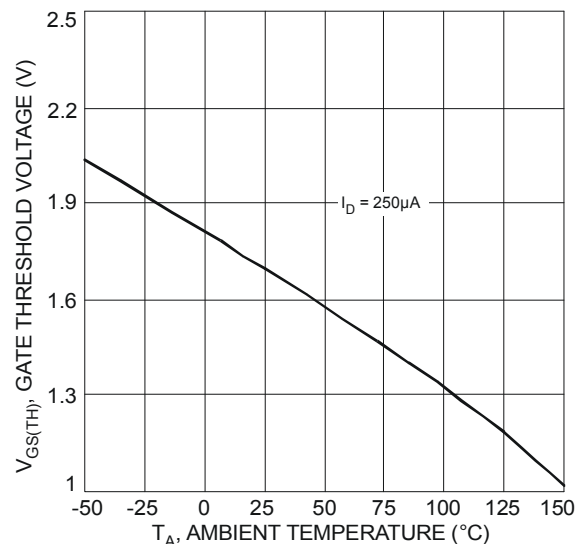


Fig. 6 Gate Threshold Variation vs. Ambient Temperature

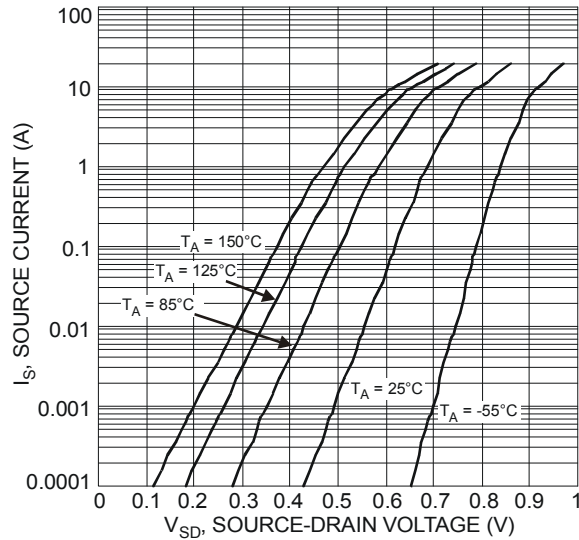


Fig. 7 Diode Forward Voltage vs. Current

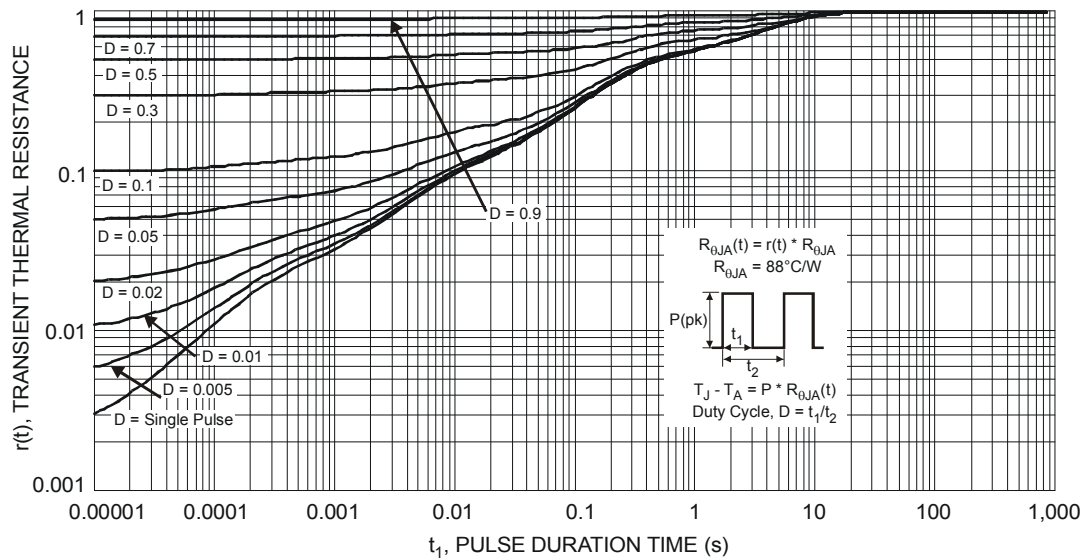
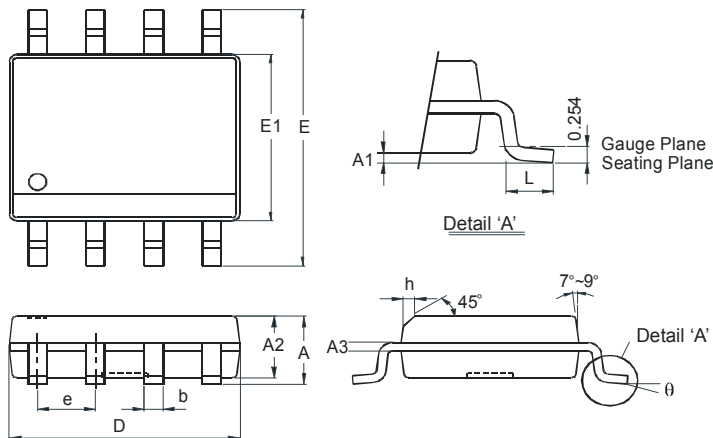


Fig. 8 Transient Thermal Response

Package Outline Dimensions

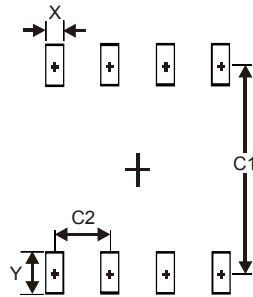
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version



SO-8		
Dim	Min	Max
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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