

### Maximum Ratings (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±8	V
Drain Current (Note 5)	Steady State	$T_A = +25$ °C $T_A = +85$ °C	I <sub>D</sub>	540 390	mA
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	1.5	A

## Thermal Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	200	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	625	°C/W
Operating and Storage Temperature Range	$T_{J_i}T_{STG}$	-55 to +150	°C

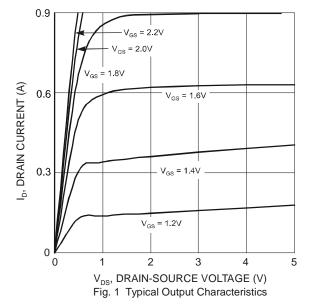
#### Electrical Characteristics (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20			V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±1	μA	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5	_	1.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
		_	0.4 0.5 0.7	0.55 0.70 0.9	Ω	$V_{GS} = 4.5V, I_D = 540mA$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>					$V_{GS} = 2.5V, I_D = 500mA$	
						$V_{GS} = 1.8V, I_D = 350mA$	
Forward Transfer Admittance	Y <sub>fs</sub>	200	_	_	mS	$V_{DS} = 10V, I_D = 0.2A$	
Diode Forward Voltage (Note 7)	$V_{SD}$	0.5	_	1.4	V	$V_{GS} = 0V, I_S = 115mA$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C <sub>iss</sub>	1	36	150	pF	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V f = 1.0MHz	
Output Capacitance	Coss	1	5.7	25	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	4.2	20	pF	1 - 1.01/11/2	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	0.53	_			
Total Gate Charge (V <sub>GS</sub> = 8.0V)	$Q_g$	_	0.95	_	nC	V <sub>DS</sub> = 10V, I <sub>D</sub> = 250mA	
Gate-Source Charge	$Q_{gs}$	_	0.08	_			
Gate-Drain Charge	$Q_{gd}$	_	0.07	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	4.1	_	ns	$V_{DD} = 10V, R_L = 47\Omega,$	
Turn-On Rise Time	t <sub>R</sub>	_	7.3	_	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>		13.8		ns		
Turn-Off Fall Time	t <sub>F</sub>		10.5	_	ns		

Notes:

- 5. Device mounted on FR-4 PCB. 6. Pulse width ≤10µs, Duty Cycle ≤1%.
- 7. Short duration pulse test used to minimize self-heating effect.





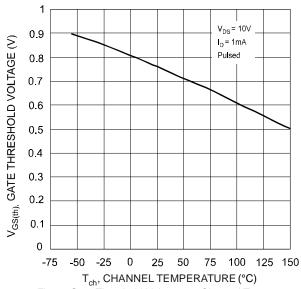


Fig. 3 Gate Threshold Voltage vs. Channel Temperature

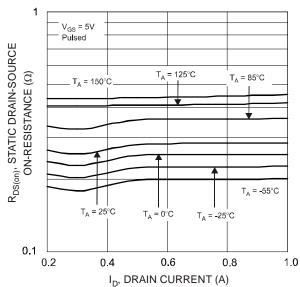
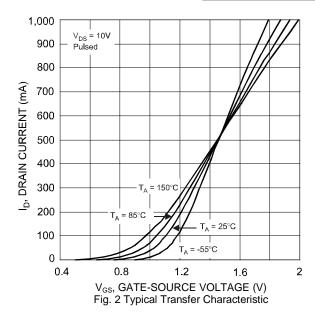


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current



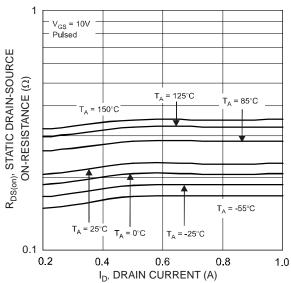


Fig. 4 Static Drain-Source On-Resistance Vs. Drain Current

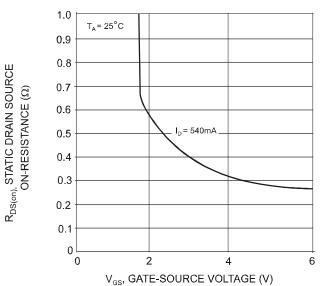


Fig. 6 Static Drain-Source, On-Resistance vs. Gate-Source Voltage



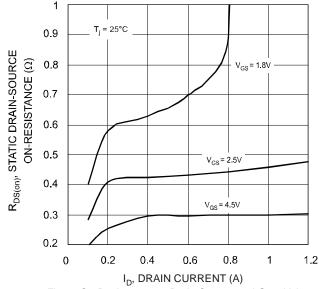
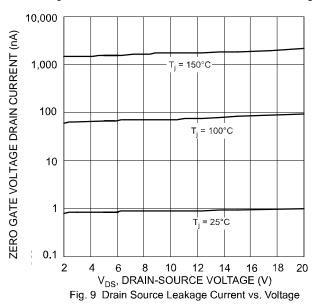


Fig. 7 On-Resistance vs. Drain Current and Gate Voltage



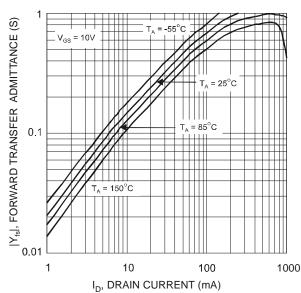


Fig. 11 Forward Transfer Admittance vs. Drain Current

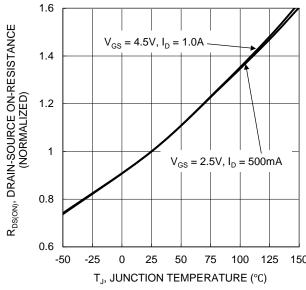
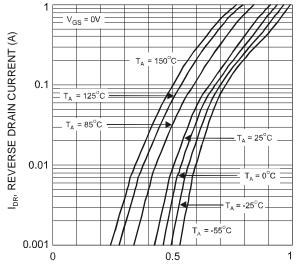
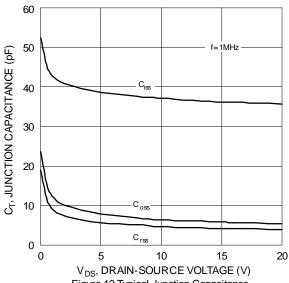


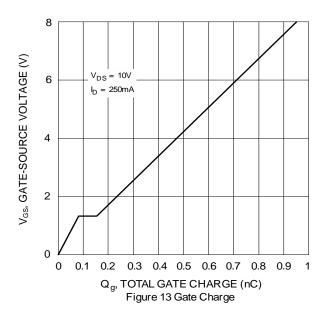
Fig. 8 On-Resistance Variation with Temperature

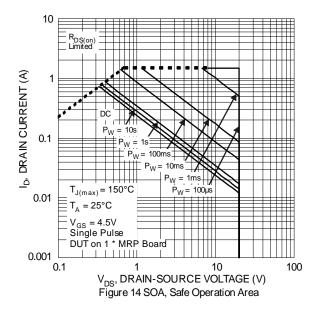


V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Fig. 10 Reverse Drain Current vs. Source-Drain Voltage





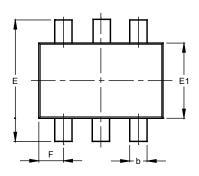


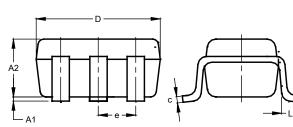




### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

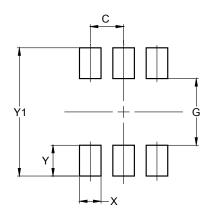




SOT363				
Dim	Min	Max	Тур	
A1	0.00	0.10	0.05	
A2	0.90	1.00	1.00	
b	0.10	0.30	0.25	
С	0.10	0.22	0.11	
D	1.80	2.20	2.15	
E	2.00	2.20	2.10	
E1	1.15	1.35	1.30	
е	0.650 BSC			
F	0.40	0.45	0.425	
L	0.25	0.40	0.30	
а	0°	8°		
All Dimensions in mm				

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Υ	0.600
Y1	2.500



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