

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	400	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage (Note 9)	V _R		
RMS Reverse Voltage	V _{R(RMS)}	280	V
Average Rectified Output Current	I _O	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	30	A

Thermal Characteristics

Characteristic	Symbol	Typ	Max	Unit
Power Dissipation (Note 5) @T _A = +25°C	P _D	—	1.0	W
Thermal Resistance Junction to Ambient (Note 5) @T _A = +25°C	R _{θJA}	117	—	°C/W
Thermal Resistance Junction to Soldering Point (Note 7)	R _{θJS}	—	6	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150		°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Minimum Reverse Breakdown Voltage @I _R = 5μA	V _{(BR)R}	400	V
Maximum Forward Voltage Drop @I _F = 1.0A	V _{FM}	1.25	V
Peak Reverse Current @T _A = +25°C	I _{RM}	5.0	μA
at Rated DC Blocking Voltage (Note 9) @T _A = +100°C		200	
Maximum Reverse Recovery Time (Note 8)	t _{rr}	25	ns
Typical Total Capacitance (f = 1MHz, V _R = 4VDC)	C _T	14	pF

Notes: 5. Device mounted on 1" x 1", Polyimide PCB; 2 oz. Cu pad layout as shown on Diodes Inc. suggested pad layout document AP02001.pdf.

6. RoHS revision 13.2.2003. Glass and high temperature solder exemptions applied; see *EU Directive Annex Notes 5 and 7*.

7. Theoretical R_{θJS} calculated from the top center of the die straight down to the PCB cathode tab solder junction.

8. Measured with I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A.

9. Short duration pulse test used to minimize self-heating effect.

10. Device mounted on FR-4 PCB, 2oz. Cu pad layout as shown on Diodes Inc. suggested pad layout document AP02001.pdf.

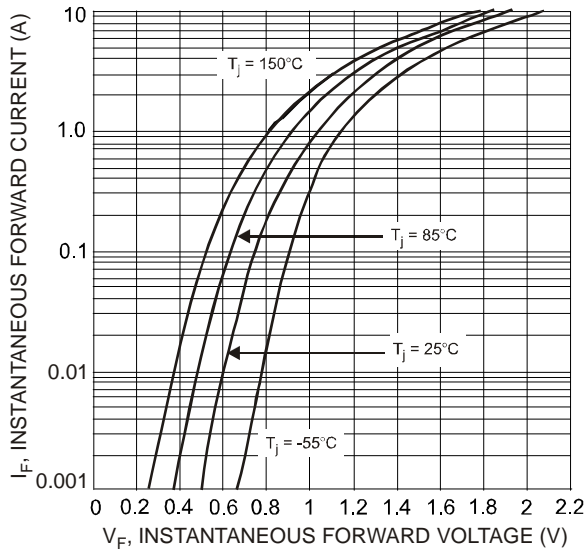


Fig. 1 Typical Forward Characteristics

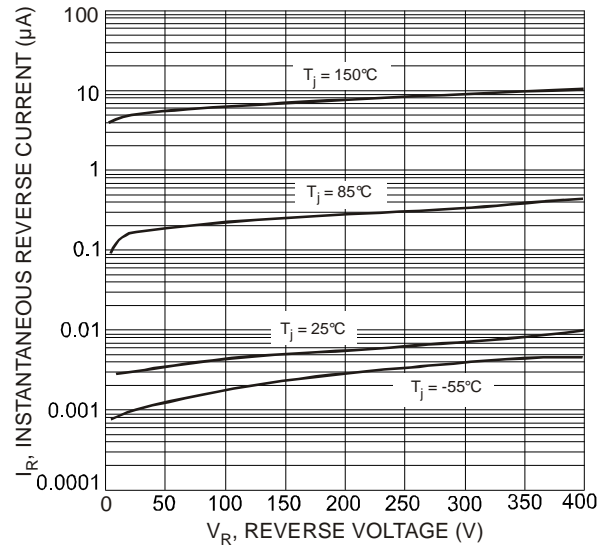


Fig. 2 Typical Reverse Characteristics

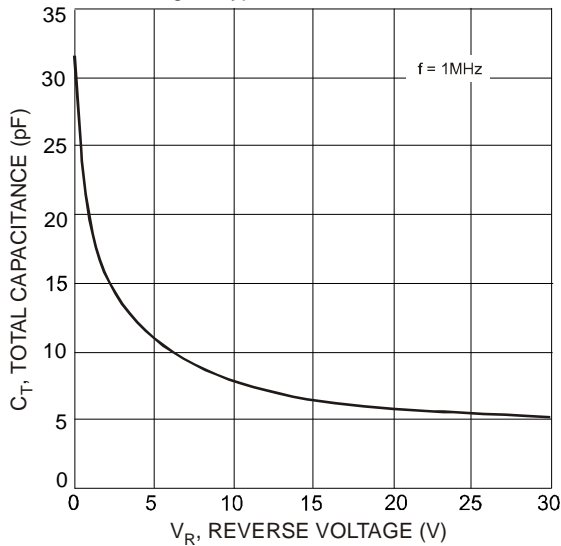


Fig. 3 Typical Total Capacitance

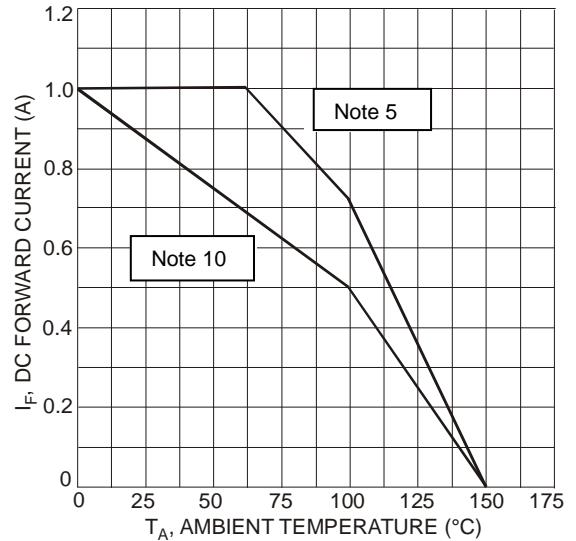


Fig. 4 DC Forward Current Derating

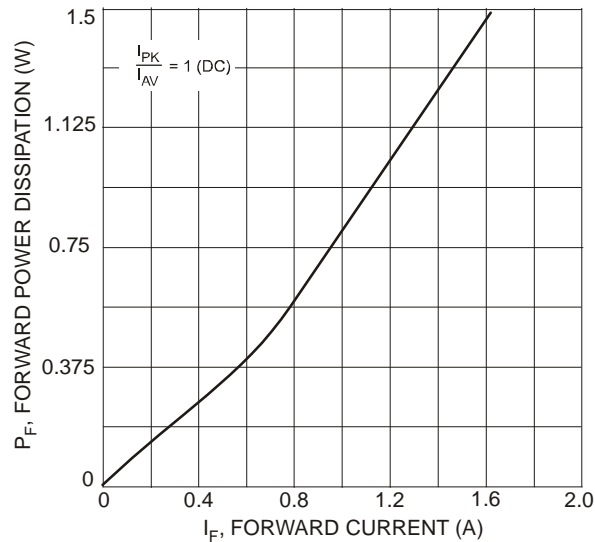


Fig. 5 Forward Power Dissipation

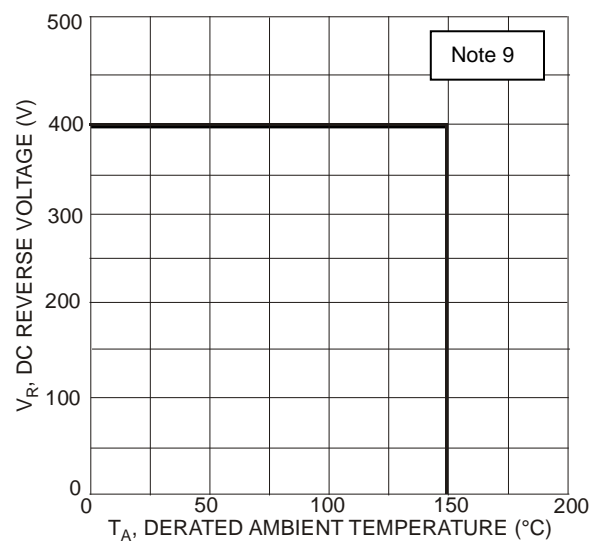
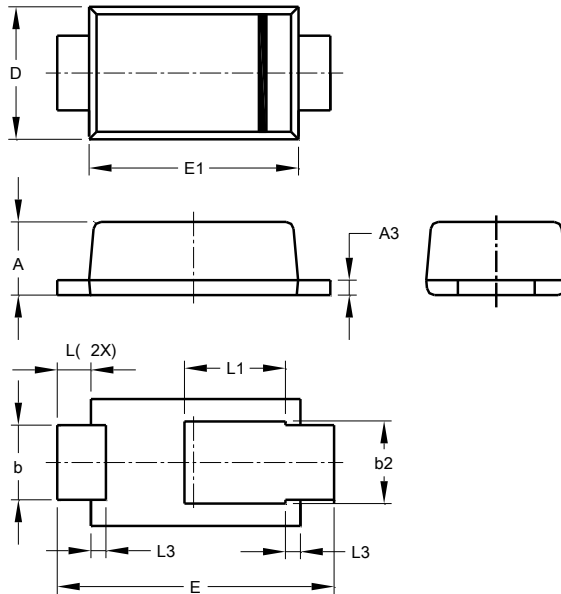


Fig. 6 Operating Temperature Derating

Package Outline Dimensions

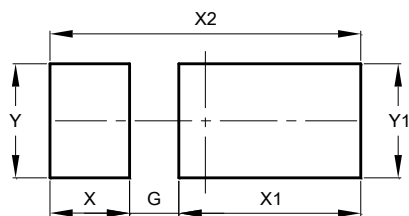
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



POWERDI [®] 123			
Dim	Min	Max	Typ
A	0.93	1.00	0.98
A3	0.15	0.25	0.20
b	0.85	1.25	1.00
b2	1.025	1.125	1.10
D	1.63	1.93	1.78
E	3.50	3.90	3.70
E1	2.60	3.00	2.80
L	0.40	0.50	0.45
L1	1.25	1.40	1.35
L3	0.125	0.275	0.20
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50

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