

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P _{PP}	130	W	8/20μs, per Fig. 1
Peak Pulse Current	I _{PP}	12	A	8/20μs, per Fig. 1
ESD Protection – Contact Discharge	V _{ESD, Contact}	±30	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	V _{ESD, Air}	±30	kV	IEC 61000-4-2 Standard

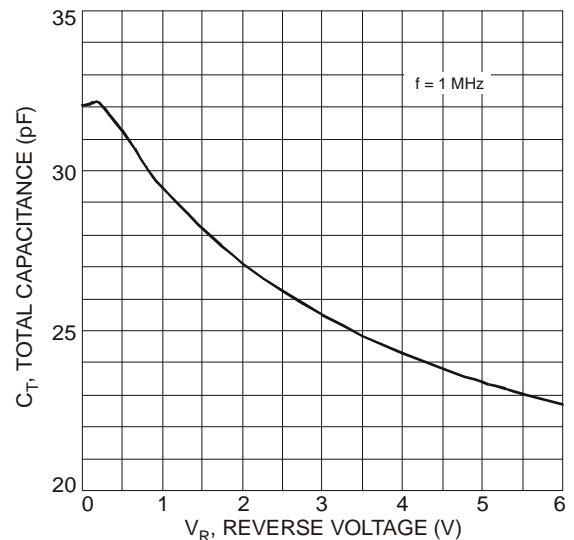
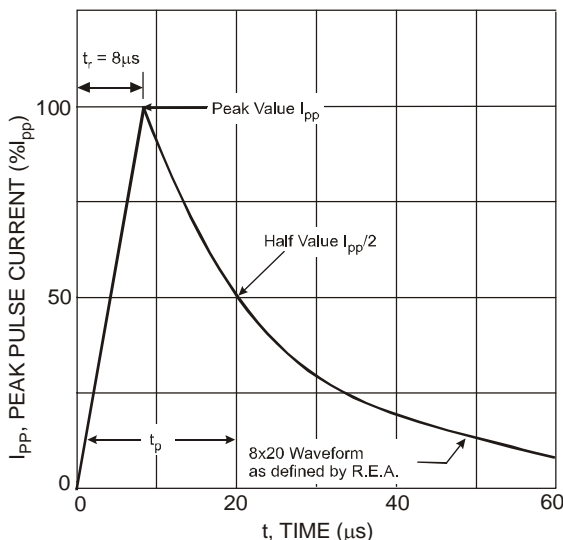
Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P _D	200	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Standoff Voltage	V _{RWM}	-	-	5	V	-
Channel Leakage Current (Note 6)	I _{RM}	-	5	100	nA	V _{RWM} = 5V
Clamping Voltage	V _{CL}	-	-	10 14	V	I _{PP} = 1A, t _p = 8/20μs I _{PP} = 12A, t _p = 8/20μs
Breakdown Voltage	V _{BR}	5.5	-	9.5	V	I _R = 1mA
Differential Resistance	R _{DIFF}	-	0.4	-	Ω	I _R = 10A, t _p = 8/20μs
Channel Input Capacitance	C _T	-	35	45	pF	V _R = 0V, f = 1MHz

- Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at <http://www.diodes.com>.
 6. Short duration pulse test used to minimize self-heating effect.



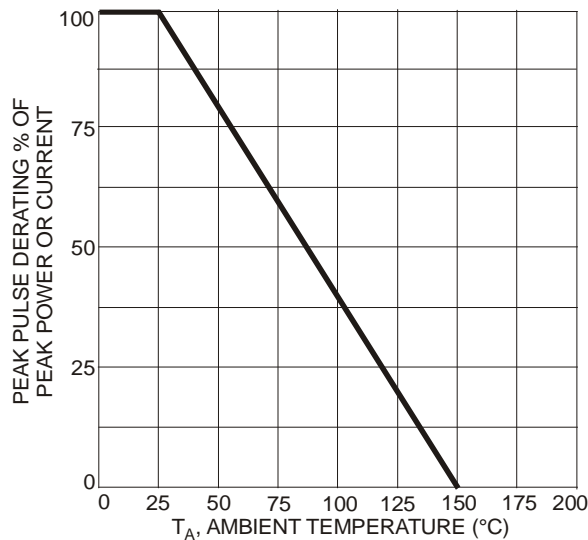


Fig. 3 Power Dissipation vs. Ambient Temperature

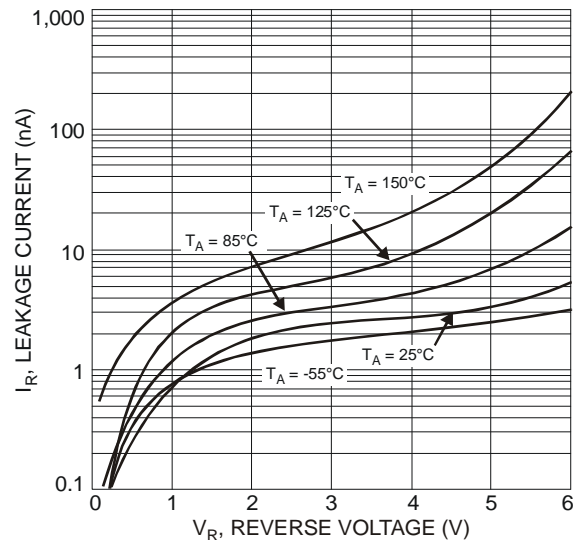
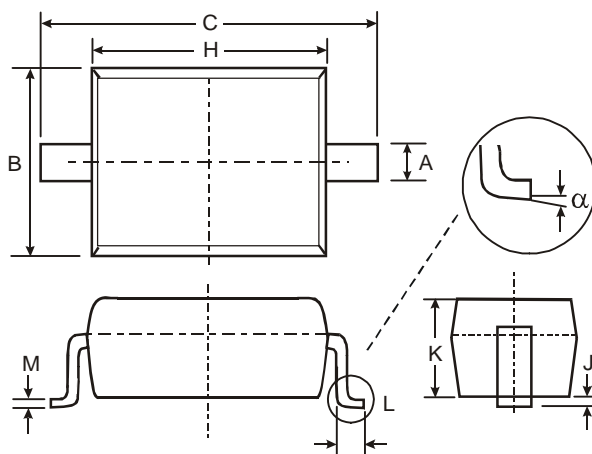


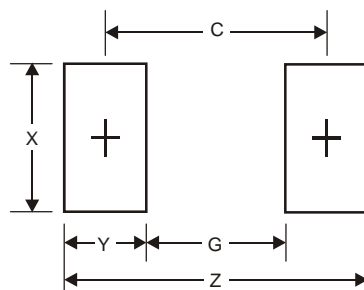
Fig. 4 Typical Reverse Characteristics

Package Outline Dimensions



SOD323		
Dim	Min	Max
A	0.25	0.35
B	1.20	1.40
C	2.30	2.70
H	1.60	1.80
J	0.00	0.10
K	1.0	1.1
L	0.20	0.40
M	0.10	0.15
α	0°	8°
All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.75
G	1.05
X	0.65
Y	1.35
C	2.40

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