Electrical Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V_F	Forward Voltage	1.5 2.0	1.8 2.4	V	I_F = 10 A T_J=25°C I_F = 10 A T_J=175°C	Fig. 1
I_R	Reverse Current	12 24	60 220	µA	V_R = 650 V T_J=25°C V_R = 650 V T_J=175°C	Fig. 2
Q_C	Total Capacitive Charge	24		nC	V_R = 400 V, I_F = 10 A di/dt = 500 A/µs T_J = 25°C	Fig. 5
C	Total Capacitance	460.5 44 40		pF	V_R = 0 V, T_J = 25°C, f = 1 MHz V_R = 200 V, T_J = 25°C, f = 1 MHz V_R = 400 V, T_J = 25°C, f = 1 MHz	Fig. 6
E_C	Capacitance Stored Energy	3.6		µJ	V_R = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Symbol	Parameter	Typ.	Unit	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.0	°C/W	Fig. 9

Typical Performance

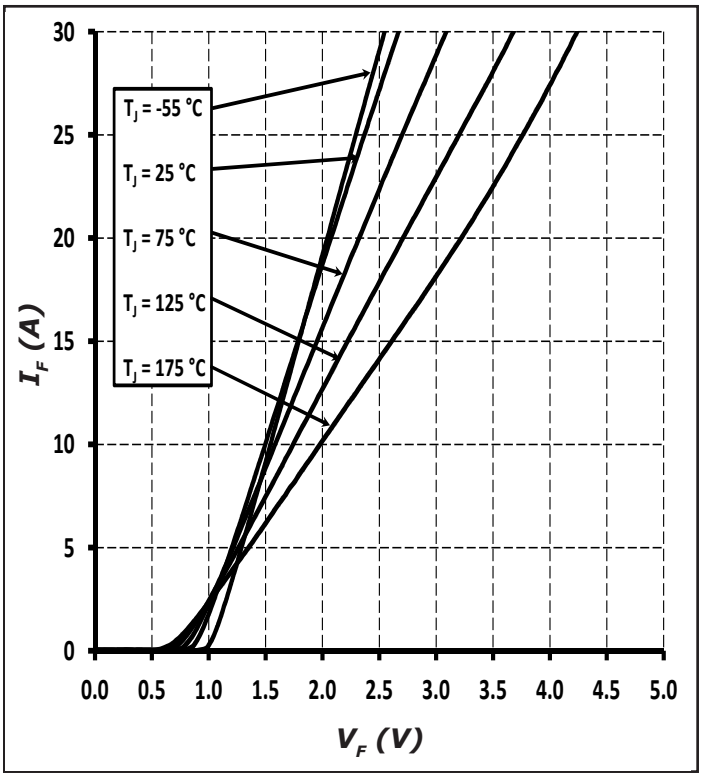


Figure 1. Forward Characteristics

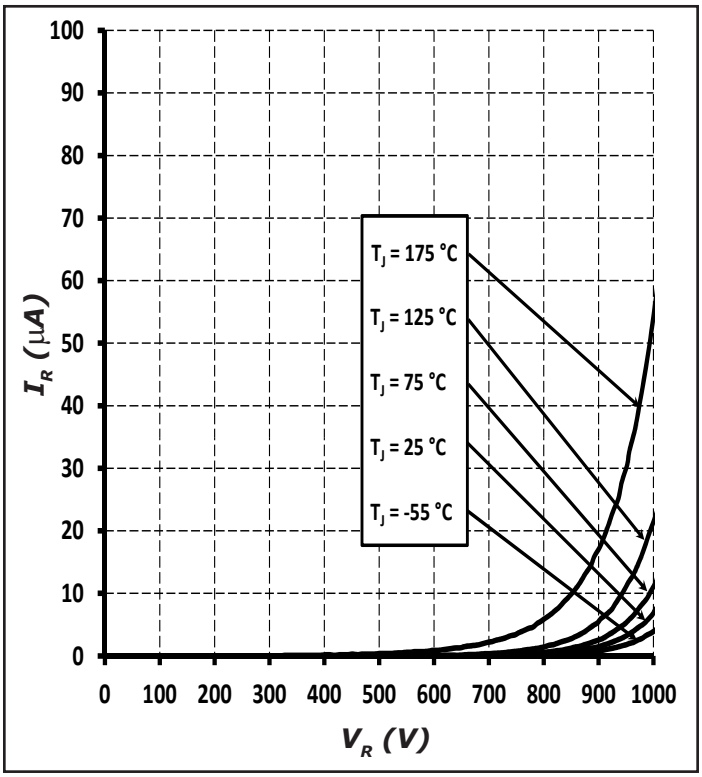


Figure 2. Reverse Characteristics

Typical Performance

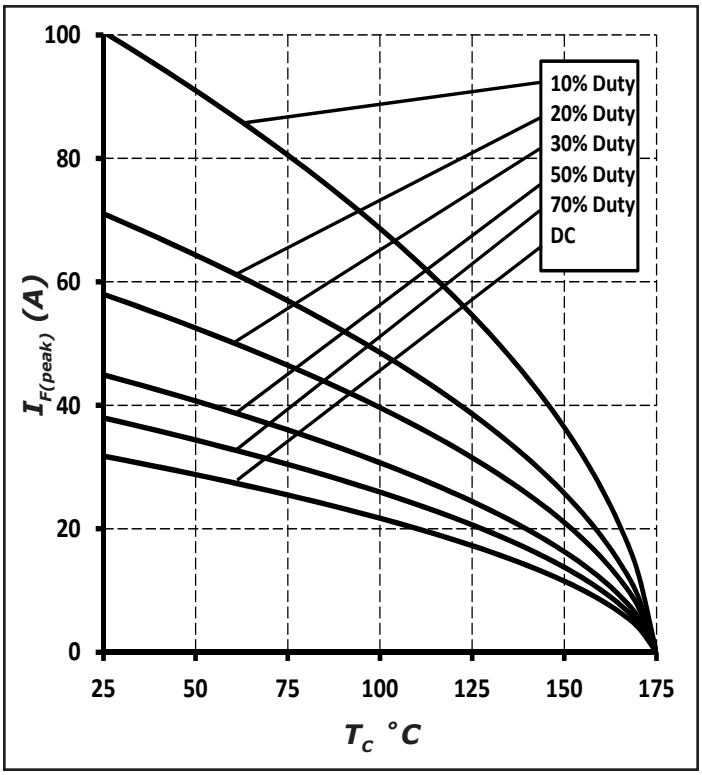


Figure 3. Current Derating

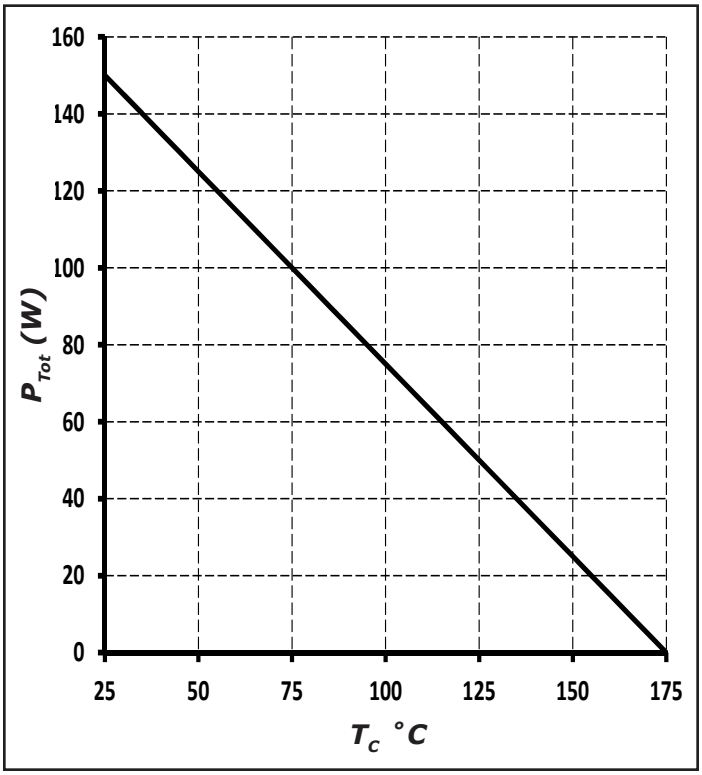


Figure 4. Power Derating

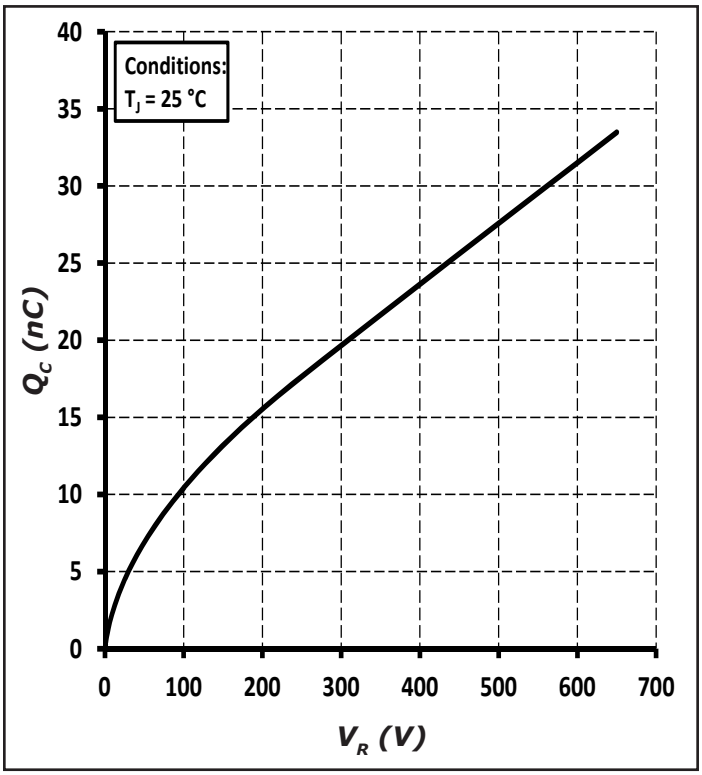


Figure 5. Total Capacitance Charge vs. Reverse Voltage

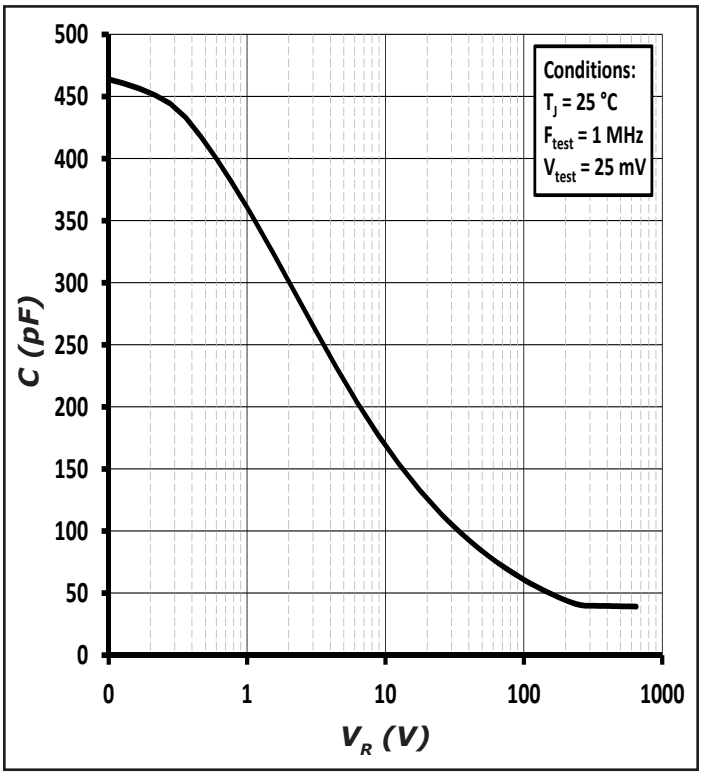


Figure 6. Capacitance vs. Reverse Voltage

Typical Performance

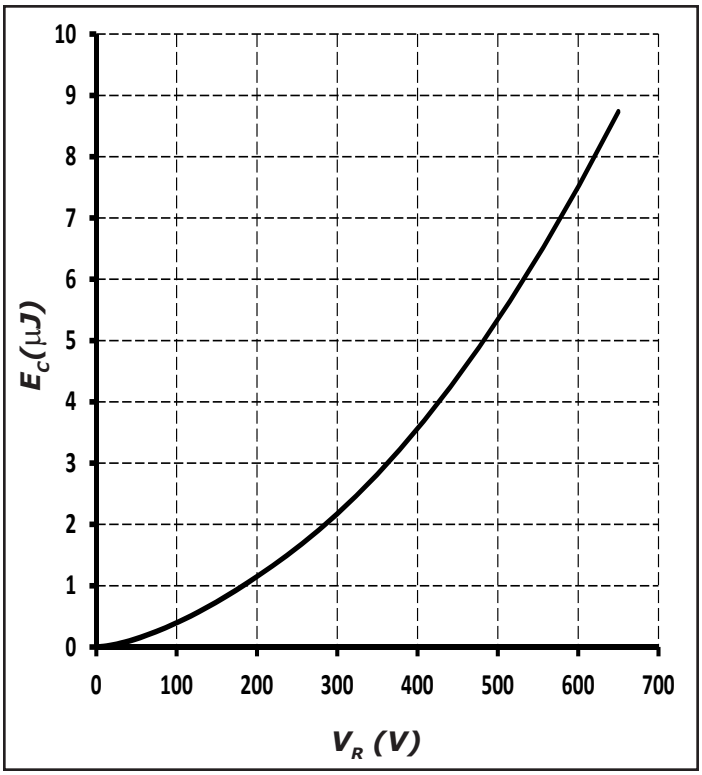


Figure 7. Capacitance Stored Energy

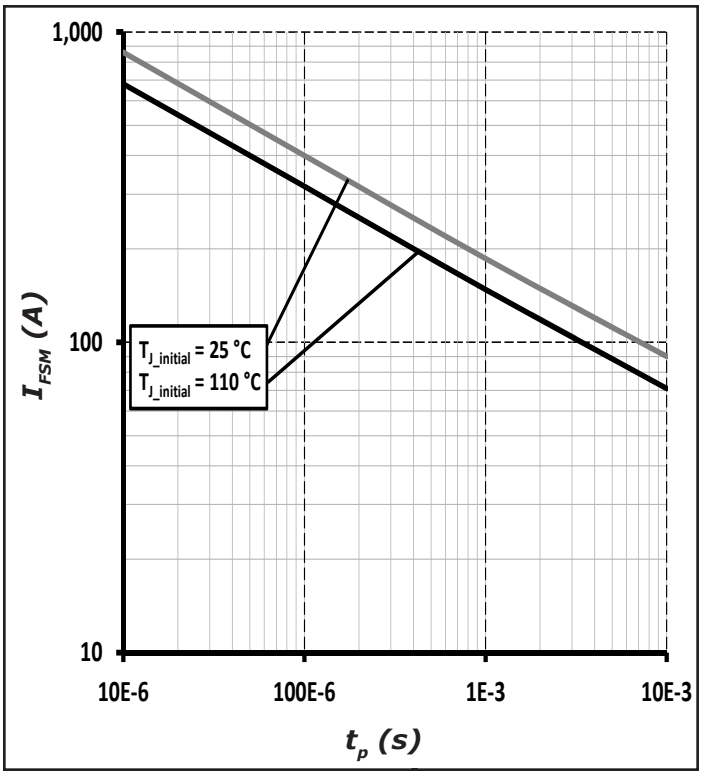


Figure 8. Non-repetitive peak forward surge current versus pulse duration (sinusoidal waveform)

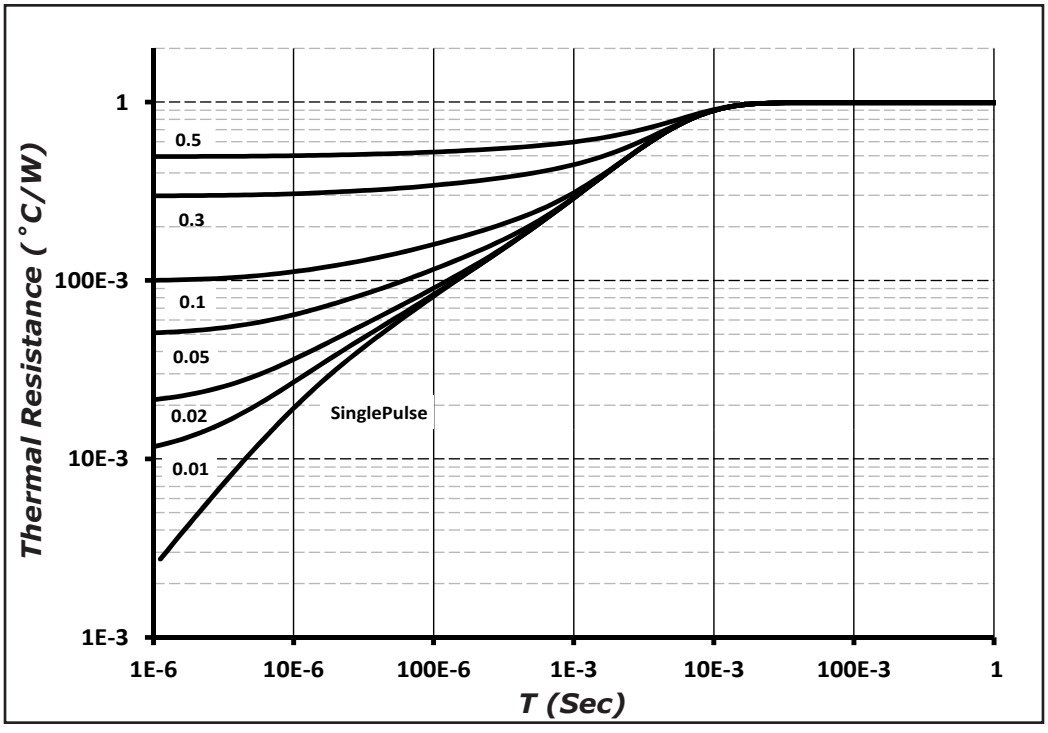
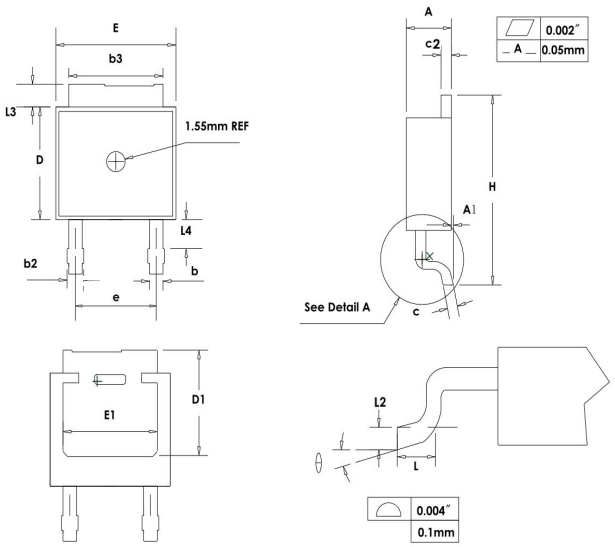


Figure 9. Transient Thermal Impedance

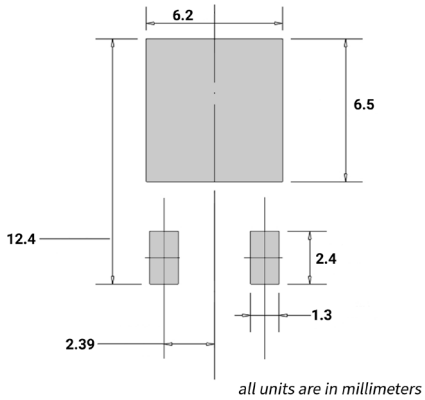
Package Dimensions

Package TO-252-2



SYMBOL	MILLIMETERS	
	MIN	MAX
A	2.159	2.413
A1	0	0.13
b	0.64	0.89
b2	0.653	1.143
b3	5.004	5.6
c	0.457	0.61
c2	0.457	0.864
D	5.867	6.248
D1	5.21	-
E	6.35	6.73
E1	4.32	-
e	4.58 BSC	
H	9.65	10.414
L	1.106	1.78
L2	0.51 BSC	
L3	0.889	1.27
L4	0.64	1.01
Θ	0°	8°

Recommended Solder Pad Layout

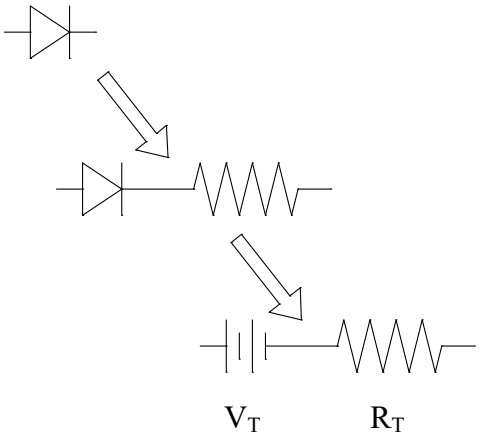


TO-252-2

Part Number	Package	Marking
C3D10065E	TO-252-2	C3D10065

Note: Recommended soldering profiles can be found in the applications note here:
http://www.wolfspeed.com/power_app_notes/soldering

Diode Model



$$Vf_T = V_T + If * R_T$$

$$V_T = 0.94 + (T_J * -1.3*10^{-3})$$
$$R_T = 0.044 + (T_J * 4.4*10^{-4})$$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Notes

- **RoHS Compliance**
  The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Wolfspeed representative or from the Product Ecology section of our website at http://www.wolfspeed.com/power/tools-and-support/product-ecology.

- **REACh Compliance**
  REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future,please contact a Cree representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

- This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

Related Links

- Cree SiC Schottky diode portfolio: http://www.wolfspeed.com/Power/Products#SiCSchottkyDiodes
- Schottky diode Spice models: http://www.wolfspeed.com/power/tools-and-support/DIODE-model-request2
- SiC MOSFET and diode reference designs: http://go.pardot.com/l/101562/2015-07-31/349i

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