## BT1308W-600D

#### 4Q Triac

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	2	5	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G+; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	4	7	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	1	10	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 0.85 A; T <sub>j</sub> = 25 °C; <u>Fig. 12</u>	-	1.35	1.6	V
Dynamic char	racteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; $T_j$ = 110 °C; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit	30	45	-	V/µs
dV <sub>com</sub> /dt	rate of change of commutating voltage	$V_{\rm D}$ = 600 V; $T_{\rm j}$ = 50 °C; dI_{com}/dt = 0.3 A/ ms; I_T = 0.84 A; gate open circuit	-	5	-	V/µs

# 5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	T1	main terminal 1	4	T2-71			
2	T2	main terminal 2		G sym051			
3	G	gate		Symoor			
4	T2	main terminal 2	∐1 ∐2 ∐3 SC-73 (SOT223)				

# 6. Ordering information

#### Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BT1308W-600D	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223		

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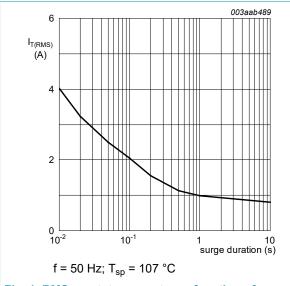
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## 7. Limiting values

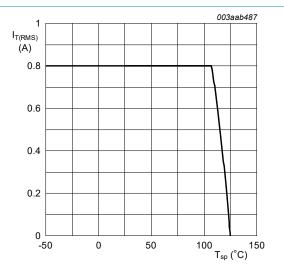
#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage		-	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; $T_{sp} \le 107 \text{ °C}$ ; <u>Fig. 1</u> ; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	0.8	A
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 20 ms; <u>Fig. 4; Fig. 5</u>	-	9	A
		full sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 16.7 ms	-	10	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; SIN	-	0.32	A²s
dl <sub>T</sub> /dt	rate of rise of on-state	I <sub>G</sub> = 20 mA; T2+ G+	-	50	A/µs
	current	I <sub>G</sub> = 20 mA; T2+ G-	-	50	A/µs
		I <sub>G</sub> = 20 mA; T2- G-	-	50	A/µs
		I <sub>G</sub> = 20 mA; T2- G+	-	10	A/µs
I <sub>GM</sub>	peak gate current		-	1	А
P <sub>GM</sub>	peak gate power		-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.1	W
T <sub>stg</sub>	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

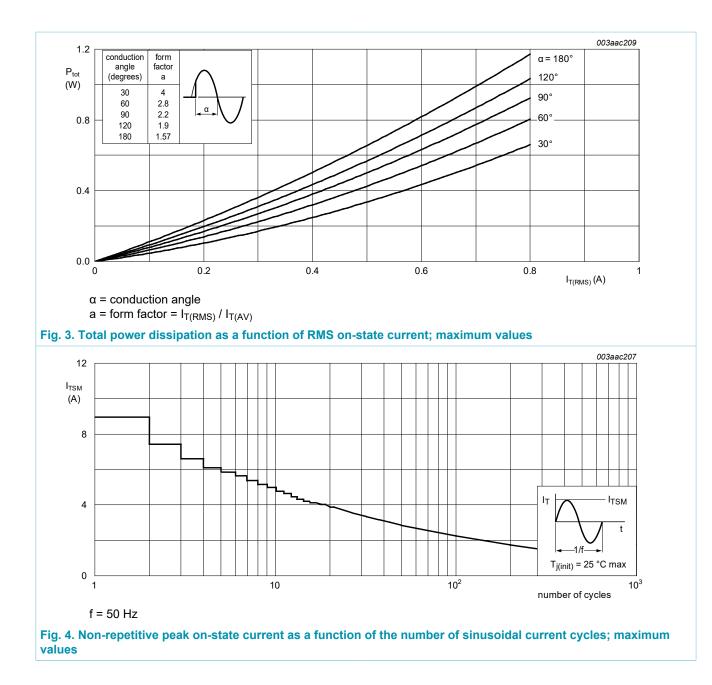








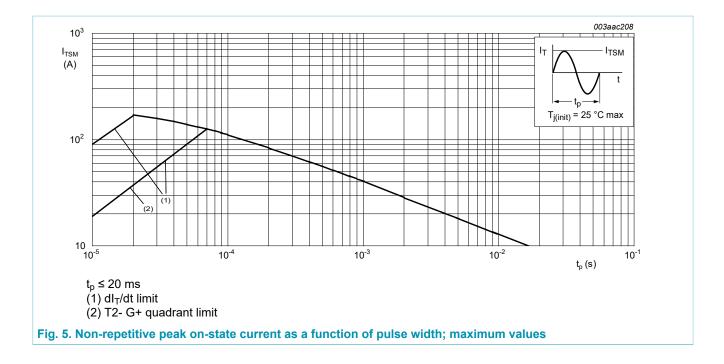
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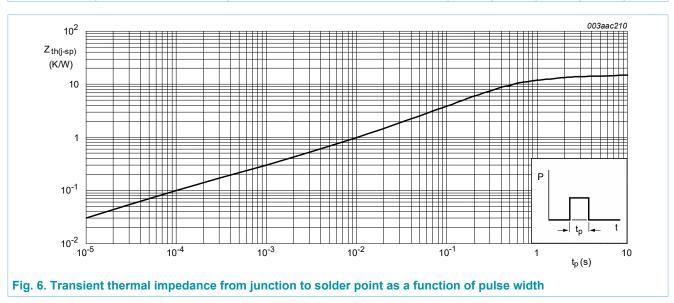
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### 8. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point	full cycle; <u>Fig. 6</u>	-	-	15	K/W
R <sub>th(j-a)</sub>	thermal resistance	full cycle; for minimum footprint; Fig. 7	-	156	-	K/W
	from junction to ambient free air	full cycle; for pad area; <u>Fig. 8</u>	-	70	-	K/W

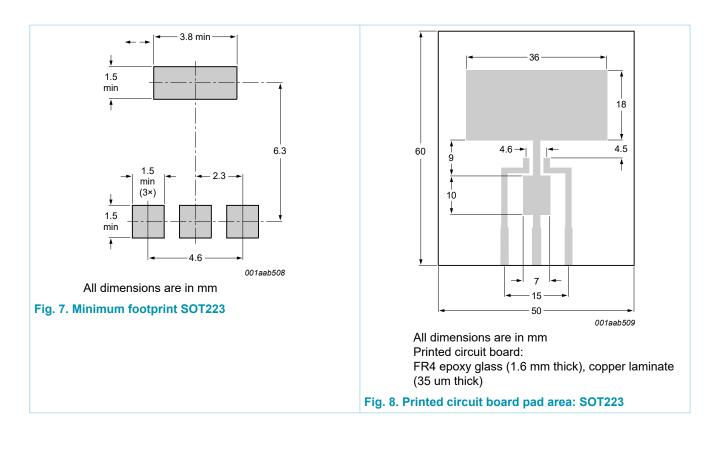


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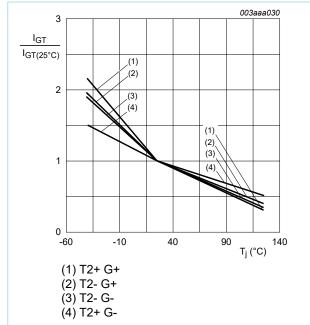
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### 9. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	1	5	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	2	5	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	2	5	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G+; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	4	7	mA
ΙL	latching current	V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1	10	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	5	10	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1	10	mA
		V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.1 A; T2- G+; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	2	10	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	1	10	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 0.85 A; T <sub>j</sub> = 25 °C; <u>Fig. 12</u>	-	1.35	1.6	V
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 25 °C; <u>Fig. 13</u>	-	0.9	1.5	V
		V <sub>D</sub> = 600 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 110 °C; <u>Fig. 13</u>	0.1	0.7	-	V
D	off-state current	V <sub>D</sub> = 600 V; T <sub>j</sub> = 125 °C	-	0.1	0.5	mA
Dynamic ch	naracteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; T <sub>j</sub> = 110 °C; (V <sub>DM</sub> = 67% of V <sub>DRM</sub> ); exponential waveform; gate open circuit	30	45	-	V/µs
dV <sub>com</sub> /dt	rate of change of commutating voltage	$V_{D}$ = 600 V; $T_{j}$ = 50 °C; $dI_{com}/dt$ = 0.3 A/ ms; $I_{T}$ = 0.84 A; gate open circuit	-	5	-	V/µs
t <sub>gt</sub>	gate-controlled turn-on time	$I_{TM}$ = 1 A; $V_D$ = 600 V; $I_G$ = 25 mA; $dI_G/$ dt = 5 A/µs	-	2	-	μs

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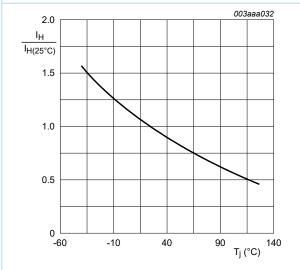
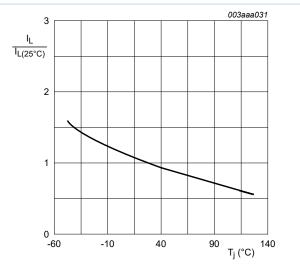
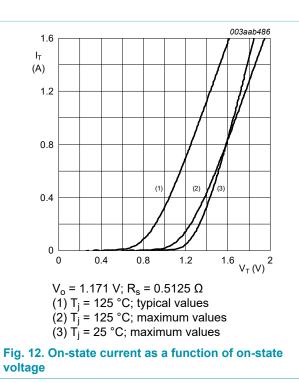


Fig. 11. Normalized holding current as a function of junction temperature



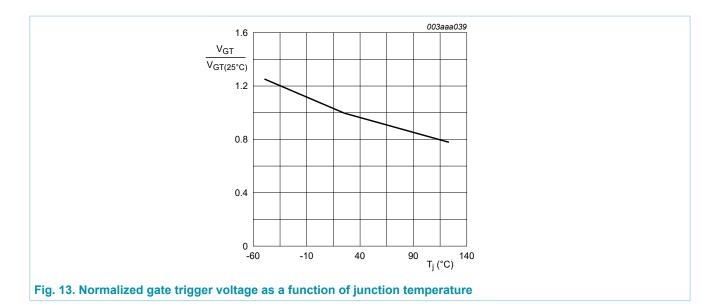




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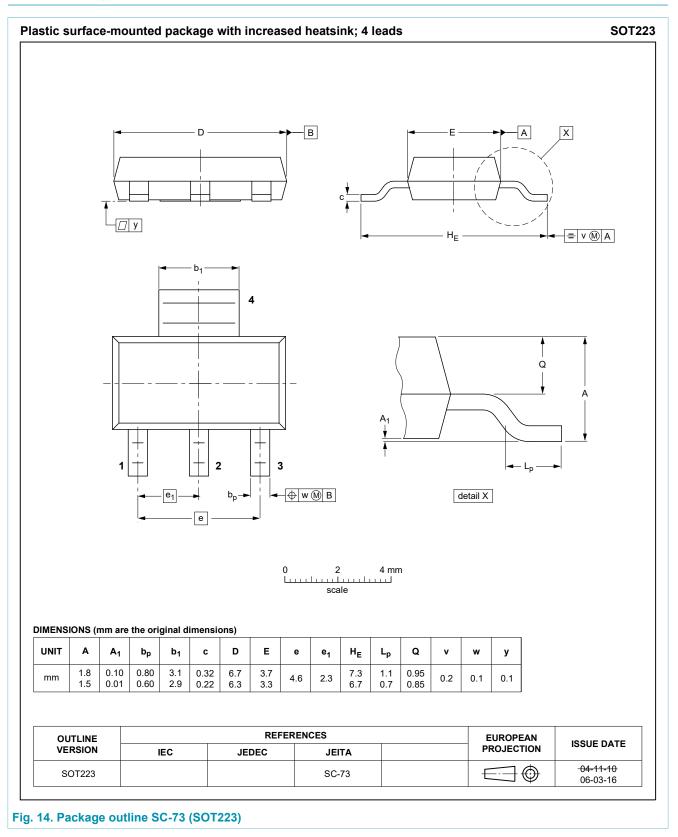


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### 10. Package outline



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## 11. Legal information

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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