

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Drain-Gate Voltage R <sub>GS</sub> ≤ 1.0MΩ	V <sub>DGR</sub>	60	V
Gate-Source Voltage Continuous	V <sub>GSS</sub>	±20	V
Drain Current (Note 5) Continuous	I <sub>D</sub>	250	mA

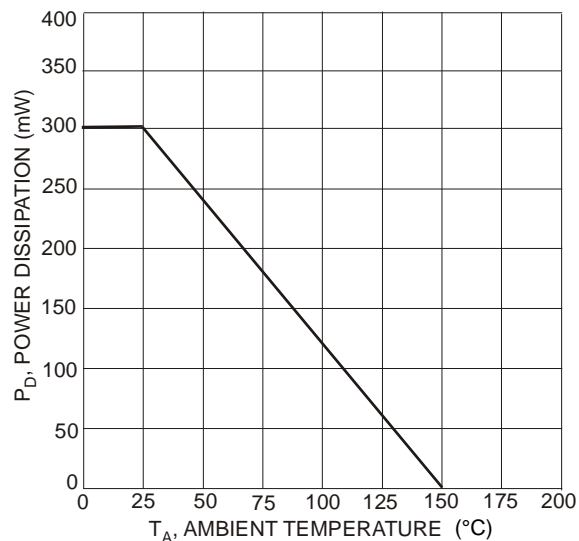
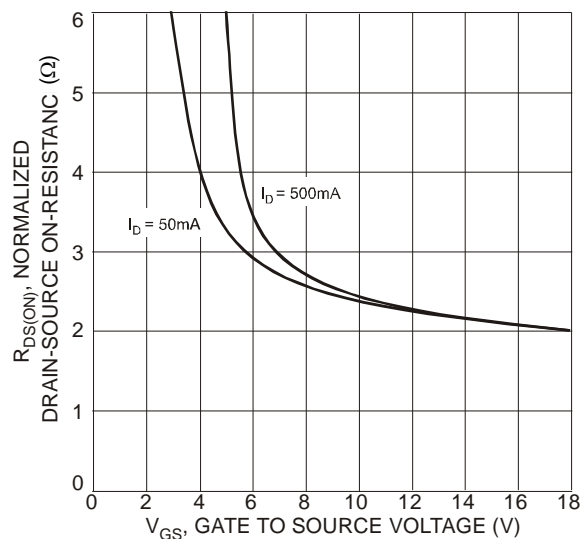
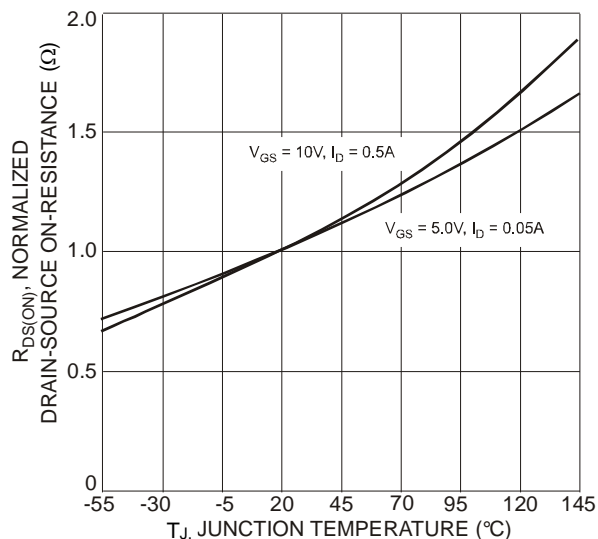
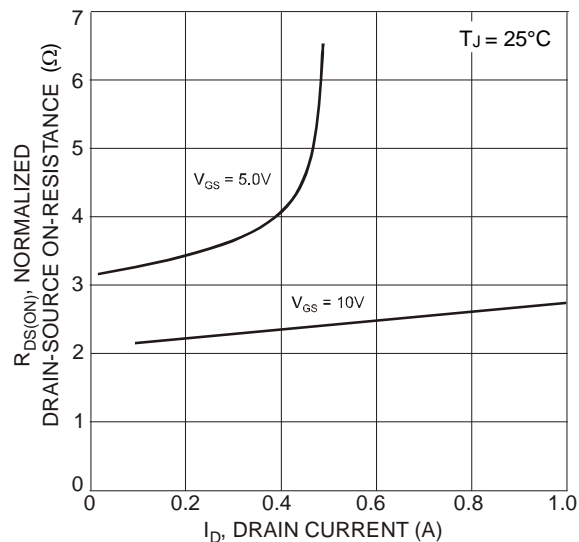
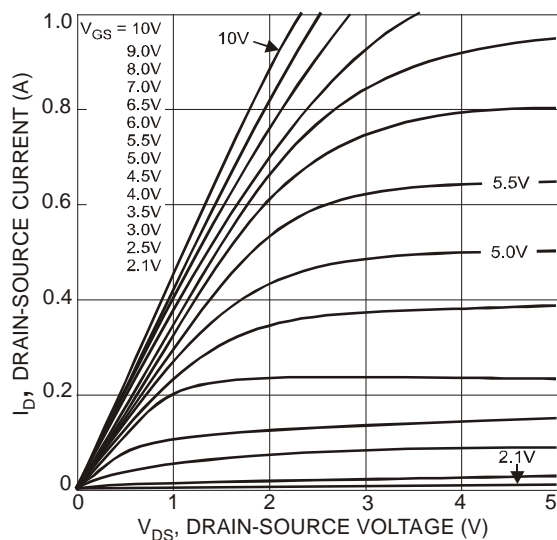
**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	80	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 100μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	0.5	μA	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V
Gate-Body Leakage	I <sub>GSS</sub>	—	—	±10	nA	V <sub>GS</sub> = ±15V, V <sub>DS</sub> = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.0	2.0	3.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	3.5	5.0	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.2A
On-State Drain Current	I <sub>D(ON)</sub>	0.5	1.0	—	A	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 7.5V
Forward Transconductance	g <sub>FS</sub>	80	—	—	ms	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.2A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C <sub>iss</sub>	—	22	50	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	11	25	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	2.0	5.0	pF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t <sub>D(ON)</sub>	—	2.0	20	ns	V <sub>ES</sub> = 10V, R <sub>L</sub> = 150Ω,
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	5.0	20	ns	V <sub>DS</sub> = 10V, R <sub>D</sub> = 100Ω

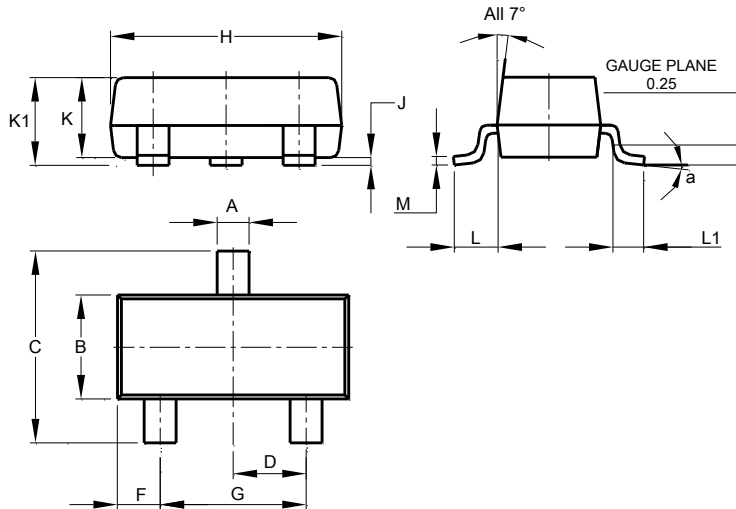
- Notes:
- Device mounted on FR-4 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
  - Short duration pulse test used to minimize self-heating effect.



## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SOT23

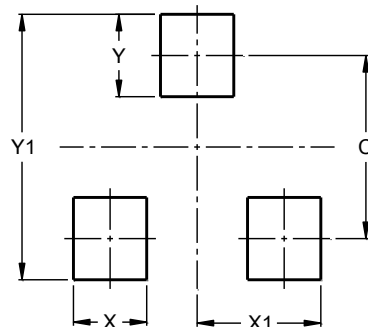


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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