## **Vishay Semiconductors**



#### **Thermal Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit	
Thermal resistance junction to ambient air		$R_{thJA}$	300 <sup>1)</sup>	K/W	
Junction temperature		T <sub>j</sub>	125	°C	
Ambient operating temperature range		T <sub>amb</sub>	- 55 to + 125	°C	
Storage temperature range		T <sub>stg</sub>	- 55 to + 150	°C	

<sup>1)</sup> Valid provided that electrodes are kept at ambient temperature

### **Electrical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Reverse breakdown voltage	$I_R = 100 \mu A \text{ (pulsed)}$	$V_{(BR)}$	100			V
Leakage current <sup>2)</sup>	V <sub>R</sub> = 1.5 V	I <sub>R</sub>			0.5	μΑ
	V <sub>R</sub> = 1.5 V, T <sub>j</sub> = 60 °C	I <sub>R</sub>			5	μΑ
	V <sub>R</sub> = 10 V	I <sub>R</sub>			0.8	μΑ
	V <sub>R</sub> = 10 V, T <sub>j</sub> = 60 °C	I <sub>R</sub>			7.5	μΑ
	V <sub>R</sub> = 50 V	I <sub>R</sub>			2	μΑ
	$V_R = 50 \text{ V}, T_j = 60 ^{\circ}\text{C}$	I <sub>R</sub>			15	μΑ
	V <sub>R</sub> = 75 V	I <sub>R</sub>			5	μΑ
	V <sub>R</sub> = 75 V, T <sub>j</sub> = 60 °C	I <sub>R</sub>			20	μΑ
Forward voltage <sup>2)</sup>	I <sub>F</sub> = 0.1 mA	$V_{F}$			250	mV
	I <sub>F</sub> = 10 mA	V <sub>F</sub>			450	mV
	I <sub>F</sub> = 250 mA	$V_{F}$			1000	mV
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>D</sub>		10		pF
	$V_R = 1 V, f = 1 MHz$	C <sub>D</sub>		6		pF

 $<sup>^{2)}</sup>$  Pulse test  $t_{\text{p}}$  < 300  $\mu\text{s},~\delta$  < 2 %

#### **Typical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

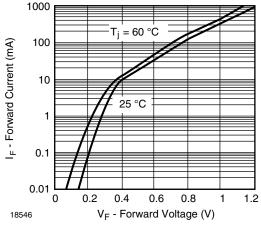


Figure 1. Typical Instantaneous Forward Characteristics

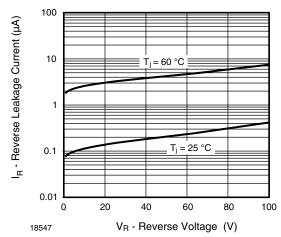


Figure 2. Typical Reverse Characteristics



## **Vishay Semiconductors**

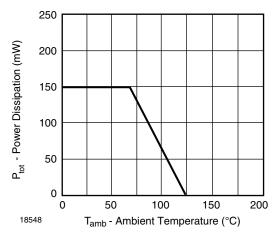
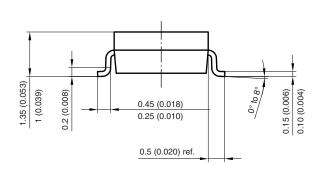
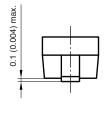


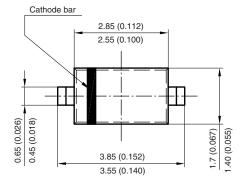
Figure 3. Admissible Power Dissipation vs. Ambient Temperature

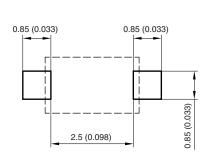
### Package Dimensions in millimeters (inches): SOD-123





Mounting Pad Layout





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## **Legal Disclaimer Notice**



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